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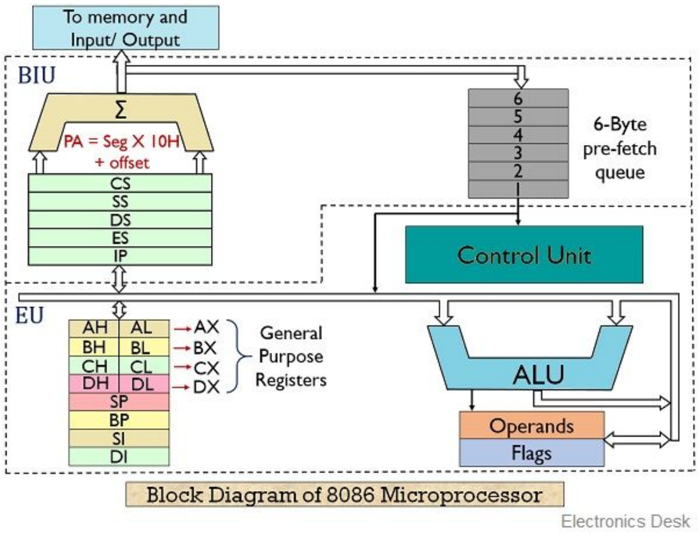
EE2003

Intel 8086



Execution Unit

- Flag register used to store some result about an operation. Example result of a comparision or overflow bit etc. Used by the ALU
- E(xtended)SP: Stack pointer
- EA/B/C/D: General registers. Do whatever you want
- ESI/ EDI: For string instructions. strlen() or memcpy()

Bus Interface Unit

CS, SS, DS store the starting address of code, stack and data segments. The IP(instruction pointer) or the A,B,C,D registers are used to store the offset from the start address. Formula is given by:

$$Address = Segment << 4 + offset$$

BP and SP point to SS, IP points to CS, A/B/C/D point to DS (or ES, prof did not remember)

x86 Ops

```
MOV EAX, EBX        ; EAX <- EBX, register direct
MOV EAX, [ECX]         ; Load from memory, register indirect
MOV [ECX], EAX          ; store to memory
MOV EAX, [EBX - N]         ; register indirect with offset(immediate)
MOV EAX, EBX - ECX          ; THIS IS WRONG


; Load Effective Addr:
; Essentially you can do math in the right operand.
; Used in compilers for things like
; int *p = &point[i].ycoord;
LEA EAX, [EBP+8*EAX+4]

; ALU Ops
ADD EAX, EBX        ; EAX <- EAX + EBX
SUB EAX, DWORD PTR[EBX]
SUB EAX, WORD PTR[EBX]
SUB EAX, BYTE PTR[EBX]

; Logical Ops
AND EAX, DWORD PTR [EBP + 4]
```

1. Write assembly to do the following:
$EAX = x * y + a - b$
$EBX = (x \oplus y)|(a \wedge b)$

```
; first part
MOV EAX, X
MUL y
MOV EBX, a
SUB EBX, b
ADD EAX, EBX

;second part
MOV EBX, x
XOR EBX, y
MOV ECX, a
AND ECX, b
OR EBX, ECX
```

2. Write a program to evaluate $z = x * y$ using repeated addition

```
XOR EAX, EAX
MOV EBX, y
CMP EBX, 0
JZ done
add:
ADD EAX, x
DEC EBX
JNZ add

done:
NOP
```

3. Write a program to calculate the string length of a constant string

```
; assuming the data is little endian
XOR EAX, EAX
loop:
CMP [EBX], 0
JZ done
INC EBX
INC EAX
JMP loop

done:
NOP
```

4. Write a program to swap two integers x and y

```
Swap(int *pX, int *pY){
        __asm{
                // this uses extra
                // memory though
                // ALSO, this is wrong
                // maybe bcoz of virtual
                // addressing?
                PUSH [pX]
                PUSH [pY]
                POP [pX]
                POP [pY]

                MOV EAX, [pX]
                MOV EBX, [pY]

                XOR EAX, EBX
                XOR EBX, EAX
                XOR E
        }
}
```