# Unit Level Testbenches

1. IFD Unit
2. EX Unit

## Unit Level Testbench for IFD unit



Stimulus:

Checks to perform:

Assertions:

Coverage:

## Unit Level Testbench for EXEC unit



Stimulus:

Checks to perform:

Assertions:

Coverage:

## Chip level testbench



Stimulus:

Checks to perform:

Assertions:

Coverage: