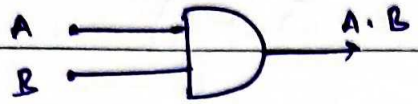
16.1.20 Experiment 1

Aim :

To study and verify all logic gates.

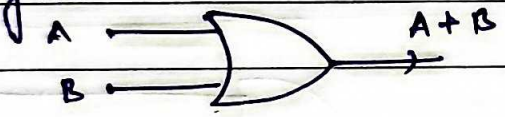
1 AND Gate



Truth Table

A	B	A.B
0	0	0
0	1	0
1	0	0
1	1	1

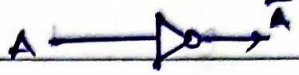
2. OR Gate



Truth Table

A	B	A+B
0	0	0
0	1	1
1	0	1
1	1	1

3. NOT Gate



Truth Table

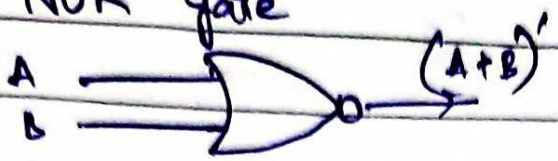
A	$\bar{A}$
0	1
1	0

4. NAND Gate

Truth Table

A	B	(A.B)'
0	0	1
0	1	1
1	0	1
1	1	0

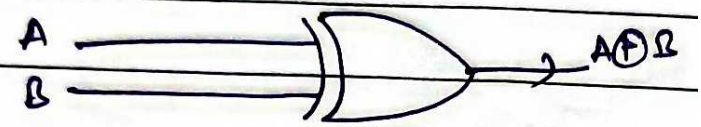
5. NOR Gate



Truth Table

A	B	(A+B)'
0	0	1
0	1	0
1	0	0
1	1	0

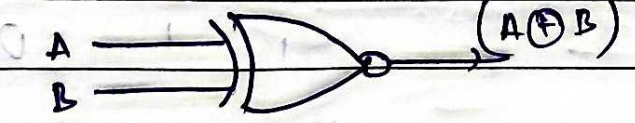
6. EX-OR



Truth Table

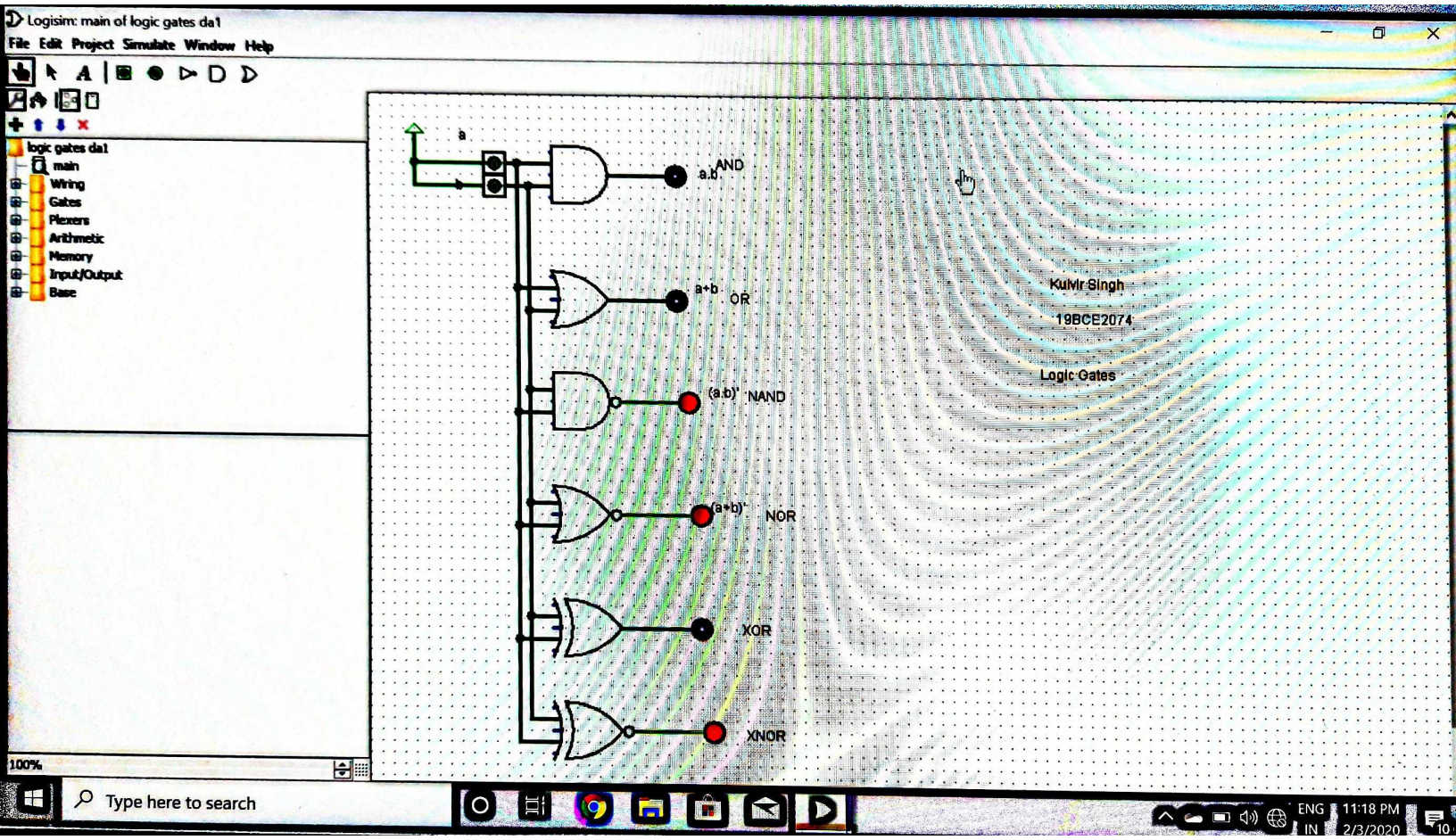
A	B	A⊕B
0	0	0
0	1	1
1	0	1
1	1	0

7. EX-NOR



Truth Table

A	B	(A⊕B)'
0	0	1
0	1	0
1	0	0
1	1	1


Logisim: main of logic gates da1
File Edit Project Simulate Window Help
logic gates da1
main
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
a
a.b
AND
a+b
OR
(a.b)'
NAND
(a+b)'
NOR
XOR
XNOR
Kulvir Singh
19BCE2074
Logic Gates
100%
Type here to search
ENG
IN
11:18 PM
2/3/2020