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Combinational Analysis

File Edit Project Simulate Window Help

Inputs Outputs Table Expression Minimized

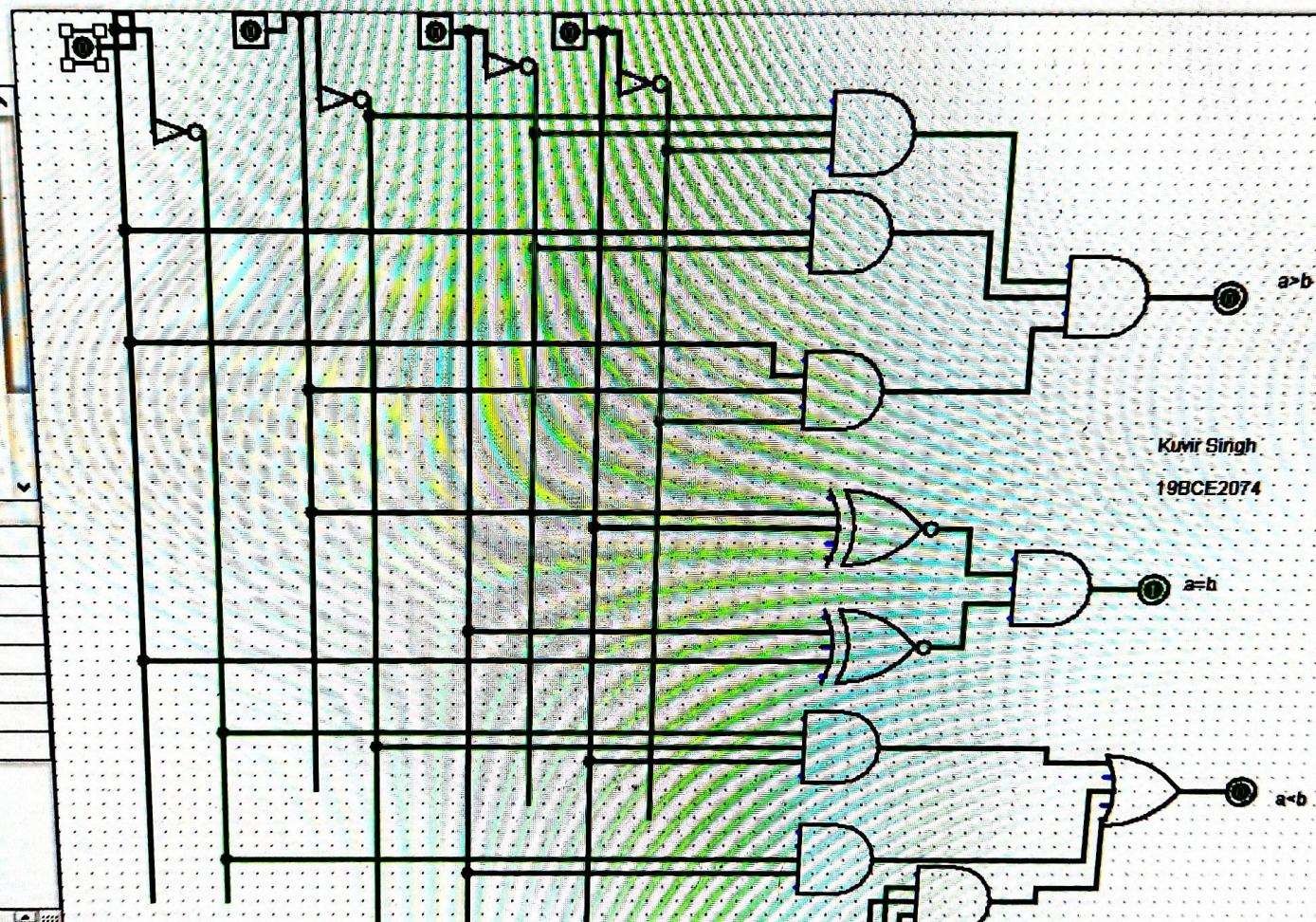
a	b	x	y	z
0	0	0	0	0
0	1	0	1	1
1	0	1	1	0
1	1	0	0	0

Build Circuit

a>b

a=b

a<b



a	b	c	d	x	y	z
0	0	0	0	0	1	0
0	0	0	1	0	0	0
0	0	1	0	0	0	1
0	0	1	1	0	0	0
0	1	0	0	0	0	1
0	1	0	1	0	1	0
0	1	1	0	0	0	1
0	1	1	1	0	0	1
1	0	0	0	0	0	1
1	0	0	1	0	0	0
1	0	1	0	0	1	0
1	0	1	1	0	0	0
1	1	0	0	0	0	1
1	1	0	1	0	0	0
1	1	1	0	0	0	1
1	1	1	1	0	1	0