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"The mind is not a vessel to be filled but a fire to be ignited."

### **Education**

#### Indian Institute of Technology, Roorkee

B.TECH. IN COMPUTER SCIENCE AND ENGINEERING

Roorkee India

2020 - Exp. 2024

• CGPA: 8.921/10

# Work Experience \_\_

**Sprinklr** Gurgaon, India

May. 2023 - July. 2023 PRODUCT ENGINEER INTERN

- Worked on language model interpretability at neuron and layer level and explaining model predictions for bias detection.
- · Built a streamlit tool with interpretability algorithms including neuron context labeling, interpreting weights for MLP, attention matrices and feature attribution.

**Trexquant LLP** (Remote) Connecticut, USA

GLOBAL ALPHA RESEARCHER

Oct. 2022 - Nov. 2022

· Worked as an alpha researcher, reading and implementing research papers on technical and macroeconomic alphas based macroeconomic data variables.

**ETS Montreal** (Remote) Montreal, Canada

RESEARCH INTERN

May. 2022 - Oct. 2022

- · Worked on neural network calibration for reliable predictions in a semantic segmentation task by developing a novel label smoothing method for segmentation of medical images mentored by **Prof. Jose Dolz**.
- · Augmented ground truth label information with various distance maps for more informed label smoothing.

## Writing.

### Visualization and Model Explanations in Convolutional Neural Networks

Blog

Nov. 2021

• **Blog** on Deep Learning model explanations and activation maps with intuitive explanations and PyTorch implementation.

# **Projects**

### **Domain Adaptation in Machine Reading Comprehension**

CONTRIBUTOR | RESEARCH PROJECT FOR INTER IIT TECH MEET 11.0

- · Surveyed and Implemented various domain adaptation algorithms for Retriever Reader Question Answering.
- Ran Experiments on reader model adaptation for BERT and DeBERTa models using CAQA and QADA and evaluated domain adaptation performance using synthetic data generation with SQuAD2.0 as the source domain.
- Report: Report, Implementation: Code

#### **Model Extraction of Action Recognition Models**

CONTRIBUTOR | RESEARCH PROJECT FOR INTER IIT TECH MEET 10.0

- $\bullet \ \ \text{Performed Model Extraction of Video Swin Transformer and MoviNet trained on the Kinetics datasets in Black Box and Grey Box settings to obtain the Control of Video Swin Transformer and MoviNet trained on the Kinetics datasets in Black Box and Grey Box settings to obtain the Control of Video Swin Transformer and MoviNet trained on the Kinetics datasets in Black Box and Grey Box settings to obtain the Control of Video Swin Transformer and MoviNet trained on the Kinetics datasets in Black Box and Grey Box settings to obtain the Control of Video Swin Transformer and MoviNet trained on the Kinetics datasets in Black Box and Grey Box settings to obtain the Control of Video Swin Transformer and MoviNet trained on the Control of Video Swin Transformer and MoviNet trained on the Control of Video Swin Transformer and MoviNet Trained On the Control of Video Swin Transformer and MoviNet Trained On the Control of Video Swin Transformer and MoviNet Trained On the Control of Video Swin Transformer and MoviNet Trained On the Control of Video Swin Trained On Trained On Trained On Trained On Trained On Trained O$ competitive results in the task.
- · Used conditional video generator and adversarial crafting along with knowledge distillation-based techniques.
- Report: Report, Implementation: Code

#### SIC/XE Assembler

COURSE PROJECT CSN-252 SYSTEM SOFTWARE | IIT ROORKEE

- Implemented an assembler for the SIC/XE architecture as mentioned in the book Software Systems: An Introduction to Systems Programming by Leland L. Beck in Java
- Implemented multiple features like Assembler Directives, Error Messages, Program Blocks, Literal Handling, Expression Handling etc.

#### **CPU design and implementation on Logisim Simulator**

COURSE PROJECT CSN-221 COMPUTER ARCHITECTURE | IIT ROORKEE

- Implemented a 32-bit Simple RISC architecture-based CPU as described in the book Basic Computer Architecture by Smruti R. Sarangi.
- Implemented features like Program Counter, Register File, Control Unit, Main Memory with Cache Management with a Direct Mapped Cache.

#### **Other Deep Learning Projects**

SELF LEARNING PROJECTS | VISION AND LANGUAGE GROUP

- Contributed paper summaries from top tier conferences to papers\_we\_read, an open source repository maintained by Vision and Language Group.
- Implemented CVPR 2020 paper Zero-DCE for lowlight image enhancement on the LowLight Images dataset using PyTorch framework. Github.
- Implemented CycleGAN for image-to-image translation from horse images to zebra images on horses2zebras dataset. Github
- Implemented the **U-Net Architecture** for semantic segmentation using PyTorch framework on the HELEN\* Dataset for performing pixel-level classification of human faces. **Github**

# **Achievements**

- Bronze Medalist, DevRev Improving Domain Specific QA Inter IIT Tech Meet11.0
- 2022 **Gold Medalist**, Bosch Model Extraction High Prep Inter IIT Tech Meet10.0
- 2020 All India Rank 339, Joint Entrance Exam (JEE) Advanced
- 2020 All India Rank 1433, Joint Entrance Exam (JEE) Mains

# Skills\_

**Programming Languages** Python, JAVA, HTML, CSS, JavaScript

Packages Pytorch, TensorFlow, Keras, Numpy, sckit-learn, opency

**Utilities** Git, nano, Linux Shell

# **Extracurricular Activity**

### Vision and Language Group, IIT Roorkee

Roorkee, India

CORE MEMBER

May, 2021 - Present

- Core member of VLG, a student group that promotes deep learning research culture at IITR by discussing relevant research papers, organizing workshops and working on related projects. [Link]
- Involved in paper discussions, contributing to projects, organizing workshops, mentoring open projects etc.