

Fusa Pass - Validated List - SAF85xx_RFE_SW_EAR_0.8.18_D230823
rfe_fuSaFault_R1_sm3_bb_tx1_e
rfe_fuSaFault_R1_sm4_bb_tx2_e
rfe_fuSaFault_R1_sm5_bb_tx3_e
rfe_fuSaFault_R1_sm6_bb_tx4_e
rfe_fuSaFault_R1_sm7_paout_rtm_tx1_e
rfe_fuSaFault_R1_sm8_paout_rtm_tx2_e
rfe_fuSaFault_R1_sm9_paout_rtm_tx3_e
rfe_fuSaFault_R1_sm10_paout_rtm_tx4_e
rfe_fuSaFault_R1_sm11_bist_tx1_tx2_phaseDiff_sw_e
rfe_fuSaFault_R1_sm11_bist_tx1_phaseStep_sw_e
rfe_fuSaFault_R1_sm12_bist_tx2_tx3_phaseDiff_sw_e
rfe_fuSaFault_R1_sm12_bist_tx2_phaseStep_sw_e
rfe_fuSaFault_R1_sm13_bist_tx3_tx4_phaseDiff_sw_e
rfe_fuSaFault_R1_sm13_bist_tx3_phaseStep_sw_e
rfe_fuSaFault_R1_sm13_bist_tx4_phaseStep_sw_e
rfe_fuSaFault_R1_sm15_bb_rx1_e
rfe_fuSaFault_R1_sm16_bb_rx2_e
rfe_fuSaFault_R1_sm17_bb_rx3_e
rfe_fuSaFault_R1_sm18_bb_rx4_e
rfe_fuSaFault_R1_sm23_bist_rx1_rx2_gainDiff_sw_e
rfe_fuSaFault_R1_sm23_bist_rx1_rx3_gainDiff_sw_e
rfe_fuSaFault_R1_sm23_bist_rx1_rx4_gainDiff_sw_e
rfe_fuSaFault_R1_sm23_bist_rx1_rx2_phaseDiff_sw_e
rfe_fuSaFault_R1_sm23_bist_rx1_rx3_phaseDiff_sw_e
rfe_fuSaFault_R1_sm23_bist_rx1_rx4_phaseDiff_sw_e
rfe_fuSaFault_R1_sm28_ov_gld1v3_gldo_e
rfe_fuSaFault_R1_sm29_ov_gld1v4_gldo_e
rfe_fuSaFault_R1_sm29_uv_gld1v4_gldo_e
rfe_fuSaFault_R1_sm31_ov_hld1v8_gldo_e
rfe_fuSaFault_R1_sm31_uv_hld1v8_gldo_e
rfe_fuSaFault_R1_sm46_xo_pllclk_det_mcgen_e
rfe_fuSaFault_R1_sm50_adpll_dco_level_max_mcgen_e
rfe_fuSaFault_R1_sm50_adpll_dco_level_min_mcgen_e
rfe_fuSaFault_R1_sm54_level_low_chirp_e
rfe_fuSaFault_R1_sm54_level_high_chirp_e
rfe_fuSaFault_R1_sm55_unlock_chirp_e
rfe_fuSaFault_R1_sm58_cg_chirp_e
rfe_fuSaFault_R1_sm59_li_chirp_e
rfe_fuSaFault_R1_sm60_sd_chirp_e
rfe_fuSaFault_R1_sm62_lockstep_te_e
rfe_fuSaFault_R2_sm63_te_swt_radar_pipeline_e
rfe_fuSaFault_R2_sm68_sm69_reg_crc_e
rfe_fuSaFault_R2_sm70_mosi_miso_crc_e
rfe_fuSaFault_R2_sm83_rfe_test_mode_active_e
rfe_fuSaFault_R2_sm87_m7_rfe_lockup_e
rfe_fuSaFault_R2_sm88_sm89_m7_tcm_sram_e
rfe_fuSaFault_R2_sm88_sm89_m7_tcm_sram_e
rfe_fuSaFault_R2_sm92_sw_wdt_e
rfe_fuSaFault_R2_sm93_xbic_integrity_check_e
rfe_fuSaFault_R1_sm94_m7_message_e2e_crc_sw_e
rfe_fuSaFault_R2_sm98_m7_spi_access_ana_e
rfe_fuSaFault_R2_sm99_spi_write_check_fail_sw_e
rfe_fuSaFault_R2_sm204_dm_rfe_m7_core_sw_redundancy_e
rfe_fuSaFault_R2_sm435_generic_sw_e