release 0.8.19, SAF85xx_RFE_SW_EAR_0.8.19_D230929, Confidential and shared under NDA



Document Name	RFE SW Test Report
Document Status	Release
Project Name/ID	SAF85xx RFE SW
Release	EAR 0.8.19
Date	September 29, 2023
Author	NXP RFE SW Team
Notes	Check Release Notes on the exact content of the release. - All FuSa Monitors except SM57 have been tested and found to be functional. - TxEnable and TxTransmissionEnable issue fixed Tested on WG4-1.1 and CAB boards, with ES2 E5 T1 samples (with OTP v2)

	PARTIALLY TESTED:
	- "Values Tested" columns contain the tested subset from all possible values.
	- "Additional Info" columns contains more information on limits of the testing.
	- Values or conditions mentioned outside the text scope, values and limitations, shall not be used with this release.
	TEST REPORT UPDATES:
Help	- "Change in Current Release" columns indicates test\feature change with respect to the previous release test report.
	DO NOT USE
	- do not use this feature in this release
	- do not use this reacure in this release
	Test Coverage
	- "any" value for values with large range (e.g. int32_t) means tested using random values

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		release 0.8.19, SAF85xx_F	RFE_SW_EAR_0.8.19_D23	30929, Confidential and shared under NDA	
RFE SW Testing	Legend	? x !	PASS DO NOT USE FAIL Partially Tested	Notation ul: 32bit decimal number 0x: hexadecimal b: binary number	SAF85XX
Component	Functionality	Environment	Result	Additional info	Change in current release
	rfeAppBasicExample	Windows PC, Simulator	✓	RFE Control Application	
RFE Control Application	rfeAppBasicExample	APP-A53/APP-M7 Bare Metal, C (arm-gcc and GHS Compiler)	✓	RFE Control Application	
	RFE Driver: stand alone	APP-A53/APP-M7 Bare Metal, C (arm-gcc and GHS Compiler)	✓	internal tests, limited API functionality (see RFE Abstract API Tab). Added suppoort on APP-M7	
	RFE Driver: rfeAppBasicExample	APP-A53/APP-M7 Bare Metal, C (arm-gcc and GHS Compiler)	✓	rfeAppBasicExample functionality	
RFE Driver	RFE Driver: RSDK	APP-A53 Zephyr OS, C (arm-gcc and GHS Compiler)	✓	see SAF85xx RSDK release, limited API functionality (see RFE Abstract API Tab)	
	RFE Driver: Radar Xplorer \ Radar FW	APP-A53 Bare Metal, C++	✓	see Radar Xplorer \ Radar FW release, limited API functionality (see RFE Abstract API Tab)	
	RFE CDD: rfeAppAutosarBasicExample	APP-M7 Bare Metal, C++ (arm-gcc and GHS Compiler)	✓	rfeAppAutosarBasicExample functionality on APP-M7	
	Ī		√	internal Unit Tests, Internal Integration Tests, limited functionality (see RFE Firmware and RFE	
	RFE FW stand alone	RFE-M7		Configuration Parameters Tabs) rfeAppBasicExample functionality	
RFE FW	RFE Driver: stand alone	RFE-M7	✓		
=	RFE Driver: RSDK	RFE-M7	✓	see SAF85xx RSDK release, limited functionality (see RFE Firmware and RFE Configuration Parameters Tabs)	
	RFE Driver: Radar Xplorer \ Radar FW	RFE-M7	✓	see Radar Xplorer \ Radar FW release, limited functionality (see RFE Firmware and RFE Configuration Parameters Tabs)	
	RFE Configuration Tool: Stand Alone	Windows PC	✓	Dedicated RFE configuration tests, Basic Cofndgiration, Ceiling Demo configuration	
DEE 0	RFE Configuration Tool: Radar Xplorer	Windows PC	✓	integrated in Radar Xplorer	
RFE Configuration Tool	Timing Tool: Stand Alone	Windows PC	√	integrated in Radar Xplorer	
	Timing Tool: Radar Explorer	Windows PC	✓	integrated in Radar Xplorer	

release 0.8.19, SAF85xx_RFE_SW_EAR_0.8.19_D230929, Confidential and shared under NDA							
					Notation		
RFE Abstract API			✓ 2	PASS	ul: 32bit decimal number		
Testing		Legend	· · · · · · · · · · · · · · · · · · ·	FAIL	0x: hexadecimal b: binary number SAF85XX		
resung			î	Partially Tested	b bindly itember		
Function Tested		Parameter	Values Tested	Result	Additional info	Change in current	
						release	
rfe_sync()	[out]	RFE_ERROR	rfe error t	✓	error propagation tested		
ne_sync()	[return]	void	-	N\A			
	[in]	pConfig	basic configuration	✓ ✓ ✓	fixed configuration		
	[iii]	pcomig	smoke test \ ceiling test configuration (TBC) other configurations	<u> </u>	fixed configuration		
rfe_configure()	E-1	domento Toble Address	0	· ·			
_ 0 "	[in]	dynamicTableAddress	rfe sysMemAddress t	✓			
	[out]	RFE_ERROR	rfe_error_t	✓			
	[return]	void	-	N\A			
	[in]	radarCycleCount	I _{am} ,	,			
			any false	· ·			
	[in]	isScheduled	true	?			
rfe_radarCycleStart()	[in]	startTime	0	✓			
			other values	?			
	[out]	RFE_ERROR	rfe_error_t	√			
	[return]	void	-	N\A			
	[out]	RFE_ERROR		· ·			
rfe_radarCycleStop()	[out] [return]	uint32 t		→			
	[return]	umtoz_t		· ·			
	[out]	RFE ERROR	rfe error t	✓			
rfe_getTime()	[return]	uint32_t	any	✓			
rfe_getRadarCycleCount()	[out]	RFE_ERROR	rfe_error_t	√			
	[return]	rfe_radarCycleCount_t	1-1000+	✓			
	[qut]	RFE_ERROR	rfo arror t	√			
rfe_getState()	[out] [return]	rfe_state_t	rfe_error_t all	<u> </u>			
	[return]	ne_state_t	all				
rfe_getFuSaFaults()	[out]	pFuSaR1R2FaultList	any	✓			
rfe_getInternalError()	[return]	uint32_t	any	✓			
	[out]	pR1FaultPromotedToR2		√			
rfe_getFuSaFaultStatistics()	[out]	pRadarCycleCount pFuSaR1FaultCountList					
	[return]	void	-	· ·			
	, ,,						
	[out]	pTxReferenceData		✓			
rfe_getBistZeroHourReferenceData()	[out]	pRxReferenceData		✓			
	[return]	void	-	N\A			
	f 63	DEE EDDOD		4			
rfe_getVersion()	[out] [return]	RFE_ERROR rfe_version_t	rfe_error_t				
	[return]	ile_version_t	all	· ·			
	[in]	monitorSelect		<u> </u>	tested temperature and Tx Power, not tested: Rx saturations		
6 10 10	[out]	pMonitorValues		· .	tested temperature and Tx Power, not tested: Rx saturations		
rfe_monitorRead()	[out]	RFE_ERROR		✓			
	[return]	rfe_radarCycleCount_t					
of a cathlant Darlow Corlo Chart Time ()	[out]	pRadarCycleIndex		✓			
rfe_getNextRadarCycleStartTime()	[out] [return]	RFE_ERROR uint32 t		√			
	[return]	um32_t					
	[in]	startTime		✓			
rfe_setNextRadarCycleStartTime()	[out]	RFE ERROR		· /			
= , , , , , , , , , , , , , , , , , , ,	[return]	uint16_t		✓			
	[in]	profileIndex		✓			
rfe_continuousWaveTransmissionStart()	[out]	RFE_ERROR		✓			
	[return]	uint32_t					

	[out]	RFE ERROR		✓		
rfe_continuousWaveTransmissionStop()	[return]	void	_	✓		
	[
rfe_testSetParam()	[in]	testParam	outputDataTestPattern_e rfe_testParam_keepTxTransmissionEnabled_e chirpPlITestPinEnable_e maskError_e unmaskError_e rfe_testParam_enableClockRetuning_e enableLargerDynamicTable assertErrorNSignal deAssertErrorNSignal assertHeartBeatSignal copybackConfig	4	outputDataTestPattern_e (sineWave, incremental, disabled) rfe_testSetParam(rfe_testParam_maskError_e, rfe_maskError_all_error,RFE, ERROR_FUNCTION_ARGUMENT);	
	[in]	value		✓		
	[out]	RFE_ERROR		✓	error propagation tested	
	[return]	void	-	4		
rfe_updateBegin()	[out]	RFE_ERROR		✓		
ine_updateBegin()	[return]	void	-	✓		
	[in]	dynamicTableIndex		✓		
	[in]	paramsSelect		✓		
rfe_updateDynamicTable()	[out]	RFE ERROR		√	partial Dynamic Table update suported	
	[return] uint32 t			✓		
	, ,					
	[in]	section		✓		
	[in]	param		√		
rfe_updateParam()	[in]	value		1		
inc_apatici aram()	[out]	RFE ERROR		1		
	[return]	void	_	1		
	[return]	Volu		, i		
	[out]	RFE ERROR		- /		
rfe_updatePush()	[return]					
	[return]	uint16_t		-		
	[in]	coreld	rfe_coreId_t	✓	rfe configureInterrupt() configures which conditions will raise an IRQ to coreld, these conditions are:	
	[in]	events	rfe_eventsIRQ_t	✓	□ rie_configureInterrupt() configures which conditions will raise an IRQ to coreid, these conditions are: □ - RFE state changes	
rfe_configureInterrupt()	[in]	rfeAPIs	rfe_apilRQ_t	✓	- RPE state changes - Radar Cycle count changes	
ine_comparementupe()	[in]	apiWaitForInterrupt	rfe apiWaitForInterrupt f	✓	- Chirp Sequence count changes	
	[out]	RFE_ERROR	The second of th	√	- Specific RFE API responses from the firmware	
	[return]	void		✓	Specific to 2.10 Toopeneds from the minimum	
	, ,					
rfo_tortCotInternalError()	[out]	RFE_ERROR		✓		
rfe_testGetInternalError()	[return]	uint32_t		✓		
rfe_clockPllLock	[out]	RFE_ERROR		✓		✓
	[return]	rfe clk error t		√		✓

		release	: 0.0.13, 3AI 03XX_III L_	_SW_EAR_0.8.19_D230929, Confidential and shared under NDA Notation	l
		✓	PASS	Indiation Notation NXP	
RFE FW	Legend	?	Not Available	0x: hexadecimal	
Testing	Logona	X	FAIL	b: binary number SAF85XX	
Functionality	Туре	Environment	Partially Tested Result	Additional info	Change in cur
					Telease
	CLK PLL Lock \ No Lock			0xC0DE0001\0xC0DE <internalcode></internalcode>	
		Boards: WG-4 1.1, CAB		where internalCode = 0x10A1 => wrong 0.9[V] supply 0x10A2 => lack oftunstable XO	
		Boards. WG-4 1.1, CAB		0x10A3 => wrong Clock PLL supply derrived from 0.9[V]	
RFE Initialization				0x10A4 => CLK PLL not locked	
	Digital Clocks available (640MHz outpu		√,	0xC0DE0001	
	FuSa errors masked Trimming	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	✓	All FuSa faults except SM57 Umasked OTP based	✓
	Trimming	Boards, WG-4 1.1, CAB	•	OTF based	
RFE Synchronization		Boards: WG-4 1.1, CAB	✓		
	basic demo	Boards: WG-4 1.1, CAB	-		
RFE Configuration	smoke test \ ceiling demo	Boards: WG-4 1.1, CAB	· ·		
	arbitrary	Boards: WG-4 1.1, CAB	✓		
	oingle shirp cogue	Boards: WG-4 1.1, CAB	√		
Dadas C. I	single chirp sequence multiple chirp sequences	Boards: WG-4 1.1, CAB	<u> </u>		
Radar Cycle	best effort execution	Boards: WG-4 1.1, CAB	1		
	timed execution		?		
	single profile	Boards: WG-4 1.1, CAB	✓		
Chirp Sequence	multiple profile	Boards: WG-4 1.1, CAB	· ·		
	Chirp CW Mode	Boards: WG-4 1.1, CAB	✓		
Chirpo	profile 0.7\ basis test\ asiling dama	Boards: WG-4 1.1, CAB	✓		
Chirps	profile 0-7 \ basic test \ ceiling demo	boards: WG-4 1.1, CAB	•		
	Packet Processor	Boards: WG-4 1.1, CAB	✓	refer to RSDK CSI2 driver (contains PPE and CSI-Rx)	
	SRAM	Boards: WG-4 1.1, CAB	✓	data patters ok, ADC data ceiling test reflections OK	
Data Out	CSI2-Tx Fork (PPE + CSI2)	Boards: WG-4 1.1, CAB	?		
Data Gat	Virtual Channels	Boards: WG-4 1.1, CAB	· /		
	Meta data packets	Boards: WG-4 1.1, CAB	√,	CSI2 feature, not used for Packet Processor	
	SW Fields Meta Data	Boards: WG-4 1.1, CAB	✓	CSI2 feature, not used for Packet Processor	
Parameter Updates	RFE parameters	Boards: WG-4 1.1, CAB	✓		
rarameter opuates	D : T !!	Boards: WG-4 1.1, CAB		partial update possible	
	Dynamic Table	Doards. WO-4 1.1, OAD	✓	partial update possible	
			√	partiar upuate possible	
Dynamic Tables	single chirp sequence	Boards: WG-4 1.1, CAB	→	partial upuate possible	
Dynamic Tables			✓	partial upuate possible	
,	single chirp sequence multiple sequences	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	✓	paruar upuare possible	
Dynamic Tables RFE States	single chirp sequence multiple sequences	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	✓	partial update possible	
,	single chirp sequence multiple sequences	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	✓	paruar upuare possible	
,	single chirp sequence multiple sequences Large and Small Dynamic Tables	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	∀ ∀ ∀ ∀	paruar upuare possible	
,	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU	Boards: WG-4 1.1, CAB	* * * * * * * * * * * * * * * * * * *	Partially tested	
,	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL	Boards: WG-4 1.1, CAB			
,	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL	Boards: WG-4 1.1, CAB	/ / / / / / /		
RFE States	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI	Boards: WG-4 1.1, CAB			
,	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL	Boards: WG-4 1.1, CAB	* * * * * * * * * * * * * * * * * * *		
RFE States	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI Tx Tx Bist Rx	Boards: WG-4 1.1, CAB	4 4		
RFE States	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI Tx Tx Bist Rx Rx Rx Bist	Boards: WG-4 1.1, CAB	* * * * * * * * * * * * * * * * * * *		
RFE States	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI TX TX Bist RX RX Bist ADC	Boards: WG-4 1.1, CAB			
RFE States	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI Tx Tx Bist Rx Rx Rx Bist	Boards: WG-4 1.1, CAB	* * * * * * * * * * * * * * * * * * *		
RFE States	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI Tx Tx Bist Rx Rx Bist ADC PDC: Decimation	Boards: WG-4 1.1, CAB	\frac{\lambda}{\lambda}		
RFE States	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI Tx Tx Bist Rx Rx Bist ADC PDC: Decimation PDC: DC Notch Filter Packer	Boards: WG-4 1.1, CAB	* * * * * * * * * * * * * * * * * * *		
RFE States	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI Tx Tx Bist Rx Rx Bist ADC PDC: Decimation PDC: DC Notch Filter Packer RFE initialization timing	Boards: WG-4 1.1, CAB	* * * * * * * * * * * * * * * * * * *		
RFE States Front End	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI Tx Tx Bist Rx Rx Bist ADC PDC: Decimation PDC: DC Notch Filter Packer	Boards: WG-4 1.1, CAB			
RFE States	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI TX TX Bist RX RX Bist ADC PDC: Decimation PDC: DC Notch Filter Packer RFE initialization timing API timing (T1-T6) Radar Cycle timing Chirp sequence timing	Boards: WG-4 1.1, CAB		Partially tested	
RFE States Front End	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI Tx Tx Bist Rx Rx Bist ADC PDC: Decimation PDC: DC Notch Filter Packer RFE initialization timing API timing (T1-T6) Radar Cycle timing Chirp sequence timing Chirp sequence timing Chirp sequence timing Chirp sequence timing	Boards: WG-4 1.1, CAB		Partially tested Use RFE Configuration tool for calculation of radar cycle time schedule	
RFE States Front End	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI Tx Tx Bist Rx Rx Bist ADC PDC: Decimation PDC: DC Notch Filter Packer RFE initialization timing API timing (T1-T6) Radar Cycle timing Chirp sequence timing Chirp timing Calibration Timing Calibration Timing	Boards: WG-4 1.1, CAB		Partially tested	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
RFE States Front End	single chirp sequence multiple sequences Large and Small Dynamic Tables Cortex-M7 Timing Engine FCCU CLK PLL Chirp PLL LOI Tx Tx Bist Rx Rx Bist ADC PDC: Decimation PDC: DC Notch Filter Packer RFE initialization timing API timing (T1-T6) Radar Cycle timing Chirp sequence timing Chirp sequence timing Chirp sequence timing Chirp sequence timing	Boards: WG-4 1.1, CAB		Partially tested Use RFE Configuration tool for calculation of radar cycle time schedule	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \

F 15.1.	HW errors: mask \ unmask	Boards: WG-4 1.1, CAB	V	see Test Modes	
Functional Safety	SW errors	Boards: WG-4 1.1, CAB	<u> </u>	limited error testing	
	API function returns RFE ERROR N	Boards: WG-4 1.1, CAB	<u> </u>	limited return values tested, see RFE Abstract API tab	
	R1 error recovery	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	· · · · · · · · · · · · · · · · · · ·	Only tested via fault injection Only tested via fault injection	
	R1 ellor recovery	Boards, WG-4 1:1, CAB	<u> </u>	Only tested via rault injection	,
	temperature	Boards: WG-4 1.1, CAB	✓		
RFE Monitoring	ADC Clippping		?		
	Rx saturation		?		
	1				
Barras Maria arrant	Power On to Power Down	Boards: WG-4 1.1, CAB	<u> </u>	at start-up, at the beginning of each chirp sequence, PDCVADC (1.45V) and ChirpPLL (3.3V, 1.45V) are always on	
Power Management	Power Down to Power On	Boards: WG-4 1.1, CAB	√ 2	at end of each chirp sequence, PDC\ADC (1.45V) and ChirpPLL (3.3V, 1.45V) are always on	
	Power Sleep	<u> </u>		not supported	
CLK PLL ReCalibration	CLK PLL	Boards: WG-4 1.1, CAB	✓		
	ATB ADC	Boards: WG-4 1.1, CAB	4	RFE FW uses ATB ADC trimming data stored in OTP. Please check trimming data status in the sample information. In case samples do not contain trimming data for ATB ADC, the performance may deviate from trimmed samples. ATB-ADC SE (Single Ended) Trimming	
	ChirpPLL	Boards: WG-4 1.1, CAB	√		
	GLDO	Boards: WG-4 1.1, CAB	√		
	GBIAS LDOs	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	· · · · · · · · · · · · · · · · · · ·		
	LO	Boards: WG-4 1.1, CAB	<u>√</u>		
Trimming	MCGEN	Boards: WG-4 1.1, CAB	· ·		
	ADC	Boards: WG-4 1.1, CAB	<i>-</i> ✓		
	RX	Boards: WG-4 1.1, CAB	✓		
	RX BIST	Boards: WG-4 1.1, CAB	√		
	Temp	Boards: WG-4 1.1, CAB	✓		
	TX ADC	Boards: WG-4 1.1, CAB	✓	RFE FW uses TX ADC trimming data stored in OTP. Please check trimming data status in the sample information. In case samples do not contain trimming data for ATB ADC, the performance may deviate from trimmed samples.	
	TX BIST	Boards: WG-4 1.1, CAB	✓		
0: ::: 0:::::::::::::::::::::::::::::::	lev en e	D 1 W0 111 01D			1
Start Up Calibrations	RX-ADC	Boards: WG-4 1.1, CAB	✓		
	LOI Tx Buffer Calibration	Boards: WG-4 1.1, CAB	√		
	LOI Rx Buffer Calibration	Boards: WG-4 1.1, CAB	· ·		
	TX LOX2 Calibration	Boards: WG-4 1.1, CAB	✓		
	TX Buff2a Calibration	Boards: WG-4 1.1, CAB	✓		
Configuration Calibrations:	TX Buff2b2c Calibration	Boards: WG-4 1.1, CAB	√		
profile independent	TX PR Calibration	Boards: WG-4 1.1, CAB	✓		
promo maoponaom	RX LOX2 Calibration	Boards: WG-4 1.1, CAB	✓		
	RX LOX2 DAC Calibration	B 1 W0 111 01B	?		
	RX BIST LOX2 Calibration	Boards: WG-4 1.1, CAB	<u> </u>		
	RX BIST SSB Calibration TX PA RTM (FuSa)	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	?		
	IX FA KTW (Fu3a)	Boards: WG-4 1:1, CAB	•		
Chirp Sequence Dependent	ChirpPLL Loopfilter BW Calibration	Boards: WG-4 1.1, CAB	✓	Chirp PLL LF BW Calibration for 1GHz VCO	
	RX HPF Calibration	Boards: WG-4 1.1, CAB	√	v2	
Configuration Calibrations:	RX LPF Calibration	Boards: WG-4 1.1, CAB	<u> </u>	v2 v2	
profile dependent	RX Gain Calibration	Boards: WG-4 1.1, CAB	✓	V2	
(per profile)	ChirpPLL VCO Calibration	Boards: WG-4 1.1, CAB	√		
	TX Pout Calibration	Boards: WG-4 1.1, CAB	✓	accuracy improved	
	1. O. T. D. W. O. III	December MC 111 CAT			
	LOI Tx Buffer Calibration	Boards: WG-4 1.1, CAB	<u> </u>		
	LOI Rx Buffer Calibration TX LOX2 Calibration	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	<u> </u>		
	TX Buff2a Calibration	Boards: WG-4 1.1, CAB			
Re-Calibrations:	TX Buff2b2c Calibration	Boards: WG-4 1.1, CAB	· ·		
profile independent	TX PR Calibration	Boards: WG-4 1.1, CAB	√		
,	RX LOX2 Calibration	Boards: WG-4 1.1, CAB	✓		
	RX BIST LOX2 Calibration	Boards: WG-4 1.1, CAB	√		
	RX BIST SSB Calibration	Boards: WG-4 1.1, CAB	✓		
	TX PA RTM (FuSa)	Boards: WG-4 1.1, CAB	?		
De Celibration	lou: PLL VOO (December MC 111 CAT			
Re-Calibrations: profile independent	ChirpPLL VCO (per profile) TX Pout Calibration (per profile)	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	→		
prome independent	I A FOUL Calibration (per profile)	Dualus. WG-4 1.1, CAB	<u> </u>		
	ChirpLL LLDO	Boards: WG-4 1.1, CAB	?		
	MCGEN LLDO	350.00.1134111,070	?		
RF BIST	TX12\23\34 phase difference	Boards: WG-4 1.1, CAB	Ţ.	Partially tested	1
IVL DI91	TX1\2\3\4 phase step	Boards: WG-4 1.1, CAB	!		!
	RX12\13\14 phase difference	Boards: WG-4 1.1, CAB	į į		1
	RX12\13\14 gain difference	Boards: WG-4 1.1, CAB	!		!
	D-t	Doordo: WC 4.4.4 CAS	√		
	Datapatern sine	Boards: WG-4 1.1, CAB			

	Datapatern incremental	Boards: WG-4 1.1, CAB	✓		
	Datapatern PRB		?		
	TX Chirp Sequence Transmission Toggle	Boards: WG-4 1.1, CAB	✓	PA Toggle	
	CW Continuous (Tx Only)	Boards: WG-4 1.1, CAB	✓		
Test Modes	CW Chirp Interleaved (Tx, Rx, SRAM)	Boards: WG-4 1.1, CAB	✓		
rest wodes	Chirp PLL	Boards: WG-4 1.1, CAB	✓		
	Radar Cycle io	Boards: WG-4 1.1, CAB	✓	e.g. RFE_IO_5	
	Chirp Sequence io	Boards: WG-4 1.1, CAB		e.g. RFE_IO_6	
	mask errors (test mode)	Boards: WG-4 1.1, CAB	✓	temporary feature for development, will be removed in the product	
	unmask errors (test mode)	Boards: WG-4 1.1, CAB		temporary feature for development, will be removed in the product	
	CLK PLL enable\disable	Boards: WG-4 1.1, CAB	✓	temporary feature for development, maybe be removed in the product	
Supplies	1.3V di\dt	Boards: WG-4 1.1, CAB	Boards: WG-4 1.1, CA	AB	

		release 0.8.19, SAF85xx RFE	SW EAR 0.8.19 D2309	29, Confidential and shared un	der NDA	
				Notation		
RFE Config		✓	Fully Tested	ul: 32bit decimal number	NXO	
Testing	Legend	×	FAIL	0x: hexadecimal b: binary number	SAF85XX	
(all settings)		î	Partially Tested	b. binary namber		
Component	Functionality	Value tested	Result	Test Environment	Additional info	Change in current
oponom			Hoodit	Tool Environment		release
	chirpSequenceConfigCount	18	√	Boards: WG-4 1.1, CAB		
	chirpProfileCount	18	√	Boards: WG-4 1.1, CAB		
metaData		any	✓	Boards: WG-4 1.1, CAB		
		any	<u> </u>	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	patchVersion	any	•	Boards: WG-4 1.1, CAB		
	powerMode	1ul \ rfe_powerMode_active_e	!	Boards: WG-4 1.1, CAB	sleep power mode not tested\not supported	
	radarCycleStartSignal	all values	✓	Boards: WG-4 1.1, CAB		
	chirpActiveOut chirpSequenceActiveSignal	all values all values	<u> </u>	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		✓
		0ul \ 0x0 \ b000	•	Boards: WG-4 1.1, CAB		
	clkloConfig	in="disabled"		Boards: WG-4 1.1, CAB	reduce-driver-level don't care => clk out disabled	
	CikioCornig	out="disabled"		Boards, WG-4 1.1, CAB	Teduce-driver-lever don't care => cik out disabled	
		reduce-driver-level="false"				
	pdcDecimationFilter	Oul \ rfe_pdcDecimationFilter_narrow_e 1ul \ rfe_pdcDecimationFilter_seeepNarrow_e		Boards: WG-4 1.1, CAB		
	pdcBitwidth	2ul \rfe pdcBitwidth 16bit e	✓	Boards: WG-4 1.1, CAB		
		1ul \ 0x1 \ b01				
		packet-processor="true"		Boards: WG-4 1.1, CAB	SRAM data ok: datapattern, ADC data \ reflections	
		csi2="false" 2ul \ 0x2 \ b10				
	dataOutDest	packet-processor="false"		Boards: WG-4 1.1, CAB		
		csi2="true"				
general		3ul \ 0x3 \ b11			data fark not teeted	
		packet-processor="true" csi2="true"			data fork not tested	
		b00001				
		interleaving="true"				
		header="false"		Boards: WG-4 1.1, CAB	Interleaving ADC1, ADC2, ADC3, ADC4, ADC1, ADC2, ADC3, ADC4,	
		footer="false" metadata packet="disabled"				
		hardware-field="disabled"				
	dataOutComig	p00000				
		interleaving="false"				
		header="false" footer="false"		Boards: WG-4 1.1, CAB	No Interleaving ADC1, ADC1, ADC1, ADC2, ADC2, ADC2, ADC2, ADC2,	
		metadata packet="disabled"				
		hardware-field="disabled"				
	metadataPacketSwContent		√		CSI2 feature, not used for Packet Processor	
	metadataPacketSwContent metadataPacketVirtualChannel	hardware-field="disabled"	✓ ✓	Boards: WG-4 1 1 CAR	CSI2 feature, not used for Packet Processor CSI2 feature, not used for Packet Processor	
	metadataPacketSwContent metadataPacketVirtualChannel		✓	Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSatCountResetEveryChirpSequence	hardware-field="disabled" 2\4\8	✓	Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1	hardware-field="disabled" 2 \ 4 \ 8 true Oul	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSattCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2	hardware-field="disabled" 2\4\8 true Oul Oul	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx2	hardware-field="disabled" 2 \ 4 \ 8 true Oul	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSattCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks mxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSattCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx7 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx1	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSattCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx1 rxSaturationCountLimitiStage1 rx1 rxSaturationCountLimitiStage1 rx2	hardware-field="disabled" 2 \ 4 \ 8 Itrue Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks mxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx3	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx3 rxSaturationCountLimitStage1 rx3 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage2 rx1	hardware-field="disabled" 2 \ 4 \ 8 Itrue Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSattCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationTountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage2 rx2	hardware-field="disabled" 2 \ 4 \ 8 Itue Oul	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks mxSattCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx3 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx4	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx3 rxSaturationCountLimitStage2 rx3 rxSaturationCountLimitStage2 rx3 rxSaturationCountLimitStage2 rx3 rxSaturationCountLimitStage2 rx3 rxSaturationCountLimitStage2 rx3 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx12	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSattCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx3 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx4 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx12 temperatureSensorThresholdHigh tx12	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx3 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx12 temperatureSensorThresholdHigh tx04 temperatureSensorThresholdHigh tx04	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSattCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx12 temperatureSensorThresholdHigh rx	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks xSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx3 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx3 temperatureSensorThresholdHigh rx temperatureSensorThresholdOver tx12 temperatureSensorThresholdOver tx12 temperatureSensorThresholdOver tx12 temperatureSensorThresholdOver tx12 temperatureSensorThresholdOver tx12	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSattCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx12 temperatureSensorThresholdHigh rx temperatureSensorThresholdHigh rx temperatureSensorThresholdOver tx34 temperatureSensorThresholdOver tx34 temperatureSensorThresholdOver tx34 temperatureSensorThresholdOver tx34 temperatureSensorThresholdOver tx34	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx3 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx3 temperatureSensorThresholdHigh rx temperatureSensorThresholdOver tx3 temperatureSensorThresholdOver xx temperatureSensorThresholdOver xx	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage1 rx7 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx12 temperatureSensorThresholdHigh rx temperatureSensorThresholdHigh rx temperatureSensorThresholdOver rx12 temperatureSensorThresholdOver rx2 temperatureSensorThresholdOver rx2 temperatureSensorThresholdOver rx12	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks xSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx4 rxSa	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB		
	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage1 rx4 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx12 temperatureSensorThresholdHigh x0 temperatureSensorThresholdOver tx3 temperatureSensorThresholdOver rx temperatureSensorThresholdOver rx temperatureSensorThresholdUnder rx temperatureSensorThresholdUnder tx12 temperatureSensorThresholdUnder bx3 temperatureSensorThresholdUnder bx3 temperatureSensorThresholdUnder xx	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB		
monitorAndSafety	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks xSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx3 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx3 rxSaturationCountLimitStage2 rx3 rxSaturationCountLimitStage2 rx3 rxSaturationCountLimitStage2 rx3 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx12 temperatureSensorThresholdHigh rx temperatureSensorThresholdOver tx12 temperatureSensorThresholdUnder rx temperatureSensorThresholdUnder tx12 temperatureSensorThresholdUnder rx	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB		
monitorAndSafety	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks rxSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx3 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx12 temperatureSensorThresholdHigh tx24 temperatureSensorThresholdHigh rx temperatureSensorThresholdOver rx temperatureSensorThresholdOver rx temperatureSensorThresholdOver rx temperatureSensorThresholdOver rx temperatureSensorThresholdOver rx temperatureSensorThresholdOver rx temperatureSensorThresholdUnder rx temperatureSensorThresholdUnder rx temperatureSensorThresholdUnder rx thresholdValueToPromoteR1Faults zeroHourReferenceForTxPasseDiff tx12	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB		
monitorAndSafety	metadataPacketSwContent metadataPacketVirtualChannel jumpbackTimeTicks xSatCountResetEveryChirpSequence rxSaturationThresholdStage1 rx1 rxSaturationThresholdStage1 rx2 rxSaturationThresholdStage1 rx3 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage1 rx4 rxSaturationThresholdStage2 rx1 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx2 rxSaturationThresholdStage2 rx4 rxSaturationThresholdStage2 rx4 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx1 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage1 rx2 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx1 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx2 rxSaturationCountLimitStage2 rx4 pdcClippingCountResetEveryChirpSequence temperatureSensorThresholdHigh tx12 temperatureSensorThresholdHigh rx temperatureSensorThresholdHigh rx temperatureSensorThresholdOver rx12 temperatureSensorThresholdOver rx temperatureSensorThresholdOver rx temperatureSensorThresholdOver rx temperatureSensorThresholdUnder tx34 temperatureSensorThresholdUnder rx temperatureSensorThresholdUnder r	hardware-field="disabled" 2 \ 4 \ 8 true Oul Oul Oul Oul Oul Oul Oul O	✓	Boards: WG-4 1.1, CAB		
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chipIntervalTimeTicks any / Boards: WG-4 1.1, CAB dwellTimeTicks any / Boards: WG-4 1.1, CAB settleTimeTicks any / Boards: WG-4 1.1, CAB acquisitionTimeTicks any / Boards: WG-4 1.1, CAB resetTimeTicks any / Boards: WG-4 1.1, CAB centerFrequency any / Boards: WG-4 1.1, CAB	chirpSequenceConfig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence Ingth dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable bEnable rxEnable pdcNotchFilterEnable pdcNotchFilterCoefficient [N]	1ul 1-4096ul any	Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
dwellTimeTicks any / Boards; WG-4 1.1, CAB settleTimeTicks any / Boards; WG-4 1.1, CAB acquisitionTimeTicks any / Boards; WG-4 1.1, CAB resetTimeTicks any / Boards; WG-4 1.1, CAB centerFrequency any / Boards; WG-4 1.1, CAB	chirpSequenceConfig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence Ingth dynamic Jublendex chirpProfileSequence Ingth dynamic Jublendex chirpFrequencyDrift fastResetEnable bEnable rxEnable pdcNotchFilterEnable pdcNotchFilterCoefficient	1ul 1-4096ul any	* * * * * * * * * * * * * * * * * * *	Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
settleTimeTicks any Boards: WG-4 1.1, CAB acquisitionTimeTicks any Boards: WG-4 1.1, CAB resetTimeTicks any Boards: WG-4 1.1, CAB centerFrequency any Boards: WG-4 1.1, CAB	chirpSequenceConfig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence Ingth dynamic Jublendex chirpProfileSequence Ingth dynamic Jublendex chirpFrequencyDrift fastResetEnable bEnable rxEnable pdcNotchFilterEnable pdcNotchFilterCoefficient	1ul 1-4096ul any	Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y	Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
acquisitionTimeTicks any	chirpSequenceConf ig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence Inpth dynamicUpdatesEnabled dynamicUpdatesEnabled dynamicUpdatesEnabled tynamicTableIndex chirpFrequencyDrift fastResetEnable bEnable bEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks	1ul 1-4096ul any	✓	Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
resetTimeTicks any Soards: WG-4 1.1, CAB CenterFrequency any Boards: WG-4 1.1, CAB CenterFrequency Boards: WG-4 1.1, CAB	chirpSequenceConf ig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfileSequence 9 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfil	1ul 1-4096ul any	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
centerFrequency any Boards: WG-4 1.1, CAB	chirpSequenceConf- ig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence InterprofileSequence 7 chirpProfileSequence InterprofileSequence InterprofileSequen	Itul 1-4096ul any	✓	Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
	chirpSequenceConf ig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence Ingth dynamicUpdatesEnabled dynamicUpdatesEnabled dynamicUpdatesEnable to Ingthe Ingth Ingthe Ingthe Ingthe Ingthe Ingthe Ingthe Ingthe Ingthe Ingthe I	1ul 1-4096ul any	✓	Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
	chirpSequenceConf- ig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence InterprofileSequence 7 chirpProfileSequence InterprofileSequence InterprofileSequ	1ul 1-4096ul any	✓	Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
	chirpSequenceConf ig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 dynamicUpdatesEnabled dynamicUpdatesEnabled dynamicUpdatesEnabled bEnable trEnable bEnable trEnable bEnable trEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks dwellTimeTicks acquisitionTimeTicks resetTimeTicks	1ul 1-4096ul any	✓	Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
	chirpSequenceConf ig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfileSequence 1 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfil	1ul 1-4096ul any	✓	Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
chirpStopoDirection Out \rf e_chirpStopoDirection_falling_e Reards: WC 4.1.1 CAR	chirpSequenceConf ig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 dynamicUpdatesEnabled dynamicUpdatesEnabled dynamicUpdatesEnabled bEnable trEnable bEnable trEnable bEnable trEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks dwellTimeTicks acquisitionTimeTicks resetTimeTicks	1ul 1-4096ul any	✓	Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
	chirpSequenceConf ig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence Ingth dynamicUpdatesEnable dynamicTableIndex chirpFrequencyDrift fastResetEnable benable recently fastResetEnable pdcNotchFilterEnable pdcNotchFilterCoefficient [IN] [IN] effectiveSamplingFrequency chirpIntervalTimeTicks acquisitionTimeTicks acquisitionTimeTicks centerFrequency effectiveChirpBandwidth chirpPlIVcoSelect	1ul 1-4096ul any	✓	Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	
ChilpStopeDirection 1ul \ rfe chirpStopeDirection rising e Boards: WG-4 1.1, CAB	chirpSequenceConf ig[M]	chirpCount chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfileSequence 1 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfil	1ul 1-4096ul any	✓	Boards: WG-4 1.1, CAB	needed for interleaving of chirp modes	

chirpProfile[N]	chirpPIILoopFilterBandwidth	Irfe_chirpPilLoopFiterBandwidth_200kHz_e Irfe_chirpPilLoopFiterBandwidth_250kHz_e Irfe_chirpPilLoopFiterBandwidth_300kHz_e Irfe_chirpPilLoopFiterBandwidth_350kHz_e Irfe_chirpPilLoopFiterBandwidth_40kHz_e Irfe_chirpPilLoopFiterBandwidth_450kHz_e Irfe_chirpPilLoopFiterBandwidth_500kHz_e Irfe_chirpPilLoopFiterBandwidth_500kHz_e Irfe_chirpPilLoopFiterBandwidth_500kHz_e Irfe_chirpPilLoopFiterBandwidth_500kHz_e Irfe_chirpPilLoopFiterBandwidth_500kHz_e Irfe_chirpPilLoopFiterBandwidth_500kHz_e Irfe_chirpPilLoopFiterBandwidth_700kHz_e Irfe_chirpPilLoopFiterBandwidth_700kHz_e Irfe_chirpPilLoopFiterBandwidth_700kHz_e Irfe_chirpPilLoopFiterBandwidth_500kHz_e	,	Boards: WG-4 1.1, CAB	
	txTransissionEnable	all combinations b0000 - b1111	✓	Boards: WG-4 1.1, CAB	
	txPower	-9 to 150	✓	Boards: WG-4 1.1, CAB	
	txPhaseRotation_Tx1	any	✓	Boards: WG-4 1.1, CAB	
	txPhaseRotation_Tx2	any	✓	Boards: WG-4 1.1, CAB	
	txPhaseRotation_Tx3	any	✓	Boards: WG-4 1.1, CAB	
	txPhaseRotation_Tx4	any	✓	Boards: WG-4 1.1, CAB	
	txEnableReferenceTime	any	✓	Boards: WG-4 1.1, CAB	
	txTransmissionTimeOffset	2-4094ul	✓	Boards: WG-4 1.1, CAB	
	fastResetDelayTicks	any	√	Boards: WG-4 1.1, CAB	
	fastResetDurationTicks	any	√	Boards: WG-4 1.1, CAB	
	rxGain	0-7ul \ rfe_rxGain_25\28\31\34\37\40\43\46dB_e	√	Boards: WG-4 1.1, CAB	
	rxHpfCutOffFrequency	0-6ul \ rfe_rxHpfCutOffFrequency_200\300\400\800\1600\3200\6400kHz_e	✓	Boards: WG-4 1.1, CAB	
	rxLpfCutOffFrequency	0ul-5ul \ rfe_rxLpfCutOffFrequency_10\15\20\25\30\40MHz_e	✓	Boards: WG-4 1.1, CAB	
	VirtualChannel	rfe_virtualChannel_0_e rfe_virtualChannel_1_e rfe_virtualChannel_2_e rfe_virtualChannel_3_e		Boards: WG-4 1.1, CAB	

		release 0.8.19, SAF	85xx_RFE_SW_EAR_0	0.8.19_D230929, Confidential a	nd shared under NDA	
				Notation		
RFE Config		✓	PASS	ul: 32bit decimal number	NXP	
Testing	Legend	?	DO NOT USE	0x: hexadecimal	This is example configuration!	
(BasicDemo)		X	FAIL	b: binary number	SAF85XX	
Component	Functionality	Value tested	Result	Test Environment	Additional info	Change in current release
		rfeConfig.xml in rfeAppBasicExample32		•	Not tested for RF performance	
	chirpSequenceConfigCount	1	✓	Boards: WG-4 1.1, CAB		
	chirpProfileCount	1	✓	Boards: WG-4 1.1, CAB		
metaData	majorVersion	0	✓	Boards: WG-4 1.1, CAB		
	minorVersion	8	✓	Boards: WG-4 1.1, CAB		
	patchVersion	0	✓	Boards: WG-4 1.1, CAB		
	· ·					
	powerMode	1ul \ rfe_powerMode_active_e	✓	Boards: WG-4 1.1, CAB		
		Oul \ rfe_io_none_e	✓	Boards: WG-4 1.1, CAB	disabled	
		all values	✓	Boards: WG-4 1.1, CAB		✓
	chirpSequenceActiveSignalio	6ul \ rfe_io_6_e	✓	Boards: WG-4 1.1, CAB		

Boards: WG-4 1.1, CAB

reduce-driver-level don't care => clk out disabled

SRAM data ok: datapattern, ADC data \ reflections

No Interleaving ADC1, ADC1, ADC1, ADC1, ADC2, ADC2, ADC2, ADC2,

Interleaving ADC1, ADC2, ADC3, ADC4, ADC1, ADC2, ADC3, ADC4, ...

0ul \ 0x0 \ b000 in="disabled"

out="disabled" reduce-driver-level="false"

1ul \ 0x1 \ b01

csi2="false"
0u \ 0x0 \ b00000
interleaving="false"
header="false"

footer="false"

true Oul

0ul

0ul

Oul Oul

0ul

1000ul

1000ul

1000ul

1000ul

1000ul

1000ul

1000ul

true

85ul

85ul

125ul 125ul

125ul

30ul

30ul

30ul

default

Oul \ rfe pdcDecimationFilter narrow e

2ul \rfe_pdcBitwidth_16bit_e

packet-processor="true"

metadata packet="disabled" hardware-field="disabled"

4ul \ chirpSequenceTimestamp_e

clkloConfig

pdcBitwidth

dataOutDest

dataOutConfig

metadataPacketSwContent

jumpbackTimeTicks

metadataPacketVirtualChannel

rxSatCountResetEveryChirpSequence

rxSaturationThresholdStage1_rx1 rxSaturationThresholdStage1_rx2

rxSaturationThresholdStage1_rx3 rxSaturationThresholdStage1_rx4

rxSaturationThresholdStage2_rx1

rxSaturationThresholdStage2_rx2 rxSaturationThresholdStage2_rx3

rxSaturationThresholdStage2_rx4 rxSaturationCountLimitStage1_rx1

rxSaturationCountLimitStage1_rx2

rxSaturationCountLimitStage1_rx3 rxSaturationCountLimitStage1_rx4

rxSaturationCountLimitStage2_rx1

rxSaturationCountLimitStage2_rx2

rxSaturationCountLimitStage2 rx3

rxSaturationCountLimitStage2_rx4

pdcClippingCountResetEveryChirpSequence

temperatureSensorThresholdHigh _tx12

temperatureSensorThresholdHigh_tx34 temperatureSensorThresholdHigh_xo temperatureSensorThresholdHigh_rx

temperatureSensorThresholdOver_tx12

temperatureSensorThresholdOver_tx34 temperatureSensorThresholdOver_xo

temperatureSensorThresholdOver_rx temperatureSensorThresholdUnder_tx12 temperatureSensorThresholdUnder_tx34

temperatureSensorThresholdUnder xo

zeroHourReferenceForTxPhaseDiff_tx12

zeroHourReferenceForTxPhaseDiff tx23

zeroHourReferenceForTxPhaseDiff tx34

zeroHourReferenceForRxPhaseDiff_rx12

zeroHourReferenceForRxPhaseDiff rx23

zeroHourReferenceForRxPhaseDiff_rx34

zeroHourReferenceForRxGainDiff_rx12

zeroHourReferenceForRxGainDiff_rx23

zeroHourReferenceForRxGainDiff rx34

thresholdValueToPromoteR1Faults

txPhaseDiffThresholdTolerance

txPhaseStepThresholdTolerance

rxPhaseDiffThresholdTolerance

rxGainDiffThresholdTolerance

monitorAndSafety temperatureSensorThresholdUnder_rx

txPowerLevelForBist

txFrequencyForBist

rxFrequencyForBist

general

pdcDecimationFilter

1			,			
	injectTestToneBeforeLna	default	✓			
	txPpdThreshold tx1	default	✓			
	txPpdThreshold_tx2	default	1			
	-		٧			
	txPpdThreshold_tx3	default	✓			
	txPpdThreshold_tx4	default	1			
	-					
	fuSaFaultMask[0]	default	✓			
		default	√		Please refer to the FuSa pass list in the release notes.	
	6.0-EIMAI-(40)		1		1	
	fuSaFaultMask[16]	default	Y			
	radarCycleDuration	200000ul	✓	Boards: WG-4 1.1, CAB	[50[ms]	
	chirpSequenceCount	1ul	✓	Boards: WG-4 1.1, CAB		
	chirpSequence_0	Oul	✓	Boards: WG-4 1.1, CAB		
				Boards. WG-4 1.1, O/tB		
	chirpSequence_1	Oul	- (
	chirpSequence_2	Oul	?			
	chirpSequence_3	0ul	?			
	chirpSequence 4	Oul	?			
	chirpSequence 5	Oul	?			
		Oul	?			
	chirpSequence_6		· ·			
	chirpSequence_7	Oul	7			
	chirpSequenceStartTimeOffset_0	any	✓	Boards: WG-4 1.1, CAB		
	chirpSequenceStartTimeOffset_1	Oul	?			
	chirpSequenceStartTimeOffset_2	Oul	?			
	chirpSequenceStartTimeOffset 3	Oul				
radarCycle			?			
.,	chirpSequenceStartTimeOffset_4	Oul	?			
	chirpSequenceStartTimeOffset_5	Oul	?			
	chirpSequenceStartTimeOffset_6	Oul .	?			
	chirpSequenceStartTimeOffset_7	Oul	?			
	recalibrateProfileDependent 0	Oul	· /	Boards: WG-4 1.1, CAB	disabled	
				Dodius, WG-4 1.1, CAB		
	recalibrateProfileDependent_1	Oul	√		disabled	
	recalibrateProfileDependent_2	Oul	✓		disabled	
	recalibrateProfileDependent_3	Oul	✓		disabled	
	recalibrateProfileDependent 4	Oul	✓		disabled	
	recalibrateProfileDependent 5	Oul	/		disabled	
			,			
	recalibrateProfileDependent_6	Oul	· ·		disabled	
	recalibrateProfileDependent_7	Oul Oul	✓		disabled	
	recalibrateProfileIndependent	Oul	✓		disabled	
	bistInterval	Oul \ rfe bistInterval everyRadarCycle e	?		BIST not included in RFE FW	
					1	
	n n	201	/	Boards: WG-4 1.1, CAB		
	[M]	1ul	· ·			
	chirpCount	128ul	✓	Boards: WG-4 1.1, CAB		
	chirpProfileSequence 0	Oul	✓	Boards: WG-4 1.1, CAB		
	chirpProfileSequence 1	Oul	?			
	chirpProfileSequence_2	Oul	?			
	chirpProfileSequence_3	Oul	?			
	chirpProfileSequence_4	Oul	?			
	chirpProfileSequence_5	Oul Oul	?			
ahirmCanuanaaCan	chirpProfileSequence_6 chirpProfileSequence_7	Oul	?			
chirpsequencecon	chirpProfileSequence 7	Oul	?			
fig[M]	chirpProfileSequenceLength	Oul	√	Boards: WG-4 1.1, CAB		
				Doards. WG-4 1.1, CAD		
	dynamicUpdatesEnabled	false				
	dynamicTableIndex	Oul	?			
	chirpFrequencyDrift	1ul	?		40Hz, can be set, effect not tested	
	fastResetEnable	false	✓	Boards: WG-4 1.1, CAB	gear shift	
	txEnable	1ul \ 0x1 \ b0001	✓	Boards: WG-4 1.1, CAB	Tx1 = ON, Tx2\3\4 = OFF	
	rxEnable	15ul \ 0xF \ b1111	✓	Boards: WG-4 1.1, CAB	Rx1\2\3\4 = ON	
	pdcNotchFilterEnable	false	· ·	Boards: WG-4 1.1, CAB		
			v			
	pdcNotchFilterCoefficient	Oul	- √ - ·	Boards: WG-4 1.1, CAB		
	[N]	1	✓	Boards: WG-4 1.1, CAB	Profile 0 tested only	
	effectiveSamplingFrequency	2ul \ rfe_effectiveSamplingFrequency_10MHz_e	✓	Boards: WG-4 1.1, CAB	10[MHz]	
	chirpIntervalTimeTicks	3200ul	✓	Boards: WG-4 1.1, CAB	80[us]	
	dwellTimeTicks	400ul	✓	Boards: WG-4 1.1, CAB	10[us]	
	settleTimeTicks	400ul	· ·	Boards: WG-4 1.1, CAB	10[us]	
			· ·			
	acquisitionTimeTicks	2048ul	V	Boards: WG-4 1.1, CAB	51.2[us]	
	resetTimeTicks	80ul	√	Boards: WG-4 1.1, CAB	2[us]	
	centerFrequency	7650000ul	✓	Boards: WG-4 1.1, CAB	76,5[GHz]	
	effectiveChirpBandwidth	500000ul	✓	Boards: WG-4 1.1, CAB	500[MHz]	
	chirpPlIVcoSelect	Oul \ rfe_chirpPIIVco_1GHz_e	/		1[GHz]	
	chirpSlopeDirection	1ul \ rfe_chirpSlopeDirection_rising_e	· ·	Boards: WG-4 1.1, CAB	rising	
		4.41) -fhim Dill Eithen Den				
	chirpPllLoopFilterBandwidth	1ul \ rfe_chirpPllLoopFilterBandwidth_250kHz_e	✓	Boards: WG-4 1.1, CAB	250[KHz]	
chirpProfile[N]	txTransissionEnable	1ul \ 0x1 \ b0001	✓	Boards: WG-4 1.1, CAB	Tx1 PA = enable, Tx2\3\4 PA = disable	
Chirperonie[N]	txPower	120ul	✓	Boards: WG-4 1.1, CAB	not optimized	
	txPhaseRotation Tx1	Oul	✓	Boards: WG-4 1.1, CAB	0 degrees	
	txPhaseRotation Tx2	Oul	✓	Boards: WG-4 1.1, CAB	0 degrees	
	txPhaseRotation Tx3	Oul	· ·	Boards: WG-4 1.1, CAB		
					0 degrees	
	txPhaseRotation_Tx4	Oul	✓	Boards: WG-4 1.1, CAB	0 degrees	
	txEnableReferenceTime	1ul \ rfe_txTransmissionReferenceTime_startOfSettleTime_e	✓	Boards: WG-4 1.1, CAB	Transmit Start Time Reference = End of Settle Time	
	txTransmissionTimeOffset	2ul	✓	Boards: WG-4 1.1, CAB	50[ns]	
	fastResetDelayTicks	Oul	✓	Boards: WG-4 1.1, CAB	when gear shift is disabled, this configuration value is not taken into account by RFE FW.	
	fastResetDurationTicks	Oul	· ·	Boards: WG-4 1.1, CAB	when gear shift is disabled, this configuration value is not taken into account by RFE FW.	
					miner godi orinicio dioabieu, tilio coringuration value io not taken into account by RFE FVV.	
		7ul\rfe rxGain 46dB e	✓	Boards: WG-4 1.1, CAB		
	rxGain					
	rxHpfCutOffFrequency	2ul \ rfe rxHpfCutOffFrequency 400kHz e	✓	Boards: WG-4 1.1, CAB		
	rxHpfCutOffFrequency rxLpfCutOffFrequency	2ul \ rfe_rxHpfCutOffFrequency_400kHz_e 0ul \ rfe_rxLpfCutOffFrequency_10MHz_e	✓ ✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	rxHpfCutOffFrequency rxLpfCutOffFrequency	2ul \ rfe_rxHpfCutOffFrequency_400kHz_e 0ul \ rfe_rxLpfCutOffFrequency_10MHz_e	√ √ <u>√</u>	Boards: WG-4 1.1, CAB		
	rxHpfCutOffFrequency	2ul \ rfe rxHpfCutOffFrequency 400kHz e	√			

		release 0.8.19, SAF	85xx_RFE_SW_EAR_0	0.8.19_D230929, Confidential ar	nd shared under NDA	
RFE Config		√	PASS	Notation ul: 32bit decimal number	NXP	
Testing	Legend	?	DO NOT USE	0x: hexadecimal	This is example configuration !	
(CeilingDemo)		X	FAIL	b: binary number	SAF85XX	
0	Functionality	Value tested	D It	T4 F	Additional info	Change in current
Component	Functionality		Result	Test Environment		release
	chirpSequenceConfigCount	rfeConfig_ceilingDemo.xml in rfeAppBasicExample32	1	Boards: WG-4 1.1, CAB	Tested for RF performance	
	chirpProfileCount	1	∨	Boards: WG-4 1.1, CAB		
metaData	majorVersion	0	✓	Boards: WG-4 1.1, CAB		
	minorVersion patchVersion	0	✓ ✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	patcriversion	Į0	v	Boards, WG-4 1.1, CAB		
		1ul \ rfe_powerMode_active_e	✓	Boards: WG-4 1.1, CAB		
	radarCycleStartSignalio	Oul \ rfe_io_none_e	✓ ✓	Boards: WG-4 1.1, CAB	disabled	
	chirpActiveOutlo chirpSequenceActiveSignalio	all values 6ul \ rfe io 6 e	∨	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		V
	o imposquentes terrosignane	Oul \ 0x0 \ b000		Bourds, 170 T 111, 07B		
	clkloConfig	in="disabled"	✓	Boards: WG-4 1.1, CAB	reduce-driver-level don't care => clk out disabled	
	3	out="disabled" reduce-driver-level="false"				
	pdcDecimationFilter	1ul \ rfe pdcDecimationFilter steepNarrow e	✓	Boards: WG-4 1.1, CAB	Steep Narrow	
	pdcBitwidth	2ul \rfe_pdcBitwidth_16bit_e	✓	Boards: WG-4 1.1, CAB	16bit	
general		1ul \ 0x1 \ b01	,			
5	dataOutDest	packet-processor="true" csi2="false"	✓	Boards: WG-4 1.1, CAB	SRAM data ok: datapattern, ADC data \ reflections	
		1u \ 0x1 \ b00001				
		interleaving="true"				
	dataOutConfig	header="false"	✓	Boards: WG-4 1.1, CAB	Interleaving ADC1, ADC2, ADC3, ADC4,	
		footer="false"			J, ,	
		metadata packet="disabled" hardware-field="disabled"				
	metadataPacketSwContent	4ul \ chirpSequenceTimestamp_e	?	Boards: WG-4 1.1, CAB		
		Oul	?	Boards: WG-4 1.1, CAB		
	jumpbackTimeTicks	8ul	✓	Boards: WG-4 1.1, CAB		
	rxSatCountResetEveryChirpSequence	true	✓	Boards: WG-4 1.1, CAB		
	rxSaturationThresholdStage1_rx1	Oul	✓	Boards: WG-4 1.1, CAB		
		Oul	√	Boards: WG-4 1.1, CAB		
		Oul Oul	✓ ✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
		Oul	· ✓	Boards: WG-4 1.1, CAB		
	rxSaturationThresholdStage2_rx2	Oul	✓	Boards: WG-4 1.1, CAB		
		Oul Oul	√	Boards: WG-4 1.1, CAB		
		1000ul	∨ ✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
		1000ul	✓	Boards: WG-4 1.1, CAB		
		1000ul	√	Boards: WG-4 1.1, CAB		
		1000ul 1000ul	✓ ✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	rxSaturationCountLimitStage2_rx1	1000ul	·	Boards: WG-4 1.1, CAB		
	rxSaturationCountLimitStage2_rx3	1000ul	✓	Boards: WG-4 1.1, CAB		
		1000ul	√	Boards: WG-4 1.1, CAB		
		true 85ul	✓ ✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	temperatureSensorThresholdHigh tx34	85ul	· ·	Boards: WG-4 1.1, CAB		
	temperatureSensorThresholdHigh _xo	85ul	✓	Boards: WG-4 1.1, CAB		
		85ul	√	Boards: WG-4 1.1, CAB		
		125ul 125ul	✓ ✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	temperatureSensorThresholdOver_xo	125ul	✓	Boards: WG-4 1.1, CAB		
	temperatureSensorThresholdOver_rx	125ul	✓	Boards: WG-4 1.1, CAB		
	temperatureSensorThresholdUnder_tx12 temperatureSensorThresholdUnder_tx34	30ul 30ul		Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	temperatureSensorThresholdUnder_tx34 temperatureSensorThresholdUnder_xo	30ul	√	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	temperatureSensorThresholdUnder rx	30ul	✓	Boards: WG-4 1.1, CAB		
monitorAndSafety	thresholdValueToPromoteR1Faults	default	✓	Boards: WG-4 1.1, CAB		
	zeroHourReferenceForTxPhaseDiff_tx12	default	✓	Boards: WG-4 1.1, CAB		
		default	✓	Boards: WG-4 1.1, CAB		
		default	✓	Boards: WG-4 1.1, CAB		
	txPhaseDiffThresholdTolerance	default	√	Boards: WG-4 1.1, CAB		
	txPhaseStepThresholdTolerance	default	1	Boards: WG-4 1.1, CAB		
	txPowerLevelForBist txFrequencyForBist	default default	✓	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
	rxFrequencyForBist	default	√	Boards: WG-4 1.1, CAB		
	zeroHourReferenceForRxPhaseDiff rx12	default	√	Boards: WG-4 1.1, CAB		
	zeroHourReferenceForRxPhaseDiff_rx23	default	·	Boards: WG-4 1.1, CAB		
		default	· ✓	Boards: WG-4 1.1, CAB		
		default	✓	Boards: WG-4 1.1, CAB		
	zeroHourReferenceForRxGainDiff_rx12	default	✓	Boards: WG-4 1.1, CAB		
	zeroHourReferenceForRxGainDiff_rx23	default	✓	Boards: WG-4 1.1, CAB		
		default	✓	Boards: WG-4 1.1, CAB		
	rxGainDiffThresholdTolerance	default	✓	Boards: WG-4 1.1, CAB		

	injectTestToneBeforeLna	default	✓	Boards: WG-4 1.1, CAB		
	txPpdThreshold tx1	default	✓	Boards: WG-4 1.1, CAB		
	txPpdThreshold tx2	default	✓	Boards: WG-4 1.1, CAB		
	txPpdThreshold tx3	default		Boards: WG-4 1.1, CAB		
	txPpdThreshold_tx4	default	· ·	Boards: WG-4 1.1, CAB		
	-		· ·			
	fuSaFaultMask[0]	default		Boards: WG-4 1.1, CAB	-	
		default	✓	Boards: WG-4 1.1, CAB	Please refer to the FuSa pass list in the release notes.	
	fuSaFaultMask[11]	default	✓	Boards: WG-4 1.1, CAB		
	errorRecovery mode	non auto	✓	Boards: WG-4 1.1, CAB		
	1	Innanan I		I D 1 140 111 01D	lear 1	
		2000000ul 1ul	√	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	50[ms]	
	chirpSequenceCount chirpSequence_0	Oul	√	Boards: WG-4 1.1, CAB		
		Oul	2	Boards. WO-4 1.1, CAB		
		Oul	?			
		Oul	?			
	chirpSequence_4	Oul	?			
		Oul	?			
	chirpSequence_6	Oul	?			
		Oul	?			
		any	· ·	Boards: WG-4 1.1, CAB		
		Oul Oul	?			
		Oul	?			
	chirpSequenceStartTimeOffset 4	Oul	?			
radarCycle	chirpSequenceStartTimeOffset_5	Oul	?			
,	chirpSequenceStartTimeOffset_6	Oul	?			
	chirpSequenceStartTimeOffset_7	Oul	?			
		Oul	√	Boards: WG-4 1.1, CAB	disabled	
		Oul	√		disabled	
	recalibrateProfileDependent_2	Oul Oul	√		disabled	
	recalibrateProfileDependent_3 recalibrateProfileDependent 4	Oul	V		disabled disabled	
		Oul	· /		disabled	
		Oul	· ·		disabled	
		Oul	✓		disabled	
	recalibrateProfileIndependent	Oul	✓		disabled	
	·	0ul \ rfe_bistInterval_everyRadarCycle_e				
	bistInterval	1ul \ rfe_bistInterval_onceIn2RadarCycles_e	✓		rfe_bistinterval_none_e added	
	Distiliterval	2ul \ rfe_bistInterval_onceIn4RadarCycles_e	·		ITE_DISTILLERVAL_HORIE_E added	
		3ul\rfe bistInterval none e				
	Irian	Itul	-/-	Poordo: WC 4.1.1 CAP		
	[M]	Tul Sau	✓	Boards: WG-4 1.1, CAB		
	chirpCount	58ul	✓ ✓ ✓	Boards: WG-4 1.1, CAB		
	chirpProfileSequence_0		✓ ✓ ✓ ?			
	chirpProfileSequence_0 chirpProfileSequence_1 chirpProfileSequence_2	58ul Oul Oul Oul	?	Boards: WG-4 1.1, CAB		
	chirpProfileSequence 0 chirpProfileSequence_1 chirpProfileSequence_2 chirpProfileSequence_3	58ul Oul Oul Oul Oul	? ? ?	Boards: WG-4 1.1, CAB		
	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4	58ul Oul Oul Oul Oul Oul Oul Oul Oul	?	Boards: WG-4 1.1, CAB		
	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5	58ul Oul Oul Oul Oul Oul Oul Oul Oul Oul O	? ? ?	Boards: WG-4 1.1, CAB		
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5	58ul Oul Oul Oul Oul Oul Oul Oul Oul Oul O	? ? ? ? ?	Boards: WG-4 1.1, CAB		
	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7	S8ul Oul Oul	? ? ?	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7	58ul Oul Oul Oul Oul Oul Oul Oul Oul Oul O	? ? ? ? ?	Boards: WG-4 1.1, CAB		
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7	S8ul Oul Oul	, , , , , , , , , , , , , , , , , , ,	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB		
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence InterprofileSequence Interprofile	S8ul Out Out	, , , , , , , , , , , , , , , , , , ,	Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB Boards: WG-4 1.1, CAB	disable freq drift	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 1 chirpProfileSequence 1 chirpProfileSequenceLength dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable	S8ul	, , , , , , , , , , , , , , , , , , ,	Boards: WG-4 1.1, CAB	gear shift	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence IntipProfileSequence IntipProfi	S8ul	? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence InterprofileSequence Interprofile	S8ul Oul Oul	? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfileSequence 7 chirpProfileSequence 9 chirpProfileSequence 7 chirpProfileSequence 9	S8ul	? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfileSequence 7 chirpProfileSequence 9 chirpProfileSequence 7 chirpProfileSequence 9	S8ul Oul Oul	? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfileSequence 7 chirpProfileSequence 9 chirpProfileSequence 7 chirpProfileSequence 9	S8ul	? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence.ength dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable txEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient	58ul	? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON Profile 0 tested only	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 1 chirpProfileSequence 1 dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable txEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient	S8ul	? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 1 dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable txEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks	S8ul	? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON Profile 0 tested only 40[MHz] 80[us]	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 1 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequenceIngth dynamicIpdatesEnabled dynamicIpdatesEnabled twnamicIpdatesEnabled twna	S8ul	? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON Profile 0 tested only 40[MHz]	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequency 2 chirpProfileSequency 2 chirpProfileSequency 3 chirpProfileSequency 4 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 7	58ul Oul Oul	? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 1 chirpProfileSequence 7 chirpProfileSequence 1 chirpProfileSequence.length dynamicTableIndex chirpFrequencyDrift fastResetEnable txEnable txEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient IN] effectiveSamplingFrequency chirpIntervalTimeTicks settleTimeTicks settleTimeTicks resetTimeTicks resetTimeTicks	58ul Oul Oul	? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON	
chirpSequenceCon	chipProfileSequence 0 chipProfileSequence 1 chipProfileSequence 2 chipProfileSequence 2 chipProfileSequence 3 chipProfileSequence 4 chipProfileSequence 5 chipProfileSequence 5 chipProfileSequence 6 chipProfileSequence 7 chipProfileSequence 7 chipProfileSequence 7 chipProfileSequence 8 chipProfileSequence 9 chipProfileSequence 1 chipProfileSequence 1 chipProfileSequence 7 chipProfileSequence 7 chipProfileSequence 1 chipProfileSequence 7 chipIntervaliteTeable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chipIntervalTimeTicks dwellTimeTicks settleTimeTicks sequence 1 chipProfileSequence 2 chipProfileSequence 3 chipProfileSequence 3 chipProfileSequence 5 chipProfileSequence 7 chipProfil	S8ul Dul Dul	? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequency chirpProfileSequency chirpInterLable chirpInterLable chirpInterLable chirpIntervalTimeTicks dwellTimeTicks settleTimeTicks caquisitionTimeTicks resetTimeTicks centeFrequency effectiveChirpBandwidth	S8ul	? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 1 dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable txEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks settleTimeTicks settleTimeTicks resetTimeTicks centerFrequency effectiveChirpBandwidth chirpPIlVcoSelect	58ul Oul Oul	? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON Rx1\2\4 = ON	
chirpSequenceCon	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 8 chirpProfileSequence 9 chirpProfileSequence 9 chirpProfileSequence 1 chirpProfileSequence 1 chirpProfileSequence 7 chirpPrefileSequence 7 chirpLireSequency chirpInterValiteTenable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpInterValiteTicks settleTimeTicks settleTimeTicks cacquisitionTimeTicks resetTimeTicks centerFrequency effectiveChirpBandwidth chirpPlIVcoSelect chirpSlopeDirection	S8ul	? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift	
chirpSequenceCon fig[M]	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 1 chirpProfileSequence 1 chirpProfileSequence 1 chirpProfileSequence 1 chirpProfileSequence 1 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequency Drift fastResetEnable txEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks dwellTimeTicks settETimeTicks caquisitionTime Ticks resetTimeTicks centerFrequency effectiveChirpBandwidth chirpPIIVcoSelect chirpSlopeDirection chirpPIILoopFilterBandwidth	S8ul	? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON Rx1\2\4 = ON Rx1\4 =	
chirpSequenceCon	chirpProfileSequence_0 chirpProfileSequence_1 chirpProfileSequence_2 chirpProfileSequence_2 chirpProfileSequence_3 chirpProfileSequence_6 chirpProfileSequence_6 chirpProfileSequence_6 chirpProfileSequence_7 chirpProfileSequence_1 dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable txEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient IN] effectiveSamplingFrequency chirpIntervalTimeTicks settleTimeTicks acquisitionTimeTicks centerFrequency effectiveChirpBandwidth chirpPIlVcoSelect chirpSlopeDirection chirpPIltcoSelect chirpSlopeDirection chirpPIltcoSelect chirpSlopeDirection chirpPIltcoSelect chirpSlopeDirection chirpPIltcoSelent tYransissionEnable	S8ul	? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON Rx1\3\4 = ON Rx1\2\3\4 = ON Rx1\2\3\4 = ON Rx1\2\3\4 = ON Rx1\2\4 = ON Rx1\4 = ON	
chirpSequenceCon fig[M]	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence.ength dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable bEnable bEnable bEnable bEnable bEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks dwellTimeTicks acquisitionTimeTicks resetTimeTicks centerFrequency effectiveChirpBandwidth chirpPilVcoSelect chirpSlopeDirection chirpPlLoopFilterBandwidth bTransissionEnable bVPower	S8ul Oul Oul	? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift	
chirpSequenceCon fig[M]	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 1 dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable txEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks dwellTimeTicks settleTimeTicks settleTimeTicks centerFrequency effectiveChirpBandwidth chirpPIIVcoSelect chirpIsopeDirection chirpPIILoopFilterBandwidth txTransissionEnable txPower txHnasissionEnable txPower	S8ul	? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON Rx1\2\4 = ON Rx1\2\3\4 = ON Rx1\2\4 = ON Rx1\4 = ON Rx	
chirpSequenceCon fig[M]	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 1 dynamicUpdatesEnabled dynamicUpdatesEnabled dynamicUpdatesEnabled tynamicUpdatesEnabled tynamicUpdatesEnable tyna	S8ul	? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON Rx1\2\4 = ON Rx1\4	
chirpSequenceCon fig[M]	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 1 chirpProfileSequence.ength dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable bEnable rxEnable rxEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks dwellTimeTicks acquisitionTimeTicks resetTimeTicks resetTimeTi	S8ul	? ? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift	
chirpSequenceCon fig[M]	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 1 dynamicUpdatesEnabled dynamicUpdatesEnabled dynamicUpdatesEnable txEnable pdcNotchFilterEnable pdcNotchFilterEnable pdcNotchFilterCoefficient IN] effectiveSamplingFrequency chirpIntervalTimeTicks dwellTimeTicks settleTimeTicks settleTimeTicks resetTimeTicks centerFrequency effectiveChirpBandwidth chirpPlIVcoSelect chirpSlopeDirection chirpPlIICopFilterBandwidth txTransissionEnable txPower txPhaseRotation Tx1 txPhaseRotation Tx1 txPhaseRotation Tx3 txPhaseRotation Tx4 txPhaseRotation Tx4	S8ul Oul Oul	? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift Tx1 = ON, Tx2\3\4 = OFF, works for all Tx but one Tx at the time was tested Rx1\2\3\4 = ON Rx1\2\4 = ON Rx1\4	
chirpSequenceCon fig[M]	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 1 dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable txEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks acquisitionTimeTicks settleTimeTicks acquisitionTimeTicks centerFrequency effectiveChirpBandwidth chirpPIlVcoSelect chirpSlopeDirection chirpPIltcoSelect chirpSlopeDirection chirpPIltcoSelect chirpSlopeDirection chirpPIltcoSelect chirpSlopeDirection chirpPIltcoSelect chirpSlopeDirection chirpPIltcoSelect thirpSlopeDirection	S8ul Oul Oul	? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift	
chirpSequenceCon fig[M]	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 5 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequenceIength dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient IN] effectiveSamplingFrequency chirpIntervalTimeTicks dwellTimeTicks settleTimeTicks settleTimeTicks resetTimeTicks centerFrequency effectiveChirpBandwidth chirpPIIVcoSelect chirpSlopeDirection chirpPIILopFilterPandwidth txTransissionEnable txPower txPhaseRotation Tx1 txPhaseRotation Tx2 txPhaseRotation Tx3 txPhaseRotation Tx4 txPhaseRotation Tx6 txTransmissionTimeOffset fastResetDelayTicks	S8ul	? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift	
chirpSequenceCon fig[M]	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 1 dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable txEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks wellTimeTicks settleTimeTicks settleTimeTicks centerFrequency effectiveChirpBandwidth chirpPilvcoSelect chirpSlopeDirection chirpPiltcoSpliterBandwidth txTransissionEnable txPower txPhaseRotation Tx2 txPhaseRotation Tx4 txBrableReferenceTime txTransmissionTimeOffset fastResetDurationTicks	S8ul Oul Oul	? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift	
chirpSequenceCon fig[M]	chirpProfileSequence 0 chirpProfileSequence 1 chirpProfileSequence 2 chirpProfileSequence 3 chirpProfileSequence 3 chirpProfileSequence 4 chirpProfileSequence 5 chirpProfileSequence 6 chirpProfileSequence 7 chirpProfileSequence 7 chirpProfileSequence 1 dynamicUpdatesEnabled dynamicTableIndex chirpFrequencyDrift fastResetEnable txEnable txEnable txEnable pdcNotchFilterEnable pdcNotchFilterCoefficient INI effectiveSamplingFrequency chirpIntervalTimeTicks wellTimeTicks settleTimeTicks settleTimeTicks centerFrequency effectiveChirpBandwidth chirpPilvcoSelect chirpSlopeDirection chirpPiltcoSpliterBandwidth txTransissionEnable txPower txPhaseRotation Tx2 txPhaseRotation Tx4 txBrableReferenceTime txTransmissionTimeOffset fastResetDurationTicks	S8ul	? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ? ?	Boards: WG-4 1.1, CAB	gear shift	

rxHpfCutOffFrequency	0ul \ rfe_rxHpfCutOffFrequency_800kHz_e		Boards: WG-4 1.1, CAB	
rxLpfCutOffFrequency	3ul \ rfe_rxLpfCutOffFrequency_25MHz_e		Boards: WG-4 1.1, CAB	
VirtualChannel	0ul \ rfe_virtualChannel_0_e	✓	Boards: WG-4 1.1, CAB	

release 0.8.19, SAF85xx_RFE_SW_EAR_0.8.19_D230929, Confidential and shared under NDA							
				Notation			
RFE Dynamic		✓	PASS	ul: 32bit decimal number		NXO	
Parameters	Legend	?	DO NOT USE	0x: hexadecimal		SAF85XX	
Testing		X	FAIL	b: binary number		SAF8SAA	
Component	Functionality	Value tested	Result	Test Environment	Additional info	Change in current	
•	•		11000	1001=1111		release	
	chirpProfileIndex	0-7	✓	Boards: WG-4 1.1, CAB			
	tx1PhaseRotation	any	✓	Boards: WG-4 1.1, CAB			
	tx2PhaseRotation	any	✓	Boards: WG-4 1.1, CAB			
	tx3PhaseRotation	any	✓	Boards: WG-4 1.1, CAB			
Dynamic Table	tx4PhaseRotation	any	✓	Boards: WG-4 1.1, CAB			
	txTransmissionEnable	any	✓	Boards: WG-4 1.1, CAB			
	dwellTimeTicks	any	✓	Boards: WG-4 1.1, CAB			
	chirpIntervalTimeTicks	any	✓	Boards: WG-4 1.1, CAB			
	chirpFrequencyDrift	any	✓	Boards: WG-4 1.1, CAB			