		Baseline	ResAlloc	IrrShape	HalfFloat
Largest Matrix Shape	M	32	56	64	96
	N	32	56	64	96
	K	32	56	56	96
FPGA Resource Usage (%)	BRAM18K	23	42	42	40
	DSP48E	72	76	90	43
	FF	18	35	34	30
	LUTs	46	95	91	40
Latency (cycles)		2352	6736	8673	19030
Clock frequency (MHz)		143	143	143	143
Esti. GFLOPS		4.1713197	7.65610451	7.76648449	13.50436994
Block Test GFLOPS		3.31	6.77	7.2	12.31
Full Test GFLOPS		1.45	2.61	3.03	4.44