

## Laboratory Report Cover Sheet

SRM INSTITUTE OF SCIENCE AND TECHNOLOGY  
College of Engineering and Technology  
Department of Electronics and Communication Engineering

**18ECC206J VLSI Design**  
**VI Semester, 2022-2023 (Even Semester)**

**Name :**

**Register No. :**

**Venue :**

**Title of Experiment :**

**Date of Conduction:**

**Date of Submission:**

Particulars	Max. Marks	Marks Obtained
Pre-Lab and Post Lab	10	
Design, HDL Code, In-Lab Performance	15	
Output verification & viva	10	
Lab Report	05	
<b>Total</b>	<b>40</b>	

### REPORT VERIFICATION

**Staff Name :**

**Signature :**