Lab 4 Report – Gem 5 and McPAT

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1 Automation Script using Gem5 and McPAT

The following study scripts were used to conduct design-space exploration experiments using Gem5 and McPAT:

- Makefile to compile the benchmark programs (*matmul.c* and *crc32.c*) using 2 different levels of optimization, -O0 and -O3.
- Run scripts to automate the performance experiments using Gem5 with 3 different memory models and 4 different operating frequencies.
- Run scripts to automate the power experiments for the above configurations

The memory models selected for the experiments are listed below:

- SimpleMemory
- DDR3 2133 8x8
- DDR4 2400 4x16

The clock frequencies selected for the experiments are listed below:

- 200 MHz
- 800MHz
- 1400MHz
- 2000MHz

The number of combinations for each benchmark program is 2*3*4 = 24 simulations. Since there are 2 benchmark programs and 2 types of simulations for each benchmark (performance and power), the total number of experiments that need to be run is 24*2*2 = 96 simulations. Hence, the Run scripts are used in this case to automate tasks. The following figures shows code snippets for Makefile.

```
FILE = crc32.c

bitcnts: ${FILE} Makefile
    arm-linux-gnueabi-gcc -static ${FILE} -00 -marm -march=armv7-a -o crc32_00_arm32
    arm-linux-gnueabi-gcc -static ${FILE} -03 -marm -march=armv7-a -o crc32_03_arm32
clean:
    rm -rf crc32_* output*
```

Figure 1: Makefile that compiles the benchmark program using -00 and -03 optimization level. This is a modified version of the provided Makefile script in the Ubuntu image.

As for the run scripts, bash script and functions are used to automate the simulation tasks. The main logic are shown in Figure 2.

```
3
  clear
4 echo ""
5 echo " Running gem5 simulations for ex1-COMP1"
6 echo ""
10 cd /home/user/gem5/
12 run () {
13
      OUTPUT=$1
14
15
      CLK_FREQ=$2
16
      MEM TYPE=$3
17
      BINARY=$4
18
     POWER=$5
19
20
      echo "Running with $1, $2, $3, $4, power: $5"
21
      ./build/ARM/gem5.opt --stats-file=${OUTPUT} \
         ./configs/example/se.py --sys-voltage="${POWER}V" \
23
          --cpu-type=TimingSimpleCPU \
         --cpu-clock="${CLK_FREQ}MHz" \
25
        --mem-type=${MEM_TYPE} \
27
         -c /home/user/COMP61232/matmul/${BINARY}
28
      echo "###"
29
30
      echo "# Stage finished..."
31
      echo "###"
32 }
33
34 # ======== Un-optimized =========
35 run "ex1_00_SimpleMemory_200MHz.txt" "200" "SimpleMemory" "matmul_00_arm32" "0.9"
36 run "ex1_00_SimpleMemory_800MHz.txt" "800" "SimpleMemory" "matmul_00_arm32" "1.1"
  run "ex1_00_SimpleMemory_1400MHz.txt" "1400" "SimpleMemory" "matmul_00_arm32" "1.4"
37
38 run "ex1_00_SimpleMemory_2000MHz.txt" "2000" "SimpleMemory" "matmul_00_arm32" "1.9"
40 run "ex1_00_DDR4_200MHz.txt" "200" "DDR4_2400_4x16" "matmul_00_arm32" "0.9"
  run "ex1_00_DDR4_800MHz.txt" "800" "DDR4_2400_4x16" "matmul_00_arm32" "1.1"
  run "ex1_00_DDR4_1400MHz.txt" "1400" "DDR4_2400_4x16" "matmul_00_arm32" "1.4"
43 run "ex1_00_DDR4_2000MHz.txt" "2000" "DDR4_2400_4x16" "matmul_00_arm32" "1.9"
```

1 #!/bin/sh

Figure 2: Automation script used for Gem5 simulations. Note that this is just a snippet to show the main logic. There are more lines to test DDR3 memory configuration and O3 optimization levels.

Function run() was designed to take various configuration arguments for Gem5. Line 14 - 18 are the input parameters for run() function. This function is called in Lin33 and onwards to run the simulation with various frequencies, memory systems and compiler optimization levels. Note that in Line 23 a voltage parameter is given to Gem5 when simulation using various frequencies.

Similar automation strategy are used for McPAT simulations. Figure 3 shows the code snippet for convert() and run_mcpat() functions used to automation McPAT simulations.

```
1 #!/bin/sh
    3 clear
    5 convert () {
                                 TNPUT=$1
                                 OUTPUT=$2
                                CLK_FREQ=$3
10
                               echo "@@DEBUG: Converting with $1, $2, $3"
11
                                  python \ /home/user/COMP61232/gem5\_to\_mcpat.py \ /home/user/gem5/m5out/\$\{INPUT\} \ \backslash \ /fraction{The python of the python of th
12
                                                                     /home/user/mcpat/ProcessorDescriptionFiles/${OUTPUT} \
                                                                       S{CLK FREQ}
15
16 }
18 run_mcpat () {
19
                                  IN_XML=$1
20
21
                                  OUT_TXT=$2
22
                                 ./mcpat -infile ./ProcessorDescriptionFiles/${IN_XML} -print_level 0 > ./ProcessorDescriptionFiles/${OUT_TXT}
25
                                echo "------ Done with IN: $1. OUT: $2 -------
26
27
                                  echo "====== "
```

Figure 3: Functions used to convert gem5 output files and run McPAT simulations

The convert() and run_mcpat() functions are called as shown in Figure 4 and Figure 5 below:

```
31 echo " Converting Gem5 Stats files to McPAT XML files... "
       ----- 00 -----
33 convert "ex1_00_SimpleMemory_200MHz.txt" "ex1_00_SimpleMemory_200MHz.xml" 200
34 convert "ex1_00_SimpleMemory_800MHz.txt" "ex1_00_SimpleMemory_800MHz.xml" 800
35 convert "ex1_00_SimpleMemory_1400MHz.txt" "ex1_00_SimpleMemory_1400MHz.xml" 1400
36 convert "ex1_00_SimpleMemory_2000MHz.txt" "ex1_00_SimpleMemory_2000MHz.xml" 2000
38 echo "Done converting simple memory"
40 convert "ex1_00_DDR4_200MHz.txt" "ex1_00_DDR4_200MHz.xml" 200
41 convert "ex1_00_DDR4_800MHz.txt" "ex1_00_DDR4_800MHz.xml" 800
42 convert "ex1_00_DDR4_1400MHz.txt" "ex1_00_DDR4_1400MHz.xml" 1400
43 convert "ex1_00_DDR4_2000MHz.txt" "ex1_00_DDR4_2000MHz.xml" 2000
45 echo "Done converting DDR4"
                        Figure 4: Automating the convert tasks
74 echo " Running McPAT Power Simulations... "
75 cd /home/user/mcpat
                    ----- 00-MCPAT -----
76 #==
77 run_mcpat "ex1_00_SimpleMemory_200MHz.xml" "ex1_mcpat_00_SimpleMemory_200MHz.txt"
78 run_mcpat "ex1_00_SimpleMemory_800MHz.xml" "ex1_mcpat_00_SimpleMemory_800MHz.txt"
79 run_mcpat "ex1_00_SimpleMemory_1400MHz.xml" "ex1_mcpat_00_SimpleMemory_1400MHz.txt"
80 run_mcpat "ex1_00_SimpleMemory_2000MHz.xml" "ex1_mcpat_00_SimpleMemory_2000MHz.txt"
81
82 run_mcpat "ex1_00_DDR4_200MHz.xml" "ex1_mcpat_00_DDR4_200MHz.txt"
83 run_mcpat "ex1_00_DDR4_800MHz.xml" "ex1_mcpat_00_DDR4_800MHz.txt"
84 run_mcpat "ex1_00_DDR4_1400MHz.xml" "ex1_mcpat_00_DDR4_1400MHz.txt"
85 run_mcpat "ex1_00_DDR4_2000MHz.xml" "ex1_mcpat_00_DDR4_2000MHz.txt"
87 run_mcpat "ex1_00_DDR3_200MHz.xml" "ex1_mcpat_00_DDR3_200MHz.txt"
88 run_mcpat "ex1_00_DDR3_800MHz.xml" "ex1_mcpat_00_DDR3_800MHz.txt"
   run_mcpat "ex1_00_DDR3_1400MHz.xml" "ex1_mcpat_00_DDR3_1400MHz.txt"
90 run_mcpat "ex1_00_DDR3_2000MHz.xml" "ex1_mcpat_00_DDR3_2000MHz.txt"
```

Figure 5: Automating the McPAT simulations using the converted files

2 Data Extraction and Processing Methods

The key data extraction targets are listed below:

- Time: "sim_seconds" form Gem5 output files

- Power: "Runtime Dynamic" from McPAT output files

Bash grep command was used for data extraction. The following figures show the command used for extracting "sim seconds" and "Runtime Dynamic" data:

```
user@ubuntu:~/gem5/m5out$ grep "sim_seconds" ex1_00_SimpleMemory_* | grep '[0-9]\.[0-9]\+' -oh
0.569242
0.555431
0.557666
0.557664
```

Figure 6: Bash command used to extract sim seocnds

```
user@ubuntu:~/mcpat/ProcessorDescriptionFiles$ grep "Processor:" -a8 ex1_mcpat_00_SimpleMemory_* | grep "Runtime Dynamic" | grep '[0-9]\.[0-9]\+' -o
0.00378404
0.00527874
0.006817221
0.006230396
```

Figure 7: Bash command used to extract "Runtime Dynamic"

The main strategy used here is bash command grep with regular expression to extract the data. Note that the data extracted are in the order {1400MHz, 2000MHz, 2000MHz, 800MHz} due to the file name sorting logic using grep. The option "-a8" was used to extract "Runtime Dynamic" power because we only want to extract the target line in "Processor: " sections, rather than the lines below "Total Cores: 1 cores:" or "Total First Level Directory:".

The data are copied into Google spreadsheet to generate the table and generate graph. An example is shown below:

Freq	SimpleMem_t	SimpleMem_O3	DDR3_t	DDR3_2133_8x8_O3	DDR4_t	DDR4_2400_4x16_O3	03
1400	0.111264	8.987633017	0.148015	6.75607202	0.151175	6.614850339	
2000	0.10857	9.210647509	0.145262	6.884112844	0.148302	6.742997397	
200	0.109673	9.118014461	0.151184	6.614456556	0.152514	6.556775116	
800	0.10966	9.119095386	0.147347	6.786700781	0.151093	6.618440298	
Freq	SimpleMem p	SimpleMem 00	DDR3_p	DDR3_2133_8x8_O0	DDR4_p	DDR4_2400_4x16_00	00
1400	0.00378404		_		_	0.002705902173	
2000	0.00527874	0.002931975837	0.0052114	0.003657219812	0.00520705	0.003717380687	
200	0.000817221	0.0004557363662	0.000745544	0.0005349852269	0.000741172	0.0005413149702	
800	0.00230396	0.001284835549	0.0022369	0.001576063818	0.00222939	0.001618488093	
		Joule		Joule		Joule	

Figure 8: Table examples of Performance and Power statistics

The energy consumption was calculated by the formula below:

Energy Consumption in joules = Runtime Dynamic Power \times Execution time sim_seconds

3 Results

3.1 Performance vs Frequency

The following figures are the performance analysis for matmul and crc32 respectively, using various frequencies and memory systems.

Performance vs Frequency - matmul

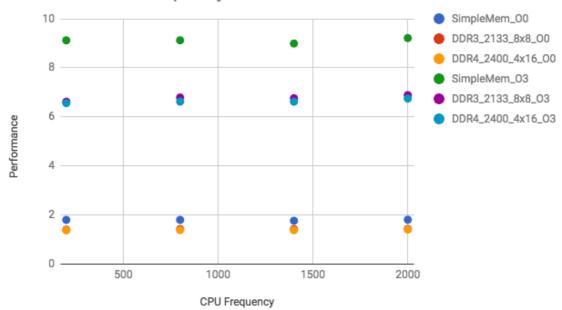
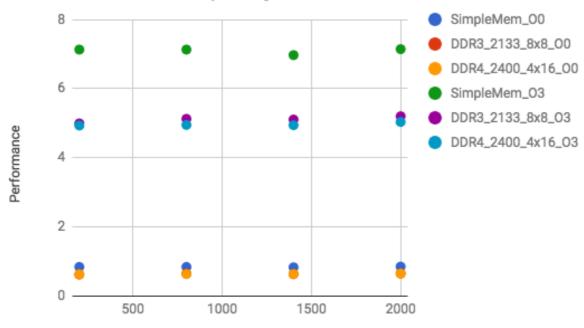


Figure 9: Gem5 simulation results using various memory systems and frequencies – matmul benchmark

Performance vs Frequency crc32



 $\textit{Figure 10: Gem5 simulation results using various memory systems and frequencies-crc 32\ benchmark}$

In both cases, SimpleMem_O3 outperforms all other configurations. The configuration with DDR4_2400_4x16_O0 runs slowest. In general, for the same configuration the simulation runs faster using matmul benchmark than crc32 benchmark.

3.2 Energy vs Frequency

The following figures are the energy consumption analysis for matmul and crc32 respectively, using various frequencies and memory systems.

Energy vs Frequency - matmul

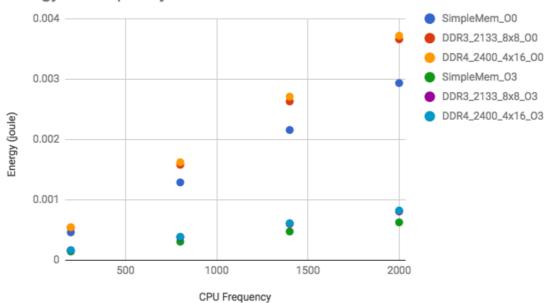


Figure 11: McPAT energy consumption result - matmul benchmark

Energy vs Frequency - crc32

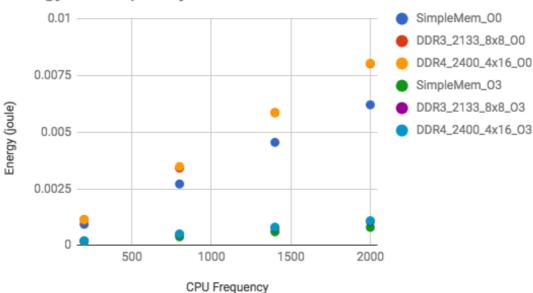


Figure 12: McPAT energy consumption result - crc32

In both cases, SimpleMem_O0 consumes the least amount of energy, and DDR4_2400_4x16_O0 consumes the most amount of energy. In general, both energy consumption increases linearly with increasing frequencies.

3.3 Energy vs Performance

The following figures shows the energy vs performance trend.

Energy vs. Performance - matmul

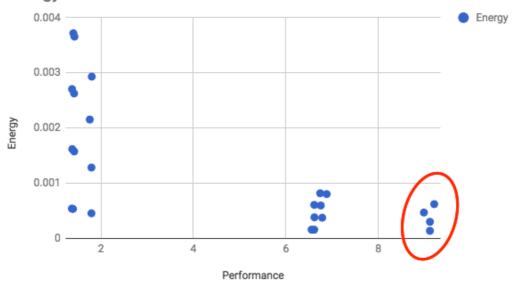


Figure 13: Energy vs Performance plot for matmul benchmark

Energy vs Performance - crc32

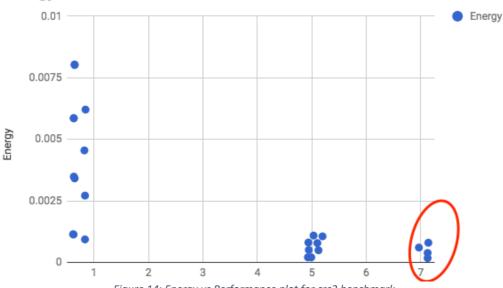


Figure 14: Energy vs Performance plot for crc2 benchmark

Unfortunately the Google spreadsheet does not have feature to add data label to the points. The user has to hover the mouse cursor on the point to identify it.

Base on Pareto optimization of multi-objective optimization, the points circled in red are the most suitable configurations for low energy consumption while still delivering high performance. These points are SimpleMemory configurations.