

RC4

ECE 3110J Analog Circuits

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Exercise: About value (Ixx, Vxx) se to get the parameter the small signal use the KVL, KCL =) again to some the circuit

BJT

 R_{out} and R_{in} Common-Emitter Amplifier Exercise



How to calculate R_{out} ?

- 1 Draw small signal model.
- 2 Connect port with V_{in} to ground. Connect a test voltage v_t on port with Vout.

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- Derive i_{t}
- 4 Calculate R_{out}



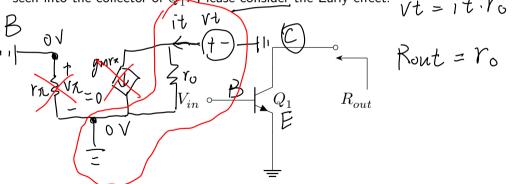


How to calculate R_{in} ?

- 1 Draw small signal model.
- 2 Connect a test voltage v_t on port with V_{in} . Do nothing on output port.
- \bullet Derive i_{+}
- Calculate $R_{in} = \frac{v_t}{i}$

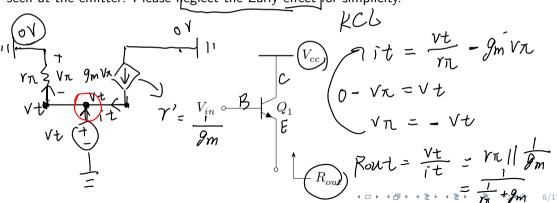


Assuming that the BJT operates in the forward active region, determine the impedance seen into the collector of Q. Please consider the Early effect.



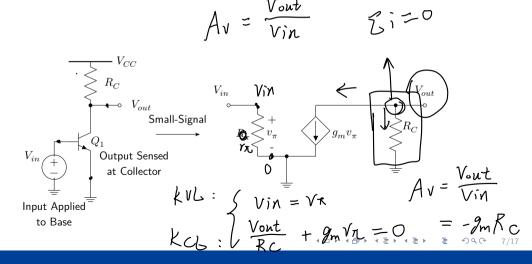


Assuming that the BJT operates in the forward active region, determine the impedance seen at the emitter. Please neglect the Early effect for simplicity.

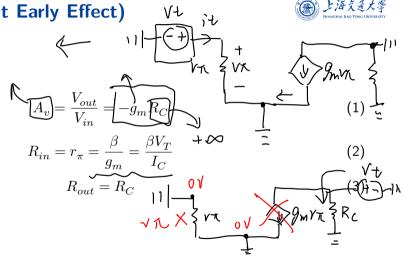


CE Amplifier (Without Early Effect)





CE Amplifier (Without Early Effect)



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CE Amplifier (Without Early Effect)

Important properties of the CE stage:

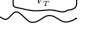
- 1 The small-signal gain is negative, i.e. $V_{BE} \uparrow$ and $I_C \uparrow$ lower Vout.
- 2 The voltage gain of the stage is limited by the supply voltage.

 $I_CR_C \text{ indicates the voltage drops at the collector resistor } R_C.$ Because $I_CR_C < V_{CC}$, $\mid A_v \mid < \frac{V_{CC}}{V_T}.$

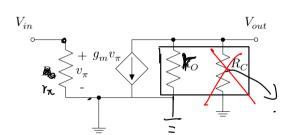
Because
$$I_C R_C < V_{CC}$$
, $\mid A_v \mid < \frac{V_{CC}}{V_T}$.

Finally, as the BJT is in the active region, we can say that, $\mid A_{\scriptscriptstyle n}$

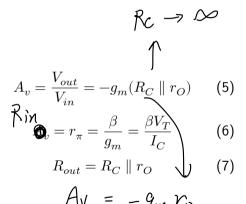
However, when $R_C \to \infty$, $|A_i| \to \infty$. Contradiction!



CE Amplifier (With Early Effect)







CE Amplifier (With Early Effect)



If $R_C \rightarrow \infty$, $A_v = -g_m r_O \rightarrow$ intrinsic gain Because

$$g_m = \frac{I_C}{V_T}, \ r_O = \frac{V_A}{I_C} \tag{8}$$

we can get:

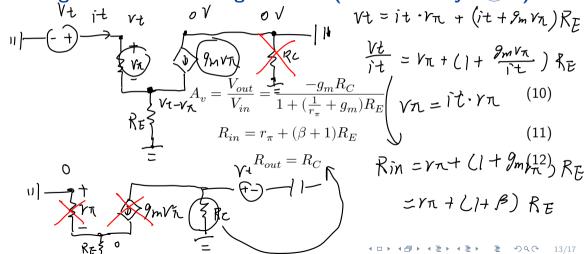
$$A_v = -\frac{V_A}{V_T} \tag{9}$$

The Early effect limits the voltage gain even if R_{C} approaches infinity.



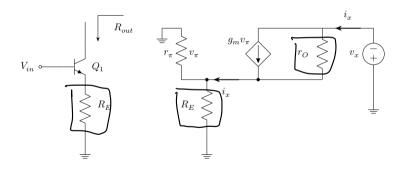
CE Stage With Emitter Degeneration (Without Early Extra - Voust 0

CE Stage With Emitter Degeneration (Without Early Emitter)



Common-Emitter Ampimer

CE Stage With Emitter Degeneration (With Early Effect) 上海京東大学 SKANGHAI HOUTONG UNIVERSITY

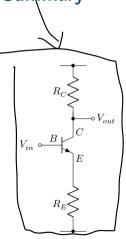


CE Stage With Emitter Degeneration (With Early Effect) 上海汽业大学

$$R_{out} = r_O + (1 + g_m r_O)(R_E \parallel r_\pi) \approx r_O[1 + g_m(R_E \parallel r_\pi)]$$
 (13)



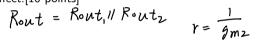


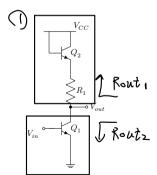


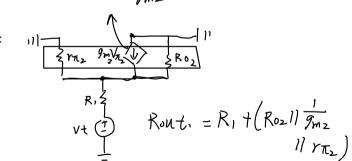
		Expression	
V	Definition	With emitter	Without emitter
		degeneration	degeneration; i.e., $R_{E}=0$
. Current gain	A_i	β	β
. Voltage gain	A_v	$-rac{eta R_C}{r_\pi + (eta + 1)R_E}$	$-g_m R_C$
. Input impedance	r_{in}	$r_{\pi} + (\beta + 1)R_E$	r_{π}
Output impedance	r_{out}	R_C	R_C



(23FA $\rm Mid/5$) Using a small-signal model, determine output impedance of the circuit shown below with and without Early effect.[10 points]

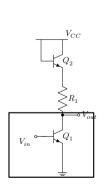


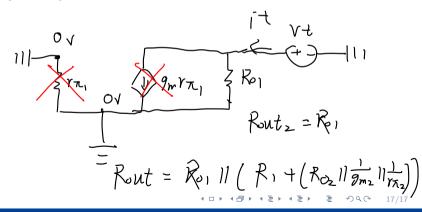






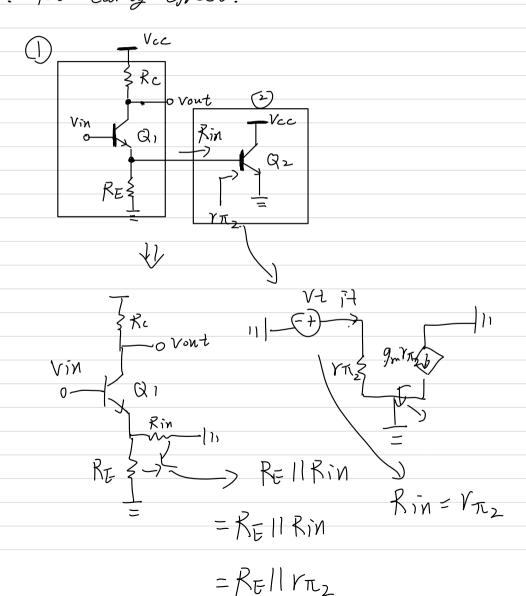
(23FA Mid/5) Using a small-signal model, determine output impedance of the circuit shown below with and without Early effect.[10 points]





Exercise 4.

Determine the voltage gain of the stage shown below. No early effect.



 $\mathcal{N}^{\mathcal{M}}$ Von-Vont 1+ RE (FT, +9m,) use the -gm, Rc

table

+ (RE//YT2)