



# Lecture 14: Source Followers

VE311 Electronic Circuits

Xuyang Lu  
2024 Summer



上海交通大學  
SHANGHAI JIAO TONG UNIVERSITY

# Recap of Last Lecture



- Review for the midterm

# Topics to Be Covered

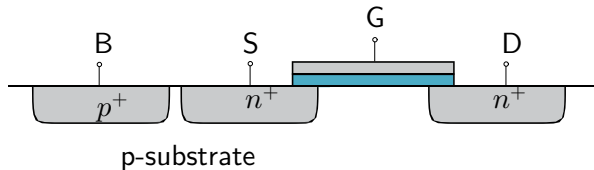


- Review for the midterm (MOSFET part)
- Source Follower

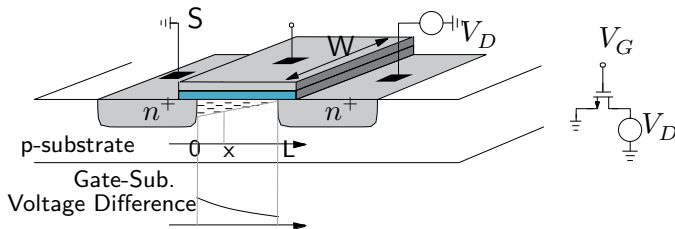
# NMOS FET



Reading: Razavi Chapter 2.

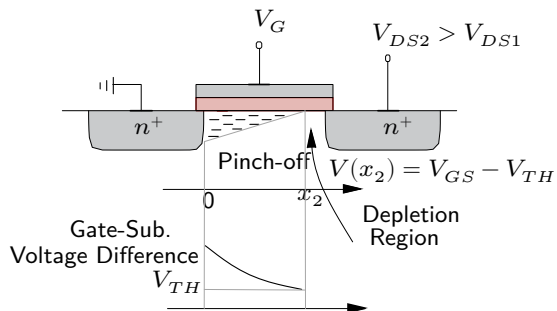
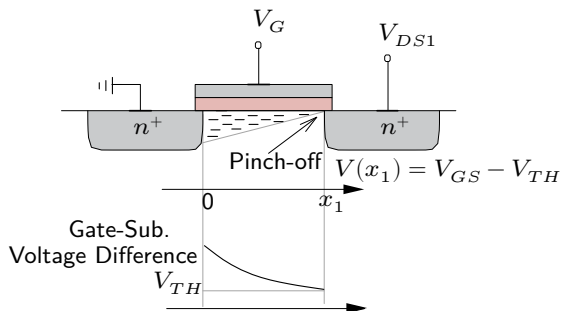


# NMOS I-V Characteristics (Triode)



$$I_D = \mu_n C_{ox} \frac{W}{L_{eff}} \left[ (V_{GS} - V_{TH}) V_{DS} - \frac{1}{2} V_{DS}^2 \right] \quad (1)$$

# Saturation Region



# Saturation Region



$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L'} (V_{GS} - V_{TH})^2 \quad (2)$$

- $I_D$ : constant along channel
- $L'$ : the point at which  $Q_d$  drops to zero
- $V_{GS} - V_{TH}$ : the overdrive voltage
- Electron velocity ( $v = I_D / Q_d$ ) becomes tremendously high at the pinch off point ( $Q_d \rightarrow 0$ ), such that electrons shoot through the depletion region and arrive at the drain terminal.

# Channel-Length Modulation



$$r_o = \frac{\partial V_{DS}}{\partial I_D} = 1 / \frac{\partial I_D}{\partial V_{DS}} \quad (3)$$

$$= \frac{1}{\frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})^2 \cdot \lambda} \quad (4)$$

$$\approx \frac{1}{I_D \cdot \lambda} \quad (5)$$



# Body Effect



$$V_{TH} = V_{TH0} + \gamma(\sqrt{|2\Phi_F + V_{SB}|} - \sqrt{|2\Phi_F|}) \quad (6)$$

$$\Phi_F = \frac{kT}{q} \ln \frac{N_{sub}}{n_i} \quad (7)$$

$$\gamma = \frac{\sqrt{2q\epsilon_{Si}N_{sub}}}{C_{ox}} \quad (8)$$

$$I_D = \frac{1}{2}\mu_n C_{ox} \frac{W}{L'} (V_{GS} - V_{TH})^2 \quad (9)$$

I will ask you how the body shall be connected.



# NMOS Formula Table

## NMOS Transistor Mathematical Model Summary

The following equations represent the complete model for the  $i - v$  behavior of the NMOS transistor.

For all regions,

$$K_n = K'_n \frac{W}{L} \quad K'_n = \mu_n C''_{ox} \quad i_G = 0 \quad i_B = 0 \quad (10)$$

Cut off region,

$$i_D = 0 \quad \text{for } v_{GS} \leq V_{TN} \quad (11)$$

Triode region,

$$i_D = K_n \left( v_{GS} - V_{TN} - \frac{v_{DS}}{2} \right) v_{DS} \quad \text{for } v_{GS} - V_{TN} \geq v_{DS} \geq 0 \quad (12)$$

# NMOS Formula Table



Saturation region,

$$i_D = \frac{K_n}{2} (v_{GS} - V_{TN})^2 (1 + \lambda v_{DS}) \quad \text{for } v_{DS} \geq (v_{GS} - V_{TN}) \geq 0 \quad (13)$$

Threshold voltage,

$$V_{TN} = V_{TO} + \gamma \left( \sqrt{v_{SB} + 2\phi_F} - \sqrt{2\phi_F} \right) \quad (14)$$

$V_{TN} > 0$  for enhancement-mode NMOS transistors. Depletion-mode NMOS devices can also be fabricated, and  $I_{TN} \leq 0$  for these transistors.

# PMOS Formula Table



## NMOS Transistor Mathematical Model Summary

The following equations represent the complete model for the  $i - v$  behavior of the NMOS transistor.

For all regions,

$$K_p = K'_p \frac{W}{L} \quad K'_p = \mu_p C''_{ox} \quad i_G = 0 \quad i_B = 0 \quad (15)$$

Cut off region,

$$i_D = 0 \quad \text{for } V_{GS} \geq V_{TP} \quad (16)$$

Triode region,

$$i_D = K_p \left( v_{GS} - V_{TP} - \frac{v_{DS}}{2} \right) v_{DS} \quad \text{for } 0 \leq |v_{DS}| \leq |v_{GS} - V_{TP}| \quad (17)$$

# PMOS Formula Table



Saturation region,

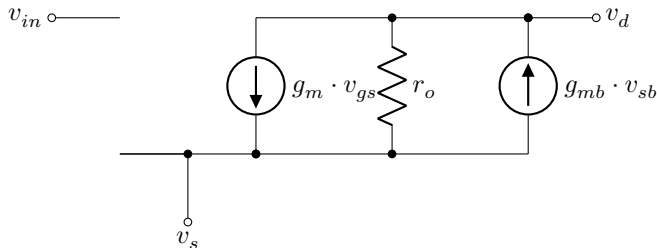
$$i_D = \frac{K_p}{2} (v_{GS} - V_{TP})^2 (1 + \lambda |v_{DS}|) \quad \text{for } |v_{DS}| \geq |v_{GS} - V_{TP}| \geq 0 \quad (18)$$

Threshold voltage,

$$V_{TN} = V_{TO} + \gamma \left( \sqrt{v_{SB} + 2\phi_F} - \sqrt{2\phi_F} \right) \quad (19)$$

For the enhancement-mode PMOS transistor,  $V_{TP} < 0$ . Depletion-mode PMOS devices can also be fabricated;  $I_{TP} \geq 0$  for these devices.

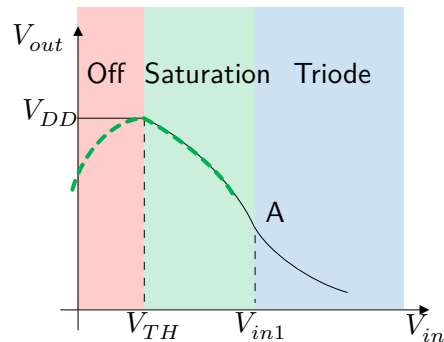
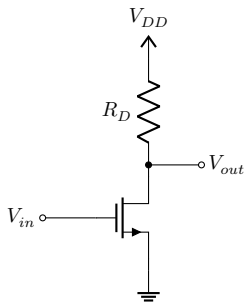
# SS of NMOS = SS of PMOS



$$g_m = \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH}) (1 + \lambda V_{DS}) \quad (20)$$

$$r_o = \frac{\partial V_{DS}}{\partial I_D} = 1 / \frac{\partial I_D}{\partial V_{DS}} \approx \frac{1}{I_D \cdot \lambda} \quad (21)$$

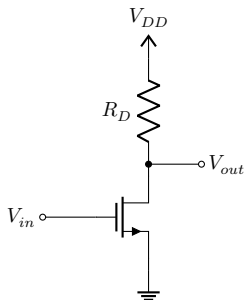
# Common-Source



I will ask you how to determine the DC operation region.

For example, What is the value of  $V_{in1}$

# Common-Source and its Variations



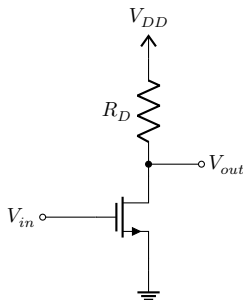
- Gain, bias, and small-signal model
- Input and output impedance
- Advantages:
  - Large input impedance
  - Large gain
  - Widely used
  - No current through Gate, easily biased.

Of course, there is going to be a question related to a common source amplifier and its derivations.





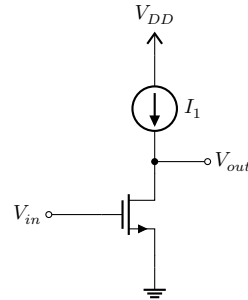
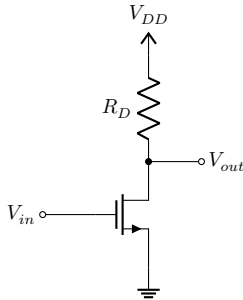
# Common-Source and its Variations



- Cons:
  - $R_D$  and  $M_1$  process variation
  - (Operating condition?) Swing limited by  $R_D$
  - Output DC voltage cannot be chosen arbitrarily
  - Output impedance and gain tradeoff

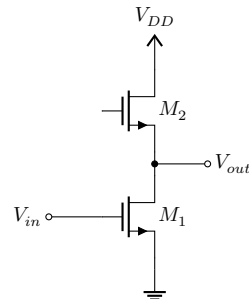
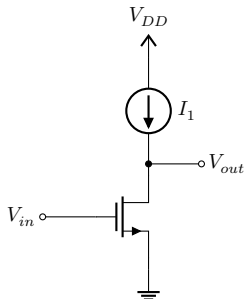
Of course, there is going to be a question related to a common source amplifier and its derivations.

# Common-Source and its Variations



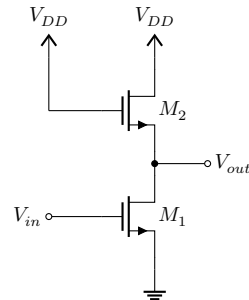
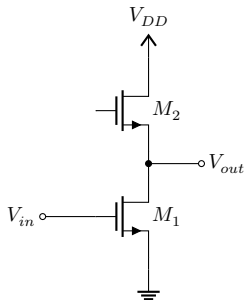
- Current Source, provide high impedance
- Problem: there is no ideal current source in the world.

# Common-Source and its Variations



- DC biased NMOS Load: Good approximation of current source.
- Pros: Less process variation.
- Cons: Additional Bias to be generated, Body effect and therefore Linearity problem.

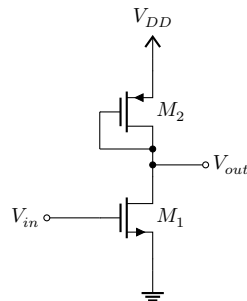
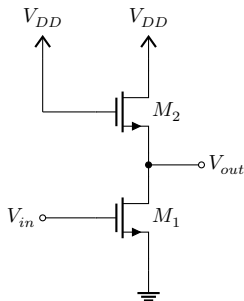
# Common-Source and its Variations



- No need to generate an additional voltage.  $M_2$  is always in saturation.
- Still, output impedance and linearity

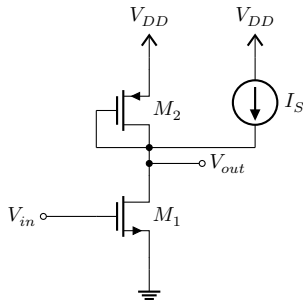
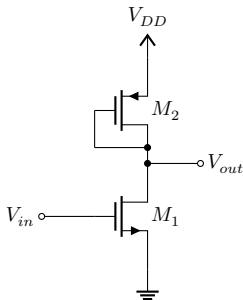


# Common-Source and its Variations



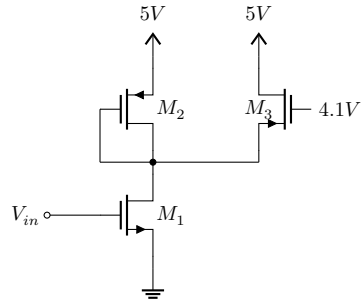
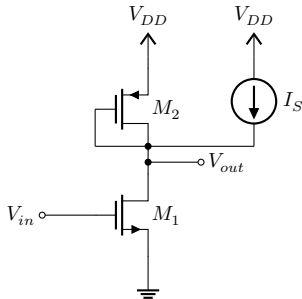
- PMOS to improve linearity
- No more body effect.
- Cons: Now how is the DC defined? Limited output swing.
- Large input capacitance.

# Common-Source and its Variations

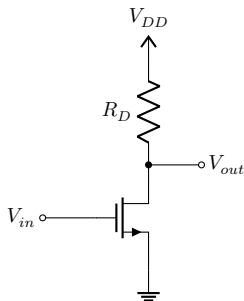


- Additional Current path
- Pros: Improve voltage swing, reduce input cap size.

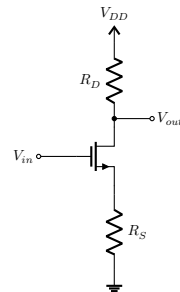
# Common-Source and its Variations



# Common-Source and its Variations



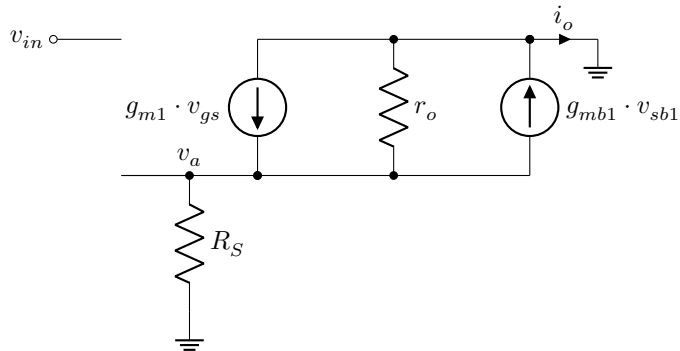
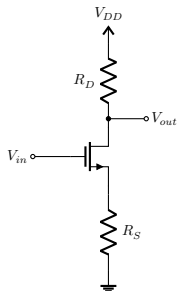
- Pros: Linearity
- Cons: Gain



$$Gain = G_m \cdot R_{out} \quad (22)$$



# CS with Source Degeneration



# CS with Source Degeneration



$$i_o = \frac{-v_a}{R_S} \quad (23)$$

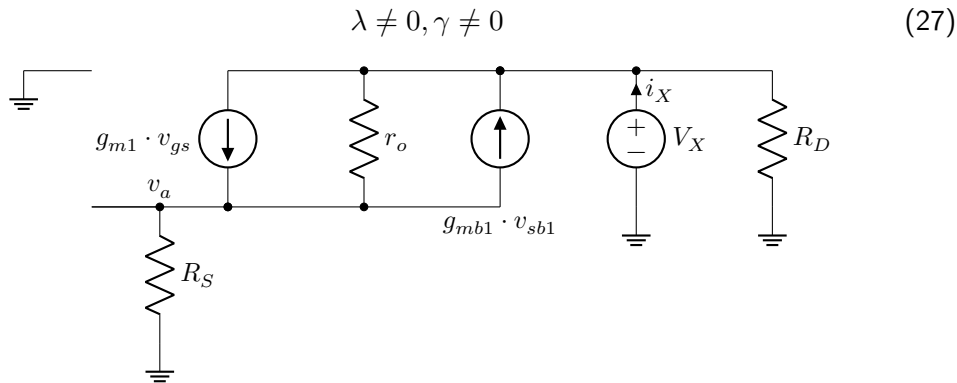
$$(v_{in} - v_a) g_{m1} + i_o = \frac{v_a}{r_{o1}} + v_a g_{mb1} \quad (24)$$

$$G_m = \frac{i_o}{v_{in}} = \frac{-g_{m1} r_{o1}}{R_S + r_{o1} + (g_{m1} + g_{mb1}) r_{o1} R_S} \approx -\frac{1}{R_S} \quad (25)$$

If,

$$(g_{m1} + g_{mb1}) r_{o1} R_S \gg r_{o1} \text{ and } R_S \quad (26)$$

# CS with Source Degeneration



# CS with Source Degeneration

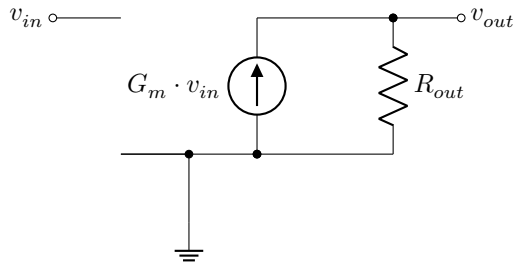


$$i_x = \frac{v_a}{R_S} \quad (28)$$

$$v_a g_{m1} + v_a g_{mb1} + \frac{v_a - v_x}{r_o} + i_x = 0 \quad (29)$$

$$R_{out} = R_{out1} \parallel R_D = [R_S + r_{o1} + (g_{m1} + g_{mb1}) r_{o1} R_S] \parallel R_D \approx R_D \quad (30)$$

# CS with Source Degeneration



# CS with Source Degeneration



$$A_v = \frac{v_{\text{out}}}{v_{\text{in}}} = G_m R_{\text{out}} \quad (31)$$

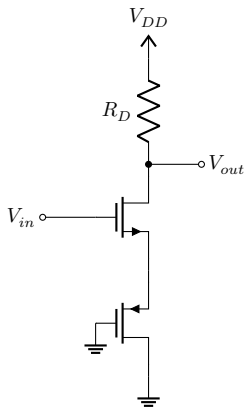
$$= \frac{-g_{m1} r_{o1}}{R_S + r_{o1} + (g_{m1} + g_{mb1}) r_{o1} R_S} \cdot \frac{[R_S + r_{o1} + (g_{m1} + g_{mb1}) r_{o1} R_S] R_D}{[R_S + r_{o1} + (g_{m1} + g_{mb1}) r_{o1} R_S] + R_D} \quad (32)$$

$$\approx -\frac{R_D}{R_S} \quad \text{If } (g_{m1} + g_{mb1}) r_{o1}, \text{ the intrinsic gain, is large.} \quad (33)$$

## Example



Assuming  $\lambda = \gamma = 0$ , calculate the small signal voltage gain of the circuit below.



$$G_m = -\frac{1}{\frac{1}{g_{m1}} + \frac{1}{g_{m2}}} \quad (34)$$

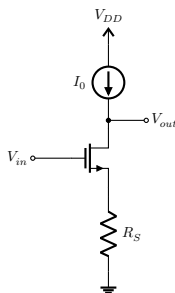
$$R_{out} = R_D \quad (35)$$

$$A_v = G_m R_{out} \quad (36)$$

## Example



Calculate the small signal voltage gain of the circuit below.



$$G_m = \frac{-g_{m1}r_{o1}}{r_{o1} + R_S + (g_{m1} + g_{mb1})r_{o1}R_S} \quad (37)$$

$$R_{out} = r_{o1} + R_S + (g_{m1} + g_{mb1})r_{o1}R_S \quad (38)$$

$$A_v = G_m R_{out} = -g_{m1}r_{o1} \quad (39)$$

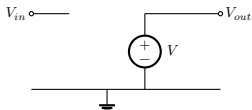
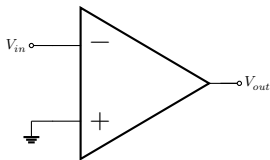
- $I_0$  is ideal current source  $\rightarrow$  Voltage across  $R_S$  is constant  $\rightarrow M_1$  source shorted to ground
- $R_D$  replaced by current source  $\rightarrow$  Nonlinearity issue arises again



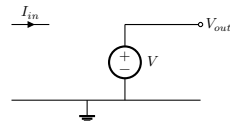
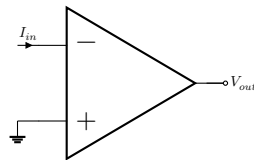
# Ideal Amplifier



Voltage Amp.



Transimpedance Amp.

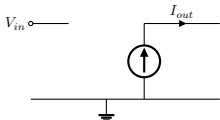
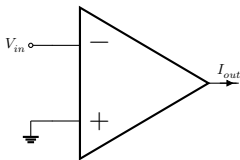


- For driving a low impedance load, source follower, as a buffer, provides no gain but large input impedance and low output impedance.

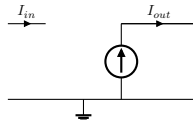
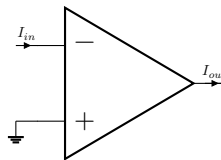
# Ideal Amplifier



Transconductance Amp.

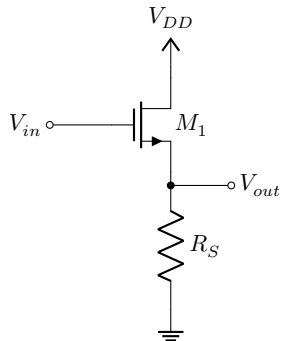


Current Amp.



- For driving a low impedance load, source follower, as a buffer, provides no gain but large input impedance and low output impedance.

# Source Follower



$$V_{DD} - V_{out} = V_{in1} - V_{out} - V_{TH} \quad (40)$$

$$\rightarrow V_{in1} = V_{DD} + V_{TH} \quad (41)$$

# Source Follower



$$V_{in} < V_{TH} \rightarrow M_1 \text{ Off} \quad (42)$$

$$V_{out} = 0 \quad (43)$$

$$V_{in1} > V_{in} > V_{TH} \rightarrow M_1 \text{ in Saturation} \quad (44)$$

$$R_S \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{in} - V_{out} - V_{TH})^2 = V_{out} \quad (45)$$

$$V_{in} > V_{in1} \rightarrow M_1 \text{ in Triode} \quad (46)$$

$$R_S \mu_n C_{ox} \frac{W}{L} \left[ (V_{in} - V_{out} - V_{TH})(V_{DD} - V_{out}) - \frac{1}{2} (V_{DD} - V_{out})^2 \right] = V_{out} \quad (47)$$

# Source Follower



$$V_{in1} > V_{in} > V_{TH} \rightarrow M_1 \text{ in Saturation} \quad (48)$$

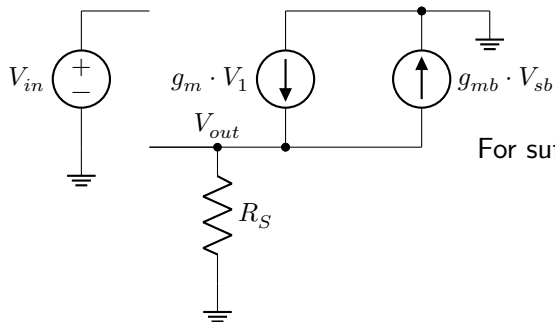
$$R_S \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{in} - V_{out} - V_{TH})^2 = V_{out} \quad (49)$$

$$R_S \frac{1}{2} \mu_n C_{ox} \frac{W}{L} 2 (V_{in} - V_{out} - V_{TH}) \left( 1 - \frac{\partial V_{out}}{\partial V_{in}} - \frac{\partial V_{TH}}{\partial V_{in}} \right) = \frac{\partial V_{out}}{\partial V_{in}} \quad (50)$$

$$R_S \mu_n C_{ox} \frac{W}{L} (V_{in} - V_{out} - V_{TH}) \left( 1 - \frac{\partial V_{out}}{\partial V_{in}} - \frac{\partial V_{TH}}{\partial V_{out}} \frac{\partial V_{out}}{\partial V_{in}} \right) = \frac{\partial V_{out}}{\partial V_{in}} \quad (51)$$

$$A_v = \frac{g_m R_S}{1 + g_m R_S (1 + \eta)} = \frac{g_m R_S}{1 + (g_m + g_{mb}) R_S} \approx \frac{1}{1 + \eta} \quad (52)$$

# Source Follower



For sufficiently large  $V_{in}$ ,  $I_D$  and thus  $g_m$ .

# Source Follower



$$G_m = g_m \quad (53)$$

$$R_{out} = R_S \parallel \left( \frac{1}{gm + g_{mb}} \right) \quad (54)$$

$$A_v = \frac{g_m R_S}{1 + (g_m + g_{mb}) R_S} \approx \frac{1}{1 + \eta} \quad (55)$$

If  $(g_m + g_{mb}) R_S \gg 1$

# Source Follower



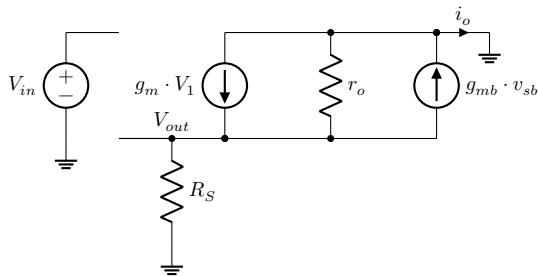
$$\lambda \neq 0, \gamma \neq 0 \quad (56)$$

$$G_m = g_m \quad (57)$$

$$R_{out} = r_o \parallel R_S \parallel \left( \frac{1}{g_m + g_{mb}} \right) \quad (58)$$

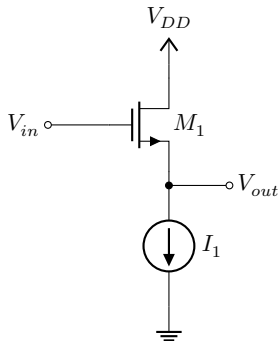
$$A_V = \frac{g_m r_o R_S}{r_o + R_S + (g_m + g_{mb}) r_o R_S} \approx \frac{1}{1 + \eta} \quad (59)$$

If  $(g_m + g_{mb}) r_o R_S \gg r_o$  and  $R_S$





# SF with Current Source



$$A_v = \frac{1}{1 + \eta} \quad (60)$$

If  $\gamma = 0$ ,  $A_v = 1$

# SF with Current Source



$$\frac{1}{2}\mu_n C_{ox} \frac{W}{L} (V_{in} - V_{out} - V_{TH})^2 = I_1 \quad (61)$$

$$\frac{1}{2}\mu_n C_{ox} \frac{W}{L} 2 (V_{in} - V_{out} - V_{TH}) \left( 1 - \frac{\partial V_{out}}{\partial V_{in}} - \frac{\partial V_{TH}}{\partial V_{in}} \right) = 0 \quad (62)$$

$$\mu_n C_{ox} \frac{W}{L} (V_{in} - V_{out} - V_{TH}) \left( 1 - \frac{\partial V_{out}}{\partial V_{in}} - \frac{\partial V_{TH}}{\partial V_{out}} \frac{\partial V_{out}}{\partial V_{in}} \right) = 0 \quad (63)$$

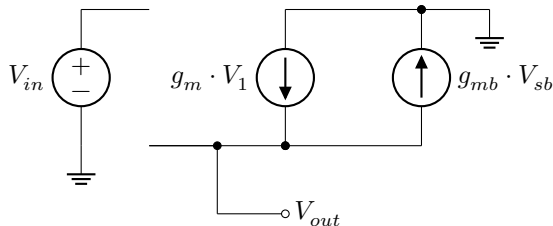
# SF with Current Source



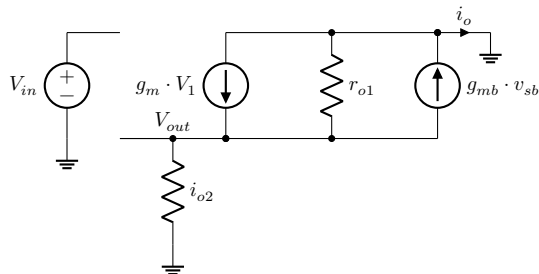
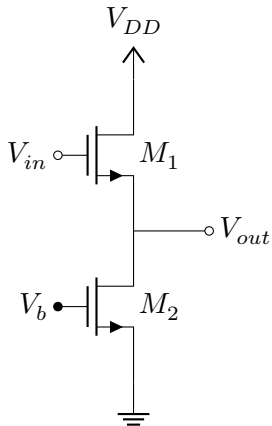
$$G_m = g_m \quad (64)$$

$$R_{out} = \frac{1}{g_m + g_{mb}} \quad (65)$$

$$A_v = \frac{1}{1 + \eta} \quad \text{If } \gamma = 0, A_v = 1 \quad (66)$$



# SF with Current Source



## SF with Current Source



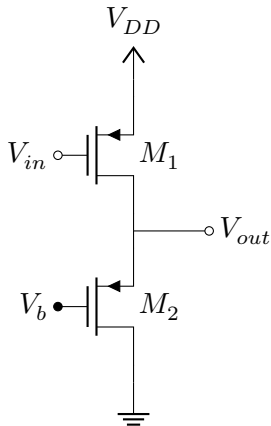
$$G_m = g_{m1} \quad (67)$$

$$R_{out} = r_{o1} \parallel r_{o2} \parallel \left( \frac{1}{g_{m1} + g_{mb1}} \right) \quad (68)$$

$$A_v = \frac{g_m r_{o1} r_{o2}}{r_{o1} + r_{o2} + (g_m + g_{mb}) r_{o1} r_{o2}} \quad (69)$$

If  $r_{o1}$  and  $r_{o2}$  large,  $A_v$  is linear.

# SF with Current Source ( $V_{SB} = 0$ )



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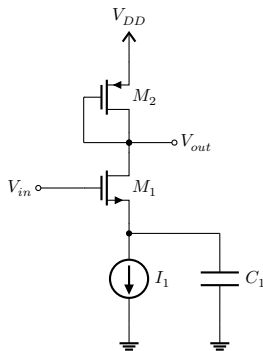
$$G_m = g_{m1} \quad (70)$$

$$R_{out} = r_{o1} \parallel r_{o2} \parallel \frac{1}{g_{m1}} \quad (71)$$

$$A_V = \frac{g_{m1} r_{o1} r_{o2}}{r_{o1} + r_{o2} + g_{m1} r_{o1} r_{o2}} \quad (72)$$

- The sacrifice here is the higher output impedance due to smaller mobility of holes relative to electrons.

# Source Follower as Level Shifter



$$V_{in} \leq V_{DD} - V_{SG2} + V_{TH1} \quad (73)$$

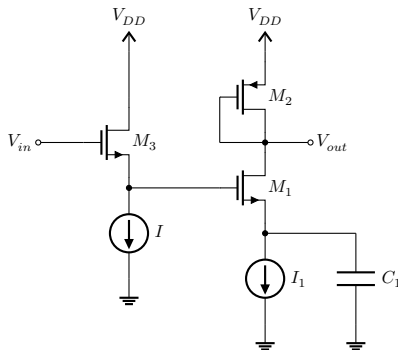
$$G_m = -g_{m1} \quad (74)$$

$$R_{out} = r_{o1} \parallel r_{o2} \parallel \frac{1}{g_{m2}} \quad (75)$$





# Source Follower as Level Shifter



$$V_{in} - V_{GS3} \leq V_{DD} - V_{SG2} + V_{TH1} \quad (76)$$

$$G_{m(left)} = gm_3 \quad (77)$$

$$R_{out(left)} = r_{o3} \parallel \frac{1}{g_{m3} + g_{mb3}} \quad (78)$$

$$R_{in(right)} = \infty \quad (79)$$

$$G_{m(right)} = -g_{m1} \quad (80)$$

$$R_{out(right)} = r_{o1} \parallel r_{o2} \parallel \frac{1}{g_{m2}} \quad (81)$$