Normal World			Switch Gate	HyperPS World	

Kernel Code	RX
Kernel Data	RW
HyperPS Code	RX
HyperPS Data	RW
Switch Gate	RX

Entry gate:
1 save kernel state
2 clear interrupt
3 write cr3
4 clear interrupt
5 jump to HyperPS World
Shared buffer:
common data and code
Exit gate:
1 write cr3
2 set interrupt
3 recover kernel state
4 jump to Normal World

Kernel Code	R
Kernel Data	RW
HyperPS Code	RX
HyperPS Data	RW
Switch Gate	RX

Normal World   Switch Gate   HyperPS World   unmapped