

Kernel Code	RX
Kernel Data	RW
HyperMI Code	RX
HyperMI Data	RW
Switch Gate	RX

Normal World

Entry gate:
 1 save kernel state
 2 clear interrupt
 3 write cr3
 4 flush TLB
 5 clear interrupt
 6 jump to HyperMI world

Shared buffer:
 common data and code

Exit gate:
 1 flush TLB
 2 write cr3
 3 set interrupt
 4 recover kernel state
 5 jump to normal world

Switch Gate

Kernel Code	R
Kernel Data	RW
HyperMI Code	RX
HyperMI Data	RW
Switch Gate	RX

HyperMI World



unmapped