Kernel Code	RX			Kernel Code	R
Kernel Data	RW			Kernel Data	RW
HyperMI Code	RX			HyperMI Code	RX
HyperMI Data	RW			HyperMI Data	RW
Switch Gate	RX			Switch Gate	RX

Entry Gate:
1 Save kernel state
2 Clear interrupt
3 Write cr3
4 Flush TLB
5 Clear interrupt
6 Jump to HyperMI world

Shared Buffer:
Common data and code

Exit Gate:
1 Flush TLB
2 Write cr3
3 Set interrupt
4 Recover kernel state
5 Jump to normal world

Normal World   Switch Gate   HyperMI World

Unmapped