Kernel Code	RX
Kernel Data	RW
HyperPS Code	RX
HyperPS Data	RW
Switch Gate	RX

Normal Space

Entry Gate:
1. Save kernel state
2. Clear interrupt
3. Write cr3
4. Clear interrupt
5. JMP to HyperPS Space

Shared buffer:
Common data and code

Exit gate:
1. Write cr3
2. Set interrupt
3. Recover kernel state
4. JMP to normal world

Switch Gate

Kernel Code	R
Kernel Data	RW
HyperPS Code	RX
HyperPS Data	RW
Switch Gate	RX

HyperPS Space

unmapped