RX	Entry gate: 1 save kernel state
RW	<ul><li>2 clear interrupt</li><li>3 write cr3</li><li>4 clear interrupt</li><li>5 jump to HyperPS wor</li></ul>
RX	Shared buffer: common data and code
RW	Exit gate: 1 write cr3 2 set interrupt
RX	3 recover kernel state 4 jump to normal world
	RW

Kernel Code	R
Kernel Data	RW
HyperPS Code	RX
HyperPS Data	RW
Switch Gate	RX

Normal World

Switch Gate

to HyperPS world

HyperPS World



unmapped