Kernel Code	RX	Entry gate: 1 save kernel state
Kernel Data	RW	<ul><li>2 clear interrupt</li><li>3 write cr3</li><li>4 clear interrupt</li><li>5 jump to HyperPS world</li></ul>
HyperPS Code	RX	Shared buffer: common data and code
HyperPS Data	RW	Exit gate: 1 write cr3 2 set interrupt
Switch Gate	RX -	3 recover kernel state 4 jump to normal world

Kernel Code	R
Kernel Data	RW
HyperPS Code	RX
HyperPS Data	RW
Switch Gate	RX

Normal World

Switch Gate

HyperPS World



unmapped