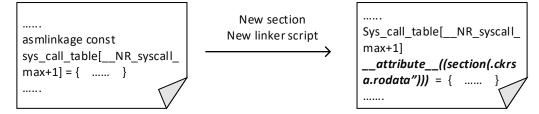
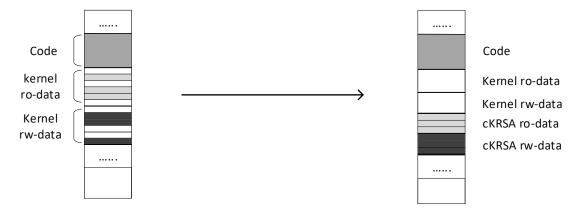


## Source Code

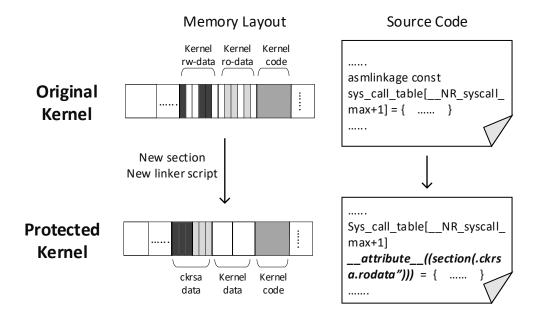


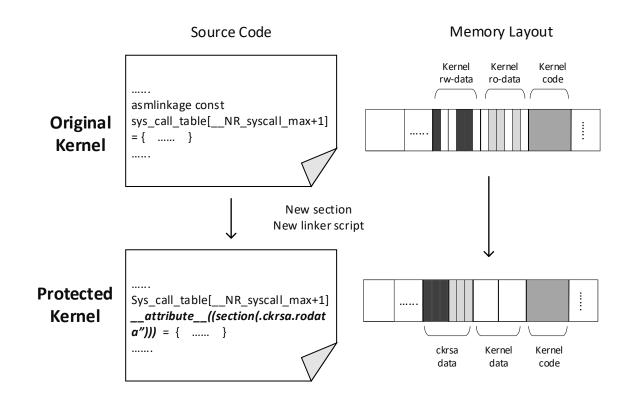
## Memory Layout

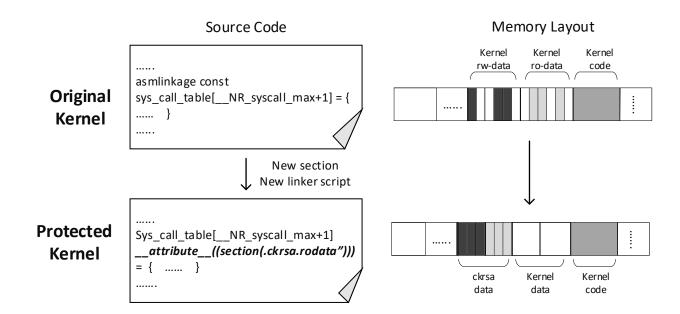


**Original Kernel** 

**Protected Kernel** 







Object Address					
	Reserve	RWX	Handle code		

31			0		
Object Physical Address					
Enable Bit	Reserve	permission	Handle Code		
63 6	2 3	8 3	36 32		



