

# EOS Lab2-Xilinx ZC702

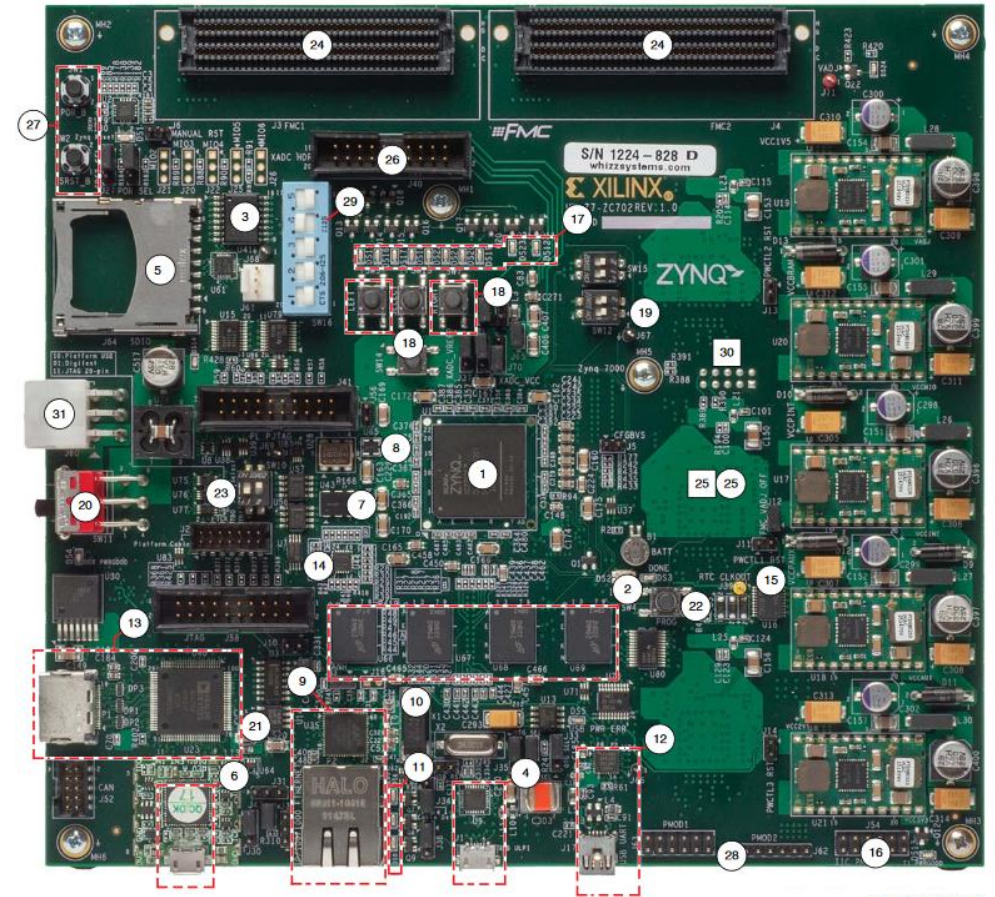
2018 Fall

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# Features

- Zynq-7000 SoC
- ARM Cortex-A9 CPU
- 1GB DDR3 memory
- Quad-SPI Flash Memory 128Mb
- JTAG interface
- USB-to-UART bridge
- HDMI codec
- .....

[https://www.xilinx.com/support/documentation/boards\\_and\\_kits/zc702\\_zvi\\_k/ug850-zc702-eval-bd.pdf](https://www.xilinx.com/support/documentation/boards_and_kits/zc702_zvi_k/ug850-zc702-eval-bd.pdf)



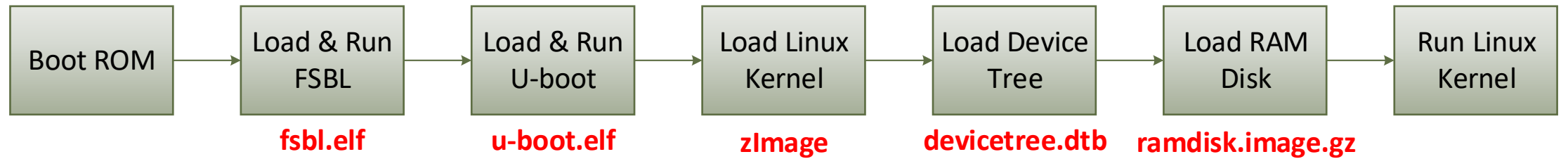
UG850\_c1.02.032013

Figure 1-2: ZC702 Board Component Locations

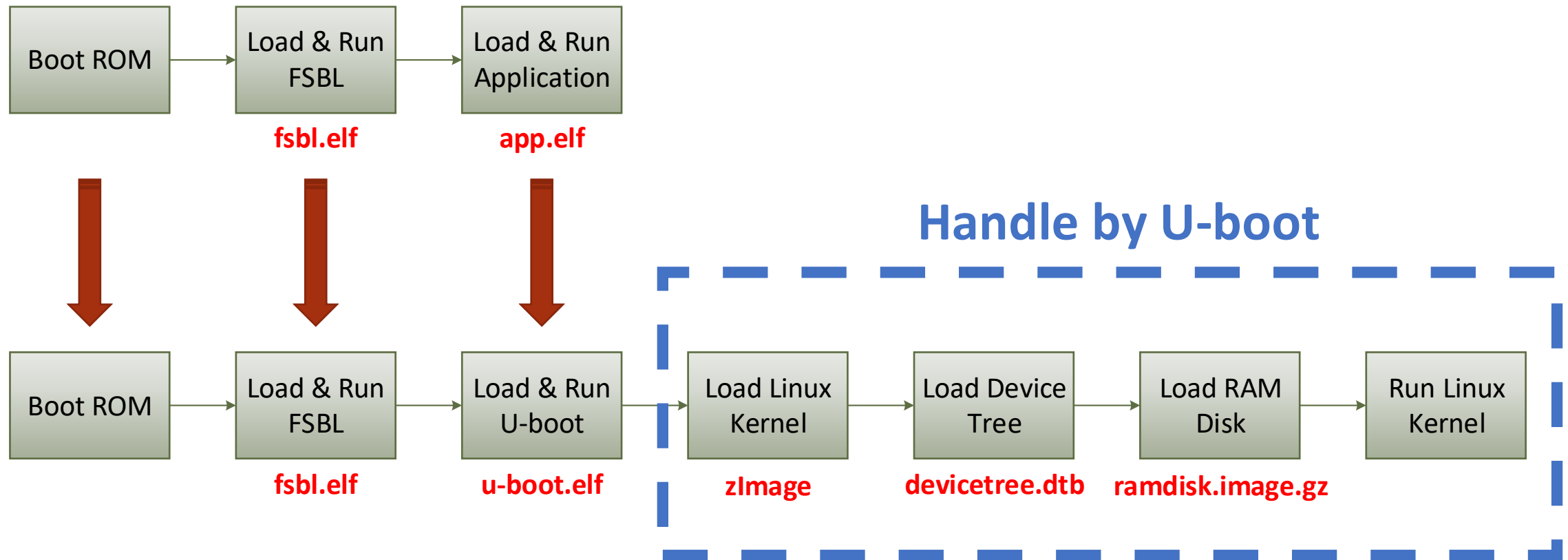
# Running a Standalone Application



# Running Linux



# Comparison



# Boot Image

- fsbl.elf

- u-boot.elf



**boot.bin**

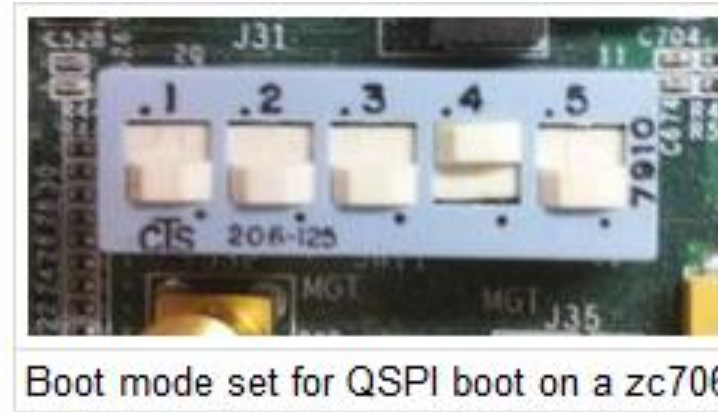
- ulmage

- devicetree.dtb

- uramdisk.image.gz

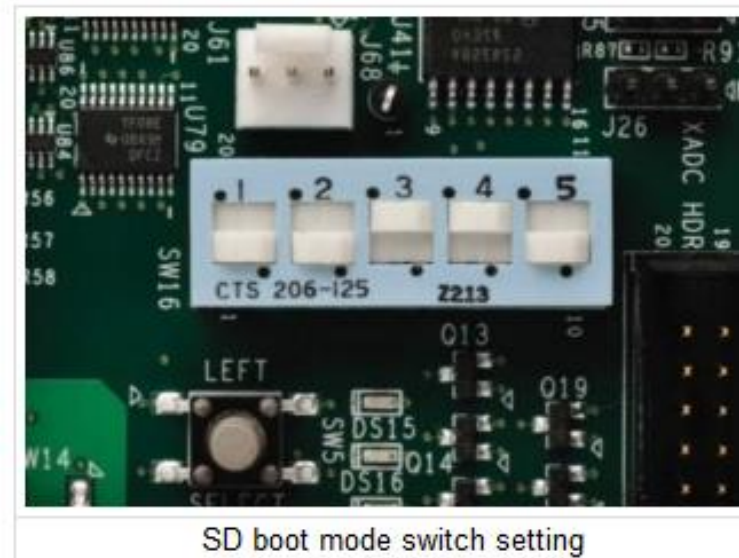
# Boot Medium

- SD card
- Quad-SPI Flash
  - via u-boot (Used in this lab)
  - via Linux Driver
- JTAG



Additionally the correct JTAG mode has to be selected, as

JTAG Mode	Switch 0	Switch 1
Invalid	off	off
Digilent USB JTAG	off	on
Xilinx Platform Cable	on	off
20-pin Header	on	on



# Connection

