GPIO Controller

Word Offset	Byte Offset	Bit 31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		Mode
0	0	X	X	PIN 9			PIN 8			PIN 7			PIN 6			PIN 5			PIN 4			PIN 3			PIN 2			PIN 1			PIN 0			FUNCTION 000-input 001-output	R/W
1	4	X	X	PIN 19			PIN 18			PIN 17			PIN 16			PIN 15			PIN 14			PIN 13			PIN 12			PIN 11			PIN 10				
2	8	X	X	PIN 29			PIN 28			PIN 27			PIN 26			PIN 25			PIN 24			PIN 23			PIN 22			PIN 21			PIN 20				
3	12	X	X	PIN 39			PIN 38			PIN 37			PIN 36			PIN 35			PIN 34			PIN 33			PIN 32			PIN 31			PIN 30				
4	16	X	X	PIN 49			PIN 48			PIN 47			PIN 46			PIN 45			PIN 44			PIN 43			PIN 42			PIN 41			PIN 40				
5	20	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	PIN 53			PIN 52			PIN 51			PIN 50				
6	24	Reserved																																	-
7	28	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	SET	W
8	32	X	X	X	X	X	X	X	X	X	X	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32		
9	36	Reserved																																	-
10	40	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	CLEAR	W
11	44	X	X	X	X	X	X	X	X	X	X	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32		
12	48	Reserved																																	-
13	52	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	LEVEL	R
14	56	X	X	X	X	X	X	X	X	X	X	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32		
15	60	Reserved																																	-
16	64	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	EVENT DETECT STATUS	R/W
17	68	X	X	X	X	X	X	X	X	X	X	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32		
18	72	Reserved																																	-
19	76	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	RISING EDGE DETECT	R/W
20	80	X	X	X	X	X	X	X	X	X	X	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32		
21	84	Reserved																																	-
22	88	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	FALLING EDGE DETECT	R/W
23	92	X	X	X	X	X	X	X	X	X	X	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32		
24	96	Reserved																																	-
25	100	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	HIGH DETECT	R/W
26	104	X	X	X	X	X	X	X	X	X	X	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32		
27	108	Reserved																																	-
28	112	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	LOW DETECT	R/W
29	116	X	X	X	X	X	X	X	X	X	X	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32		
30	120	Reserved																																	-

31	124	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	ASYNC RISING EDGE	R/W
32	128	X	X	X	X	X	X	X	X	X	X	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32		
33	132	Reserved																																	-
34	136	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	ASYNC FALLING EDGE	R/W
35	140	X	X	X	X	X	X	X	X	X	X	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32		
36	144	Reserved																																	-
37	148	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	PUD		PULL UP/DOWN ENABLE 00=off 01=pull down 10=pull up	R/W
38	152	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	PULL UP/DOWN CLOCK	R/W
39	156	X	X	X	X	X	X	X	X	X	X	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32		
40	160	Reserved																																	-