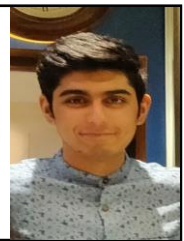


KUSH ARORA

COMPUTER ENGINEERING STUDENT

Perseverant | Performance Oriented | Passionate



EDUCATION

Bachelor of Applied Science,
University of British Columbia
Vancouver

Expected, Jul 2023

SKILLS

C
C++
Java
Python
Javascript / JSX
HTML & CSS
Verilog / System Verilog
Assembly

SOFTWARE TOOLS & FRAMEWORKS

React Native
Firebase
GitHub
Visual Studio Code
IntelliJ IDEA
PyCharm
Microsoft Office

AWARDS

Trek Excellence Scholarship
- 2019

Dean's Honor List
- 2019

CONTACT

kusharora339@gmail.com

Tel. No.: +918527478225

PROFILE

Final Year Student with a robust foundation in object-oriented programming, data structures, computer architecture and hardware system design

TECHNICAL WORK EXPERIENCE

Software Intern, Seahawk Robotics

Vancouver

May 2021 – August 2021

- Modified existing C++ code base for a drone ground control station to add additional video streams
- Customized a GUI in python to add winch control features for drones

React Native Developer Intern, Scancom India

New Delhi

Jan 2021 – Mar 2021

- Designed and developed a react native product and services app to digitize business Go To Market (GTM)
- Utilized Firebase to add authentication and store user information
- Integrated a chat-bot to automate answering FAQs

Researcher, UBC

Kelowna

Mar 2019 – May 2019

- Developed techniques to smoothen the process for 3D printing of concrete houses
- Identified solutions to enable concrete ejection through a nozzle for 3D

TECHNICAL PROJECTS

Operating Systems Project, UBC

Sep 2020 – Dec 2020

- Learned key OS concepts : Concurrency, Resource virtualization and Protection
- Implemented key parts of the kernel using C : Synchronization primitives, File tables and System calls

Wikipedia and Graph Databases, UBC

Oct 2019 – Nov 2019

- Developed a generic cache using Java with LRU implementation, for faster access of Wikipedia page requests
- Utilized external APIs for data processing
- Completed unit testing of the cache for correctness

Graph ADT Development, UBC

Oct 2019 – Nov 2019

- Developed a graph ADT by implementing vital graph algorithms in Java to find the shortest path between two vertices
- Developed crucial debugging skills

RISC Machine, UBC

Oct 2019 – Nov 2019

- Developed a Turing - complete RISC (Reduced Instruction Set Computer) using the ARM Instruction Set Architecture
- Fabricated a functional CPU, by integrating Verilog components