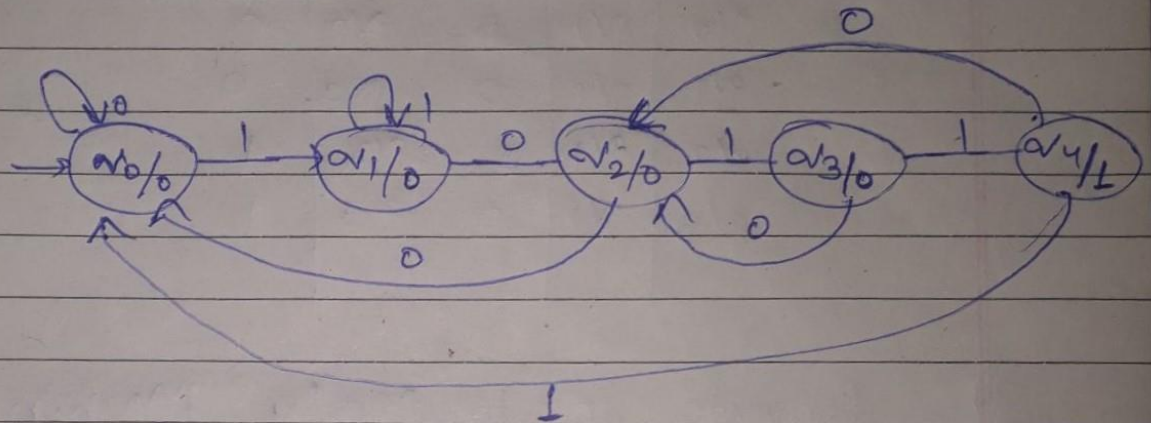


Tutorial 4

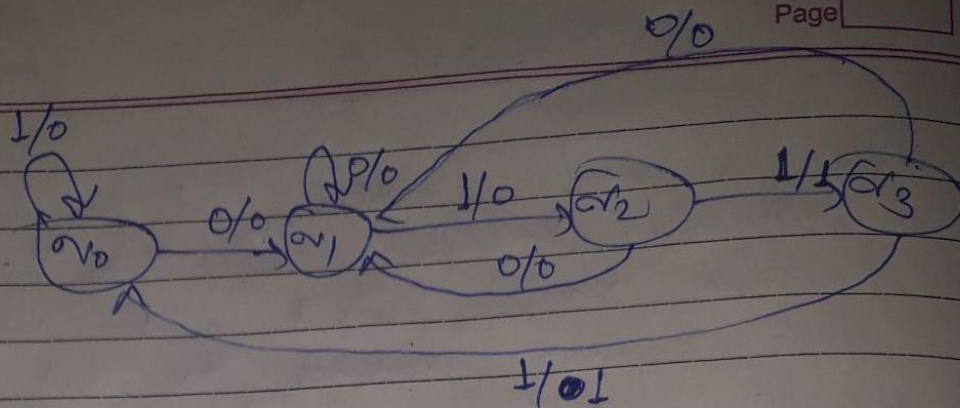
- 1) Design a moore machine that function as a pattern recognizer for "1011". Your machine should output a '1' whenever this pattern matches the last four output inputs, and there has been no overlap, otherwise output a '0'.

Ans

	0	1	output
q <sub>0</sub>	q <sub>0</sub>	q <sub>1</sub>	0
q <sub>1</sub>	q <sub>2</sub>	q <sub>1</sub>	0
q <sub>2</sub>	q <sub>2</sub>	q <sub>3</sub>	0
q <sub>3</sub>	q <sub>3</sub>	q <sub>4</sub>	0
q <sub>4</sub>	q <sub>0</sub>	q <sub>2</sub>	1

- 2) Construct a Mealy machine for a simple sequence detector for the sequence 011. Include three outputs that indicate how many bits have been received in the correct sequence.





Input 0			Input 1		
State	State	out	State	State	out
$s_0$	$s_1$	0	$s_0$	$s_0$	0
$s_1$	$s_1$	0	$s_2$	$s_2$	0
$s_2$	$s_1$	0	$s_3$	$s_3$	1
$s_3$	$s_1$	0	$s_0$	$s_0$	1

3) Convert in to Mealy Machine.

Ans

moore  $\rightarrow$

	a	b	out
$\rightarrow$ A	C	D	1
B	D	C	0
C	B	C	1
D	C	B	1

Mealy  $\rightarrow$

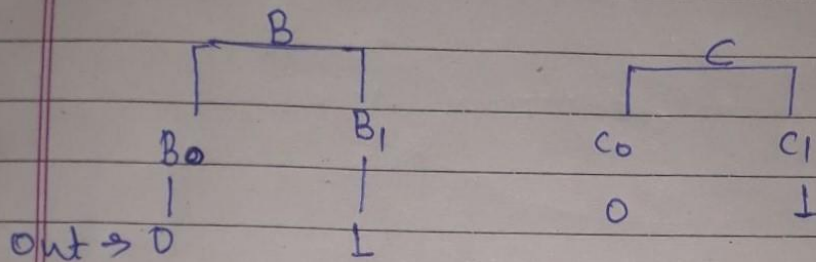
	a	out	b	out
$\rightarrow$ A	C	1	D	1
B	D	1	C	1
C	B	0	C	1
D	C	1	B	0

4)  
Ans

Convert in to Moore machine.

Mealy →

	0	out	1	out
→ A	A	1	B	0
B	D	1	D	1
C	B	1	C	1
D	C	0	A	1



	0	out	1	output
A	A	1	B <sub>0</sub>	0
B <sub>0</sub>	D	0	D	0
B <sub>1</sub>	D	1	D	1
C <sub>0</sub>	B <sub>1</sub>	1	C <sub>1</sub>	1
C <sub>1</sub>	B <sub>1</sub>	1	C <sub>1</sub>	1
D	C <sub>0</sub>	0	A	1

Moore machine

	0	1	Final output
A	A	B <sub>0</sub>	1
B <sub>0</sub>	D	D	0
B <sub>1</sub>	D	D	1
C <sub>0</sub>	B <sub>1</sub>	C <sub>1</sub>	0
C <sub>1</sub>	B <sub>1</sub>	C <sub>1</sub>	1
D	C <sub>0</sub>	A	1