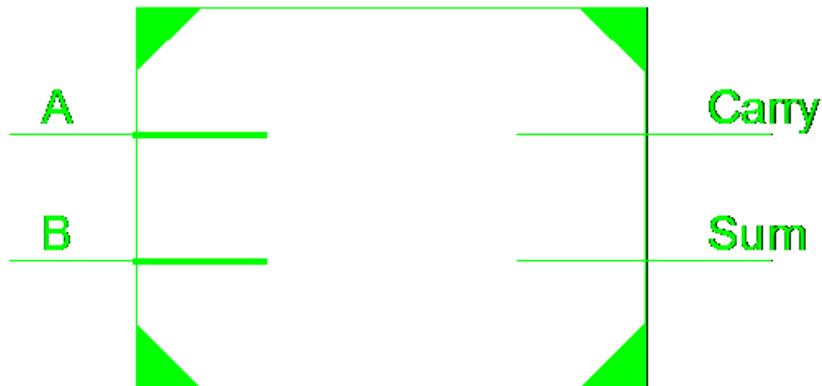


# OUTPUT

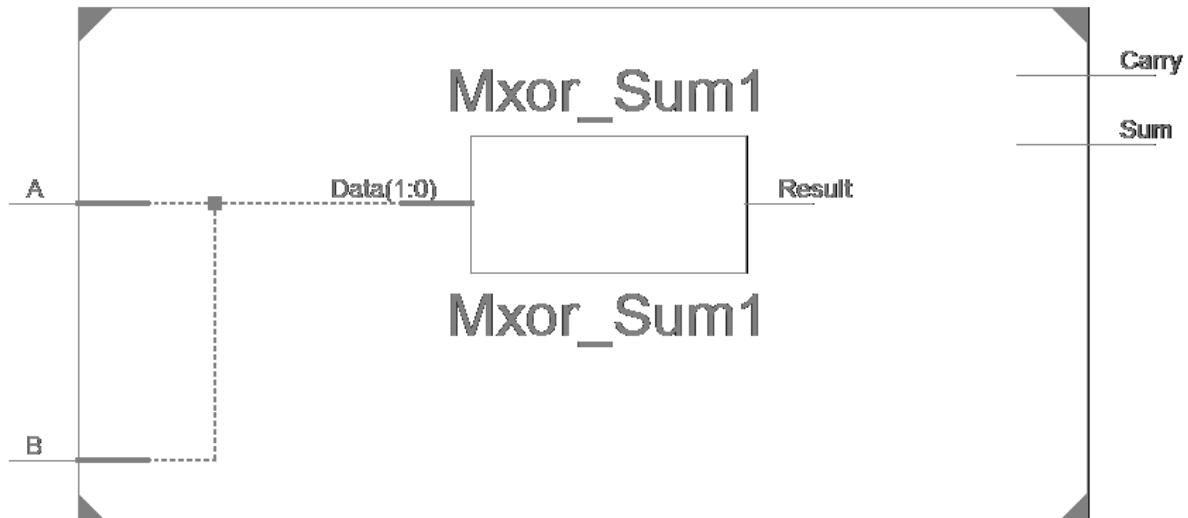
## Exp01\_HalfAdder



## Exp01\_HalfAdder

Figure 1: Half Adder RTL Schematic Block

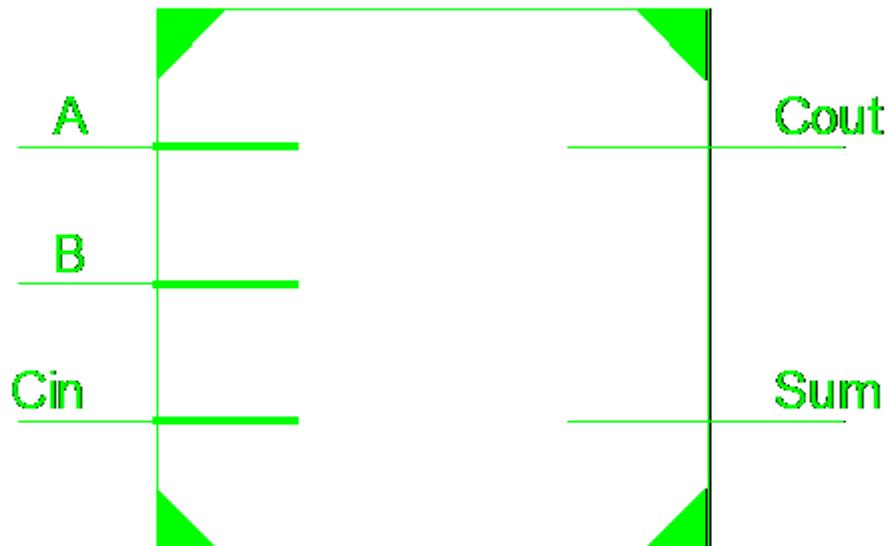
## Exp01\_HalfAdder:1



## Exp01\_HalfAdder

Figure 2: Half Adder RTL Schematic Diagram

# Exp02\_FullAdder



# Exp02\_FullAdder

Figure 3: Full Adder RTL Schematic Block

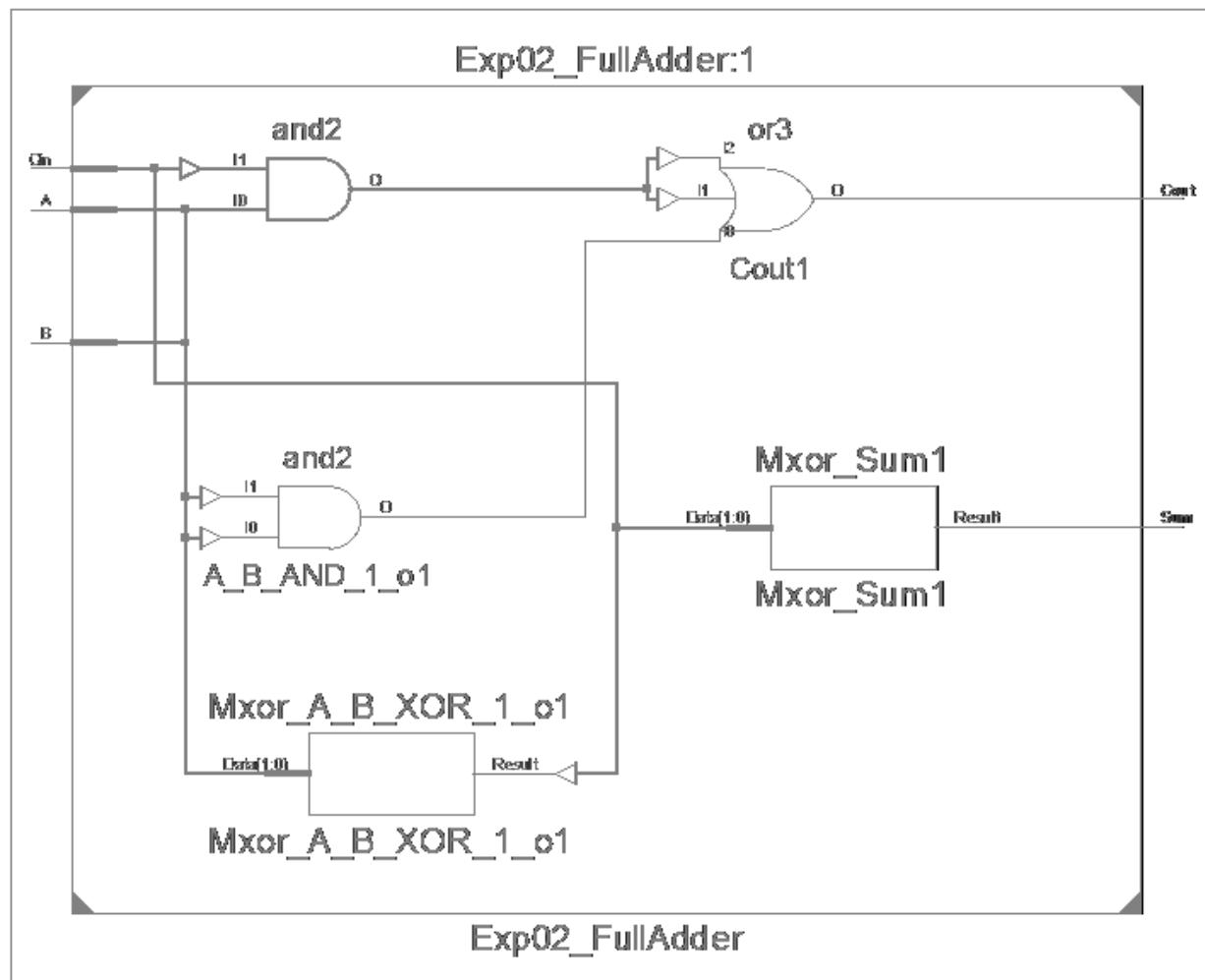


Figure 4: Full Adder RTL Schematic Diagram

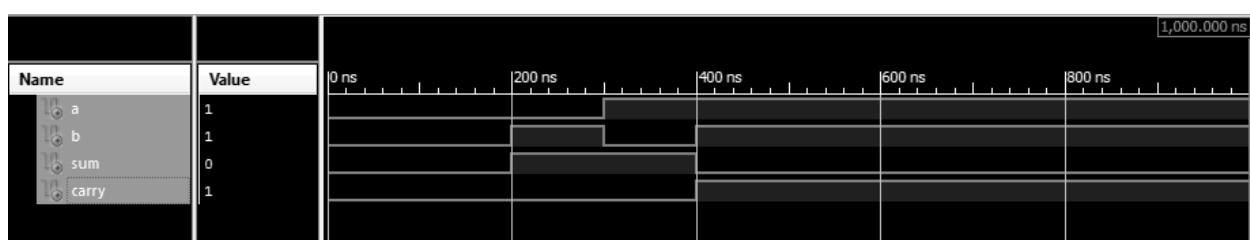


Figure 5: Test Bench of Half Adder

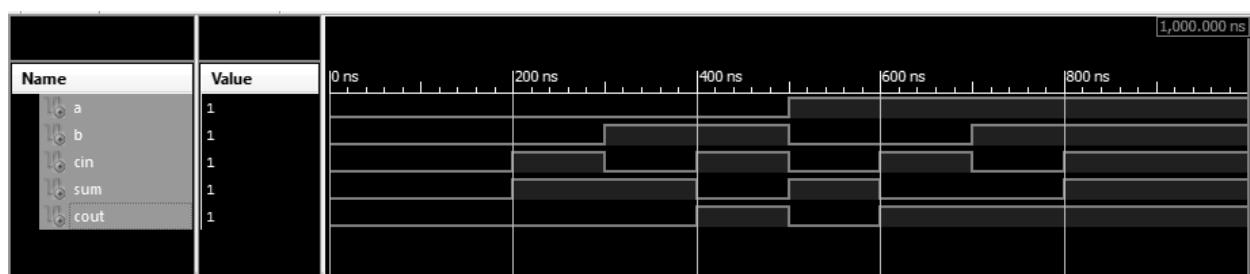


Figure 6: Test Bench of Full Adder