OUTPUT

Exp01_4BitAdder

A(3:0)

B(3:0)

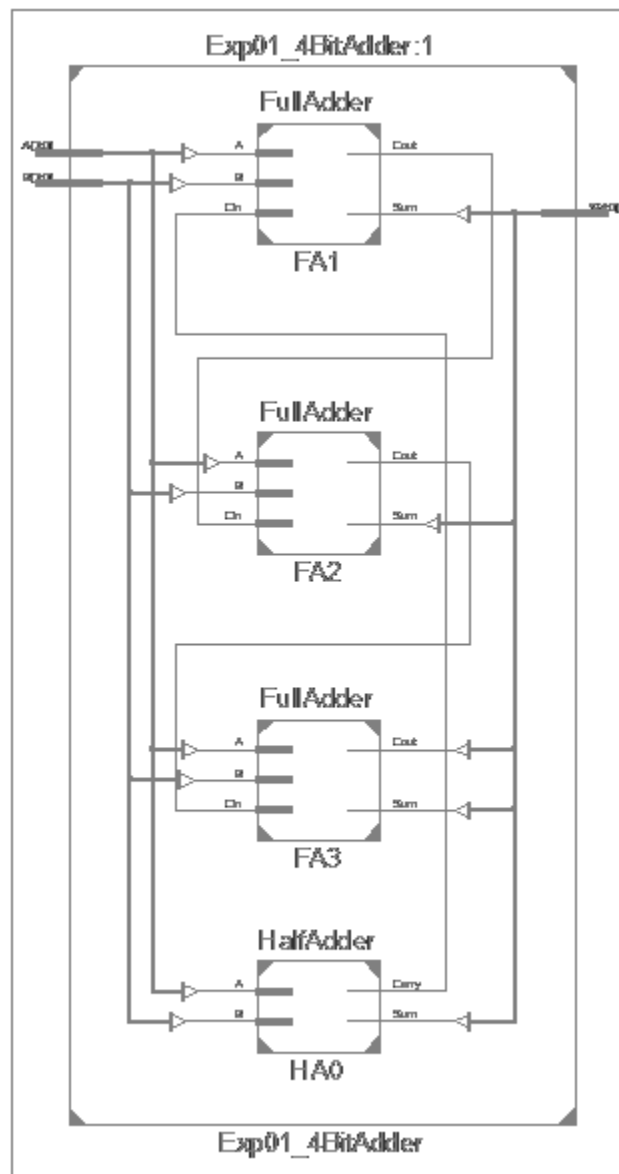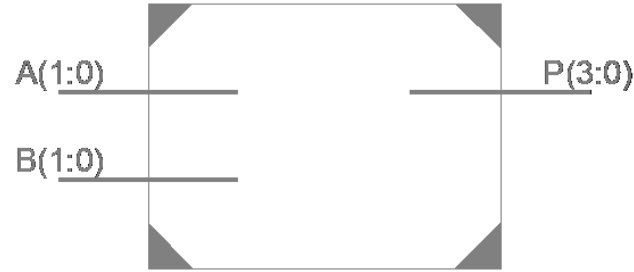S(4:0)

Exp01_4BitAdder

Figure 1: 4 Bit Adder Schematic Block

Exp01_4BitAdder:1

FullAdder

A   Cout
B
Cin   Sum

FA1

FullAdder

A   Cout
B
Cin   Sum

FA2

FullAdder

A   Cout
B
Cin   Sum

FA3

HalfAdder

A   Carry
B   Sum

HA0

Exp01_4BitAdder

Figure 2: 4 Bit Adder RTL Schematic Diagram
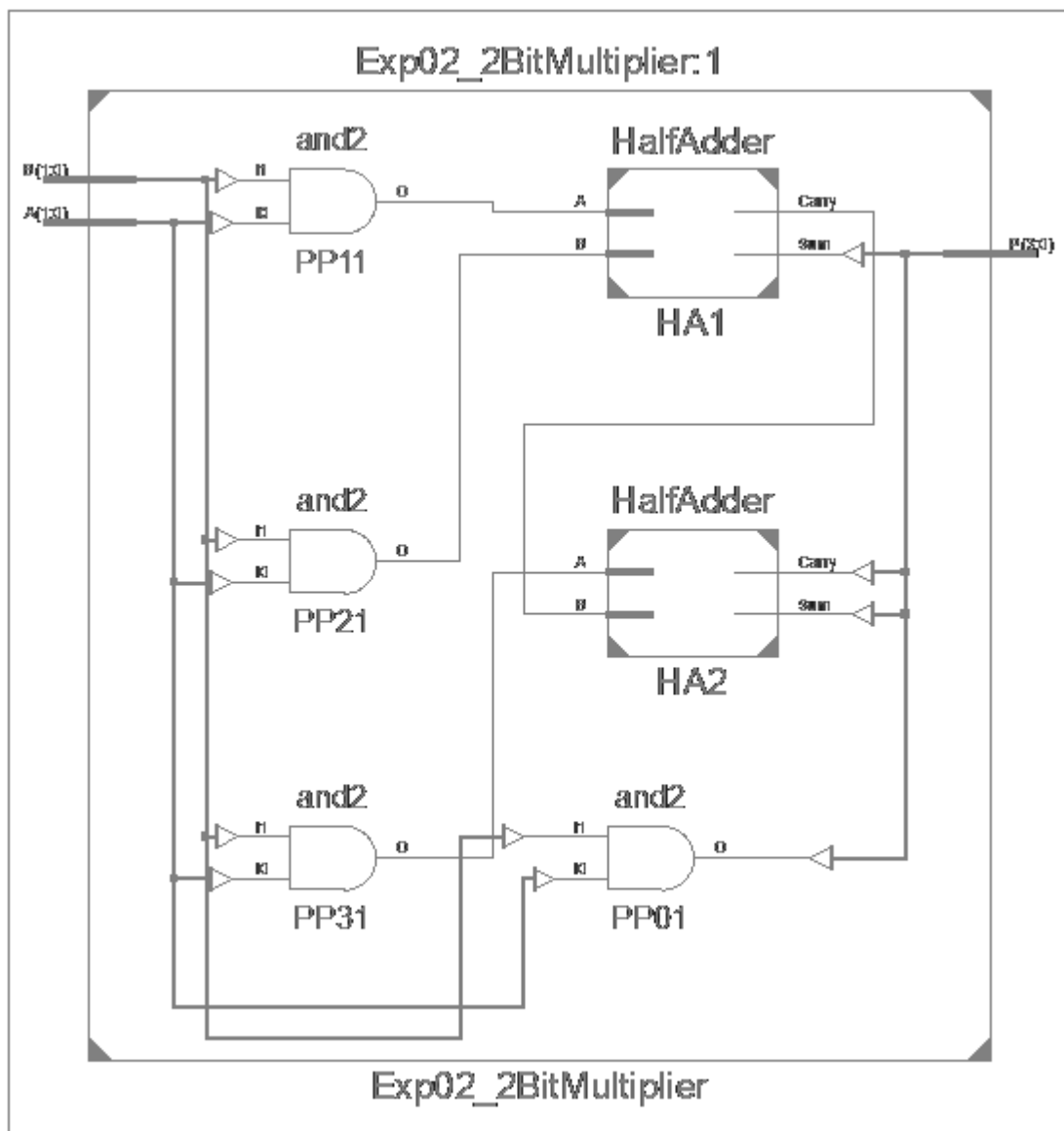
Figure 3: 2 Bit Multiplier RTL Schematic Block

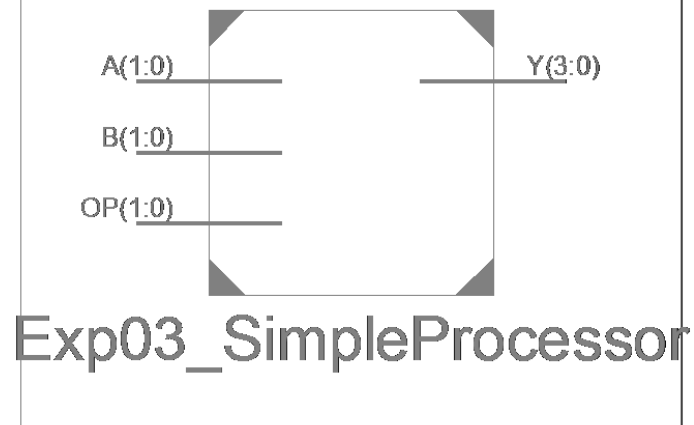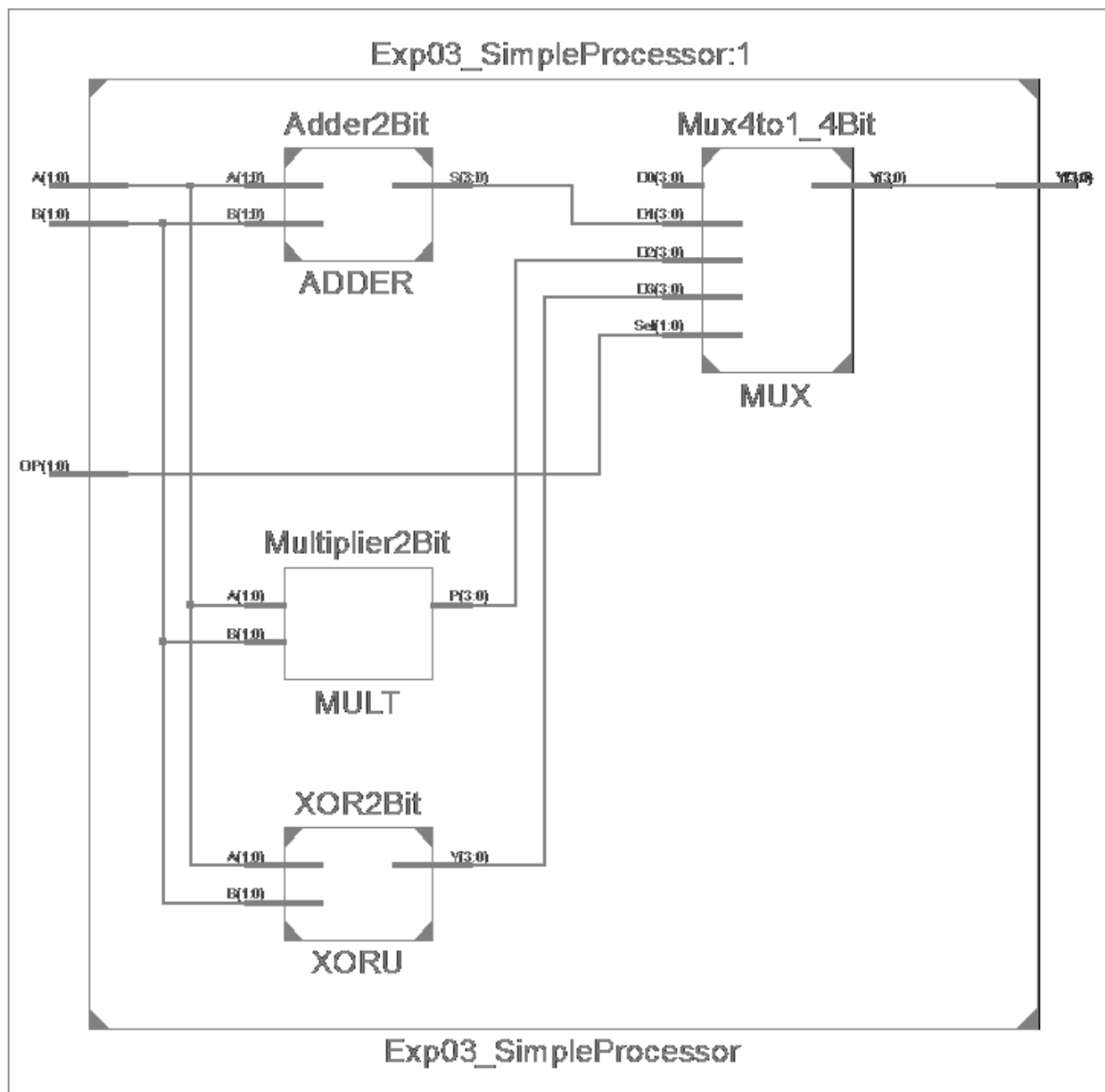*Figure 4: 2 Bit Multiplier RTL Schematic Diagram*

Figure 5: Simple Processor Block

Figure 6: Simple Processor RTL Schematic Diagram