# Part III Synchronization Software and Hardware Solutions

Computers are useless. They can only give answers.

# Software Solutions for Two Processes

- Suppose we have two processes  $P_0$  and  $P_1$ .
- Let one process be  $P_i$  and the other be  $P_j$ , where j = 1 i. Thus, if i = 0, then j = 1 and if i = 1, then j = 0.
- We will design enter-exit protocols for a critical section to ensure mutual exclusion.
- We will go through a few unsuccessful attempts and finally yield a correct one.
- These solutions are pure software-based.

## An Important Assumption: 1/3

- We have the following assumption\*:
  - Inspecting the current value of a shared variable and assigning a new value to such a shared variable are to be regarded as indivisible, non-interfering actions (i.e., atomic).

## An Important Assumption: 2/3

#### • What does this mean?

- ➤ When two processes assign a new value to the same shared variable simultaneously, the assignments are done sequentially.
- When a process checks the value of a shared variable with an assignment to it by the other one, the former process will find either the old or the new value.
- >These variables could be in registers.
- However, expression evaluation is NOT atomic.

## An Important Assumption: 3/3

#### Co-operating Sequential Processes

#### E. W. DIJKSTRA

Department of Mathematics, Technological University, Eindhoven, The Netherlands

Introduction		. 43
1. ON THE NATURE OF SEQUENTIAL PROCESSES		. 44
2. LOOSELY CONNECTED PROCESSES		. 52
2.1. A Simple Example		. 53
2.2. The Generalized Mutual Exclusion Problem		. 59
2.3. A Linguistic Interlude		. 62
3. THE MUTUAL EXCLUSION PROBLEM REVISITED		. 65
3.1 The Need for a More Realistic Solution		. 65
3.2. The Synchronizing Primitives		. 67
3.3. The Synchronizing Primitives Applied to the Mutual Exclusion	Problem	
4. THE GENERAL SEMAPHORE		. 69
4.1. Typical Uses of the General Semaphore		
4.2. The Superfluity of the General Semaphore		. 72
4.3. The Bounded Buffer		. 75
5. Co-operation via Status Variables		. 76
5.1. An Example of a Priority Rule		. 77
5.2. An Example of Conversations		. 83
6. THE PROBLEM OF THE DEADLY EMBRACE		. 103
6.1. The Banker's Algorithm		. 105
6.2. The Banker's Algorithm Applied		. 107
7. CONCLUDING REMARKS		. 110

#### INTRODUCTION

This chapter is intended for all those who expect that in their future activities they will become seriously involved in the problems that arise in either the design or the more advanced applications of digital information processing equipment; they are further intended for all those who are just interested in information processing.

The applications are those in which the activity of a computer must include the proper reaction to a possibly great variety of messages that can be sent to it at unpredictable moments, a situation which occurs in process control, traffic control, stock control, banking applications, automatization of information flow in large organizations, centralized computer service, and,

This is Dijkstra's paper.
It was a technical report
before published as
a paper.

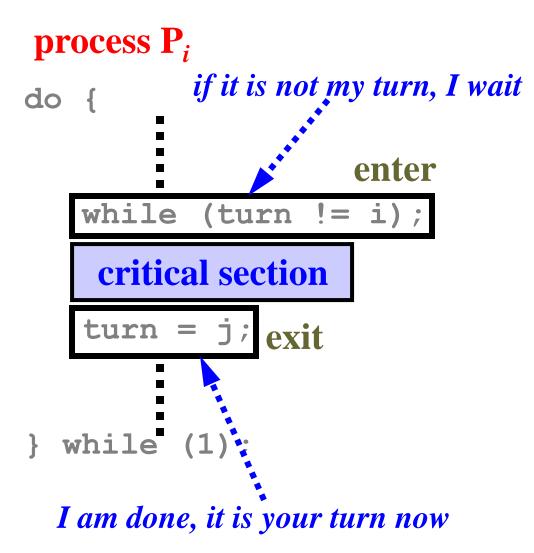
Several Examples will be discussed in terms of:

- (1) Mutual Exclusion,
- (2) Progress,
- (3) Bounded Waiting.

## **A Few More Assumptions**

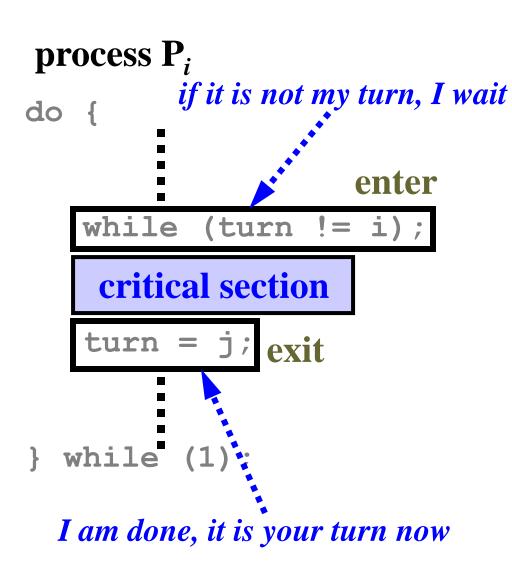
- The following assumptions are made about the behavior of the processes
  - ➤ Nothing is assumed about the remainder code except that it cannot influence the behavior of other processes.
  - > Shared objects in an entry or an exit code may not be referred to in a remainder code of a critical section.
  - ➤ A process cannot fail or loop while executing the entry code, critical section and exit code. Whenever it is scheduled it must take a step.
  - ➤ A process can take only a finite number of steps in its critical section and exit code.
  - ➤ While the collection of processes is concurrent, individual processes are sequential.

## Attempt I: 1/3



- Shared variable turn, initialized to i or j, controls who can enter the critical section.
- Since turn is either i or j, only one can enter.
- However, processes are forced to run in an alternating way.
- Not good!

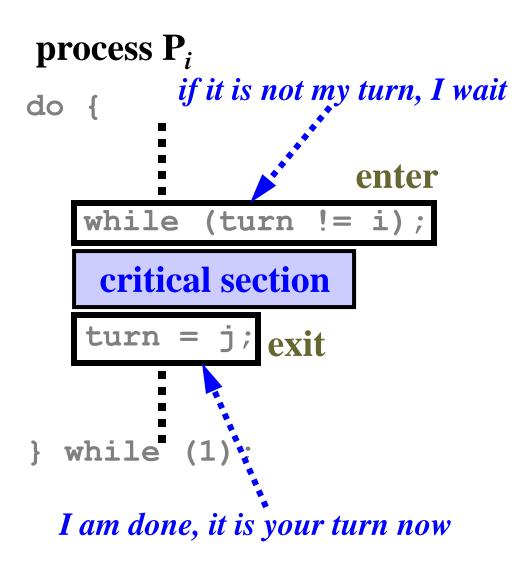
# Attempt I: 2/3



### Mutual Exclusion

- P<sub>i</sub> in its CS if turn=i.
- $P_j$  in its CS if turn=j.
- If P<sub>i</sub> and P<sub>j</sub> are BOTH in their CSs, then turn=i and turn=j must BOTH be true.
- This is absurd, because a variable can only hold one and only one value (i.e., cannot hold both i and j) at any time.

# Attempt I: 3/3



## Progress

- If P<sub>i</sub> sets turn to j on exit and will not use the critical section for some time, P<sub>j</sub> can enter but cannot enter again.
- An irrelevant process blocks other processes from entering a critical section. Not good!
- Does bounded waiting hold? Exercise!

Bound = ?

# Attempt II: 1/4

```
bool
       flag[2] = FALSE;
        I am interested
do {
              wait for you
                     enter
    flag[i] = TRUE;
     critical section
                       exit
                 FALSE;
          I am not interested
```

- Shared variable flag[i] is the "state" of process P<sub>i</sub>: interested or not-interested.
- P<sub>i</sub> indicates its intention to enter, waits for P<sub>j</sub> to exit, enters its section, and, finally, changes to "I am out" upon exit.

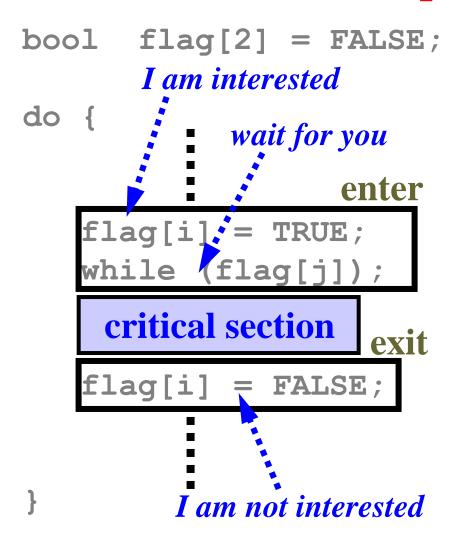
## Attempt II: 2/4

```
bool
        flag[2] = FALSE;
        I am interested
do {
              wait for you
                     enter
    flag[i] = TRUE;
     critical section
                       exit
                 FALSE;
          I am not interested
```

### Mutual Exclusion

- P<sub>i</sub> is in CS if flag[i] is TRUE AND flag[j] is FALSE.
- P<sub>j</sub> is in CS if flag[j] is TRUE AND flag[i] is FALSE.
- If both are in their CSs, flag[i] and flag[j] must be both TRUE and FALSE at the same time.
- This is absurd.

## Attempt II: 3/4



## Progress

- If both P<sub>i</sub> and P<sub>j</sub> set flag[i] and flag[j] to TRUE at the same time, then both will loop at the while forever and no one can enter.
- A decision cannot be made in finite time (i.e., not deadlock-free).

## Attempt II: 4/4

```
bool
        flag[2] = FALSE;
        I am interested
do {
              wait for you
                     enter
    flag[i] = TRUE;
     critical section
                       exit
                 FALSE;
          I am not interested
```

- Suppose  $P_j$  is in its critical section and  $P_i$  is waiting to enter.
- If P<sub>i</sub> fails to detect the change of flag[j] when P<sub>j</sub> exits, P<sub>j</sub> can come back fast before P<sub>i</sub> can check flag[j] again, and set flag[j] to TRUE. Then, no one can enter.
- We need to do more in the while.

## Attempt III: 1/6

Consider the algorithm below:

```
Process 0: P<sub>0</sub>
                                 Process 1: P<sub>1</sub>
      flag[0] = TRUE;
                                 flag[1] = TRUE;
      while (flag[1]) {
                                while (flag[0]) {
         flag[0] = TRUE; \leftarrow ---
                                 ---flag[1] = TRUE;
                    interested again if you are not
      in critical section
      flag[0] = FALSE;
                               flag[1] = FALSE;
       Then, set myself to interested again and loop back
     Wait while you are interested
                               flags are initialized to FALSE
   Set myself to not-interested
                                                     14
  While you are interested, do the following:
I am interested
```

## Attempt III: 2/6

```
flag[i] = TRUE;
while (flag[j])
   flag[i] = FALSE;
   while (flag[j])
   flag[i] = TRUE;
// critical section
flag[i] = FALSE;
```

#### Mutual Exclusion

- If P<sub>i</sub> is in its critical section, then flag[i] is TRUE and flag[j] is FALSE.
- If both processes are in their critical sections, flag[i] and flag[j] are both TRUE and FALSE.
- Contradiction.

## Attempt III: 3/6

```
flag[i] = TRUE;
while (flag[j]) {
   flag[i] = FALSE;
   while (flag[j])
   flag[i] = TRUE;
// critical section
flag[i] = FALSE;
```

- Progress
- Outsider Issue:
   Suppose P<sub>j</sub> is not entering (i.e., elsewhere) and P<sub>i</sub> is waiting to enter.
- Because flag[j] is FALSE, P<sub>i</sub> enters.

## Attempt III: 4/6

```
flag[i] = TRUE;
while (flag[j]) {
   flag[i] = FALSE;
   while (flag[j])
   flag[i] = TRUE;
// critical section
flag[i] = FALSE;
```

#### Progress

- Finite Decision Time: Suppose P<sub>i</sub> and P<sub>i</sub> are waiting to enter, and the critical section is empty.
- If P<sub>i</sub> and P<sub>j</sub> execute their corresponding statements in a fully synchronized way, both processes loop forever.
- Progress fails. 17

## Attempt III: 5/6

```
flag[i] = TRUE;
while (flag[j]) {
   flag[i] = FALSE;
   while (flag[j])
   flag[i] = TRUE
// critical section
flag[i] = FALSE;
```

## Bounded Waiting

- If after P<sub>i</sub> sets flag[i] to FALSE, then P<sub>j</sub> has a chance to break its outer while and enter.
- After P<sub>j</sub> sets flag[j] to
  FALSE upon exit, P<sub>i</sub> may
  break its inner while.
  However, it is possible
  before it sets flag[i] to
  TRUE, P<sub>j</sub> loops back,
  breaks its outer while,
  and enters.

  18

Bounded waiting also fails.
Find execution sequences yourself

## Attempt III: 6/6

```
1. flag[i] = TRUE;
2. while (flag[j]) {
3.    flag[i] = FALSE;
4.    while (flag[j])
5.    ;
6.    flag[i] = TRUE;
7. }
    // critical section
8. flag[i] = FALSE;
```

P<sub>1</sub> enters twice and can enter again & again

		•			
	$\mathbf{P_0}$	$\mathbf{P}_{1}$	flag[0]	flag[1]	Comment
	Both Proce	sses Start	F	F	
	f[0] = T	f[1] = T	T	T	
•	while(f[1])		T	T	P <sub>0</sub> 's line 2 while
<b>A</b>	f[0] = F	while(f[0])	F	Т	P <sub>1</sub> 's line 2 while
į	while(f[1])	P <sub>1</sub> enters CS	F	T	P <sub>0</sub> loops line 6
į		P <sub>1</sub> exits CS	F	T	
1		f[1] = F	F	F	P <sub>1</sub> resets f[1]
ļ	f[0] = T		Т	F	P <sub>0</sub> 's line 6
ĵ` ▼		f[1] = T	T	T	P <sub>1</sub> comes back
À.	f[0] = F		F	T	P <sub>0</sub> 's next iteration
ľ		while(f[0])	F	T	P <sub>1</sub> 's line 2 while
l	while(f[1])	P <sub>1</sub> enters CS	F	T	P <sub>0</sub> loops line 6
į		P <sub>1</sub> exits CS	F	T	
ï		f[1] = F	F	F	P <sub>1</sub> resets f[1]
i	f[0] = T		T	F	P <sub>0</sub> 's line 6
ţ		f[1] = T	T	T	P <sub>1</sub> comes back

## Attempt IV: 1/10

Variable turn being i or j can be considered as a "scheduler":

```
Bool flag[2] = { FALSE, FALSE };
                     // initialized to i or j
int
     turn;
Process i
flag[i] = TRUE;
                     // I am interested
if (turn == j) {     // If you are, is it your turn?
     flag[i] = FALSE; // it is your turn, not interested
     while (turn == j) // wait until it is not your turn
     flag[i] = TRUE;
                     // I am interested AGAIN
                      // Then, loop back and retry!
Critical Section
turn = i;
                     // upon exit, you have the turn
                         and I am not interested
flag[i] = FALSE;
```

## Attempt IV: 2/10

```
flag[2] = { FALSE, FALSE };
Bool
int
      turn;
Process i
                    turn is NOT used
flag[i] = TRUE;
while (flag[j]) {
  if (turn == j) {
      flag[i] = FALSE;
      while (turn == j)
      flag[i] = TRUE;
Critical Section
turn = j;
flag[i] = FALSE;
```

#### Mutual Exclusion: 1

- If flag[j] is FALSE, P<sub>i</sub> enters immediately.
- If flag[j] is TRUE, P<sub>i</sub> enters the while.
- At the end of the while, flag[i] is reset to TRUE.
- Thus, if P<sub>i</sub> enters, we have flag[i]=TRUE and flag[j]=FALSE.
- turn does not play a role as its value does not affect who can enter.

## Attempt IV: 3/10

```
Bool flag[2] = { FALSE, FALSE };
int
      turn;
Process i
flag[i] = TRUE;
while (flag[j]) {
   if (turn == j) {
      flag[i] = FALSE;
      while (turn == j)
      flag[i] = TRUE;
Critical Section
turn = j;
flag[i] = FALSE;
```

#### Mutual Exclusion: 2

- P<sub>i</sub> enters, flag[i] =
  TRUE and flag[j] =
  FALSE.
- P<sub>j</sub> enters, flag[j] =
  TRUE and flag[i] =
  FALSE.
- If P<sub>i</sub> and P<sub>j</sub> are in the critical section, flag[i] and flag[j] are both TRUE and FALSE.
- This is <u>impossible</u> and mutual exclusion holds.

## Attempt IV: 4/10

```
flag[2] = { FALSE, FALSE };
 Bool
 int
       turn;
 Process i
 flag[i] = TRUE;
 while (flag[j]) {
    if (turn == j) {
       flag[i] = FALSE;
       while (turn ==
       flag[i] = TRUE;
 Critical Section
turn = j;
 flag[i] = FALSE;
```

- Progress: 1
- Outsider Issue
- If P<sub>i</sub> is entering and P<sub>j</sub> is not, then P<sub>j</sub> has set turn to false.
- In this case, P<sub>i</sub> reaches the while and enters the critical section.
- turn is NOT used.
- Therefore, an outsider will not affect those waiting to enter.

## Attempt IV: 5/10

```
Bool flag[2] = { FALSE, FALSE };
int
      turn;
             P's view
Process i
flag[i] = TRUE;
while (flag[j])
   if (turn == j) {
      flag[i] = FALSE;
     while (turn == j)
      flag[i] = TRUE;
Critical Section
turn = j;
flag[i] = FALSE;
```

- Progress: 2 (turn=j)
- Finite Decision Time
- If P<sub>i</sub> and P<sub>j</sub> are both
  entering, flag[i] =
  flag[j] = TRUE and
  both enter the while.
- If turn = j, P<sub>i</sub> loops

  here after setting flag[i]

  to FALSE.
- This is equivalent to "waiting for my turn (i.e., turn = i)."

## Attempt IV: 6/10

```
Bool
       flag[2] = { FALSE, FALSE };
int
       turn;
              P<sub>i</sub>'s view
Process i
         = TRUE;
while (flag[i])
   if (turn == i)
      flag[j] = FALSE;
      while-(turn == i)
      flag[j] = TRUE;
skipped
Critical Section
turn = i;
flag[j] = FALSE;
```

- Progress: 3 (turn=j)
- Finite Decision Time
- P<sub>j</sub> checks if it is its turn (i.e., turn = j).
- Because turn is j, P<sub>j</sub> loops back to check if flag[i] is FALSE.
- Thus, P<sub>j</sub> loops around while (true) and if (false) until flag[i] is FALSE because turn is j.
- Because  $P_i$  only needs 3 statements to set flag[i] to FALSE,  $P_j$  takes finite time to enter.

## Attempt IV: 7/10

```
Bool flag[2] = { FALSE, FALSE };
int
      turn;
Process i
flag[i] = TRUE;
while (flag[j]) {
   if (turn == j) {
      flag[i] = FALSE;
      while (turn == j)
      flag[i] = TRUE
Critical Section
turn = j;
flag[i] = FALSE;
```

- Suppose P<sub>i</sub> is entering. P<sub>j</sub>'s location dictates what we have:
  - 1. P<sub>i</sub> is not interested
  - 2. P<sub>i</sub> is in the CS
  - 3.  $P_i$  is entering.
- If P<sub>j</sub> is not interested, it has already set turn to i and flag[j] to false.
- Hence, P<sub>i</sub> enters, waiting for 0 round!

## Attempt IV: 8/10

```
Bool flag[2] = { FALSE, FALSE };
int
      turn;
Process i
flag[i] = TRUE;
while (flag[j]) {
   if (turn == j) {
      flag[i] = FALSE;
      while (turn == j)
      flag[i] = TRUE;
Critical Section
turn = j;
flag[i] = FALSE;
```

- If P<sub>j</sub> is in the CS, flag[j] is true.
- When P<sub>j</sub> exits, it sets turn to i and flag[j] to false.
- If  $P_i$  fails to see flag[j] being false,  $P_j$  can come back quickly and compete against  $P_i$  to enter. This is Case 3.
- If P<sub>i</sub> sees flag[j] being false, P<sub>i</sub> enters, waiting for 0 round.

## Attempt IV: 9/10

```
Bool flag[2] = { FALSE, FALSE };
int
      turn;
Process i
flag[i] = TRUE;
while (flag[j]) {
   if (turn == j) {
      flag[i] = FALSE;
      while (turn == j)
      flag[i] = TRUE;
Critical Section
turn = j;
flag[i] = FALSE;
```

- If P<sub>j</sub> and P<sub>j</sub> are competing to enter, they both set their flag[] to true.
- The value of turn dictates who can enter. Because turn can only be i or j, either P<sub>i</sub> or P<sub>i</sub> enters.
- If P<sub>j</sub> enters, then we have **Case 2** and P<sub>i</sub> enters because P<sub>j</sub> sets turn to i upon exit.
- Thus, P<sub>i</sub> waits for at most one around.

## **Attempt IV: 10/10**



- Prof. Dr. Th. J. (Dirk)
  Dekker (March 1, 1927November 25, 2021) in
  1965 and is usually
  referred to as Dekker's
  algorithm.
- Prof. Dr. Dekker was a Dutch mathematician.
- Th. J. Dekker = Theodorus Jozef Dekker.
- Dekker's algorithm is the first known correct solution to the mutual exclusion problem..

## Attempt IV: 4/n

- Mutual Exclusion: 3
- Thus, P<sub>i</sub> can enter the critical section if and only if flag[i] = TRUE and flag[j] = FALSE.
- Thus,  $P_j$  can enter the critical section if and only if flag[j] = TRUE and flag[i] = FALSE.
- As a result, if  $P_i$  and  $P_j$  are both in the critical section, (flag[i] = TRUE and flag[j] = FALSE) and (flag[j] = TRUE and flag[i] = FALSE) are both true.
- Hence, flag[i] and flag[j] are both true and false, which is impossible.
- $\blacksquare$   $\mathbf{P_i}$  and  $\mathbf{P_i}$  cannot both be in the critical section.

## Attempt IV: 5/8

```
flag[2] = { FALSE, FALSE };
Bool
int
      turn;
Process i
flag[i] = TRUE;
if (flag[j]) {
   if (turn == j) {
      flag[i] = FALSE
      while (turn == j)
      flag[i]
               = TRUE
Critical Section
turn = j;
flag[i] = FALSE;
```

#### Mutual Exclusion: 2

- If flag[j] is FALSE, P<sub>i</sub>
   enters immediately.
- If flag[j] is TRUE, execution enters then.
- P enters if turn is i.
- If turn is j, then P<sub>i</sub> waits until turn becomes i.
- Therefore, P<sub>i</sub> is in its critical section, we have:
  - flag[j] is FALSE
  - > Or turn is i.
  - flag[i] is TRUE

## Attempt IV: 4/8

```
Bool flag[2] = { FALSE, FALSE };
int
       turn;
Process i
flag[i] = TRUE;
if (flag[j]) {
   if (turn == j) {
       flag[i] = FALSE;
       while (turn == j)
       flag[i] = TRUE;
 Critical Section
turn = j;
flag[i] = FALSE;
```

#### Mutual Exclusion: 3

- If P<sub>i</sub> and P<sub>j</sub> are both in their critical sections:
  - For P<sub>i</sub>: flag[j] is FALSE OR turn is i.
  - For P<sub>j</sub>: flag[i] is FALSE OR turn is j.
  - But flag[i] and flag[j] are both TRUE before entering.
    - Thus, turn being i and turn being j must both hold. A contradiction.

## Attempt IV: 5/8

```
Bool flag[2] = { FALSE, FALSE };
int
       turn;
Process i
flag[i] = TRUE;
if (flag[j]) {
    if (turn == j) {
       flag[i] = FALSE;
       while (turn == j)
       flag[i] = TRUE;
 Critical Section
turn = j;
flag[i] = FALSE;
```

#### Progress: 1/2

#### • Outsider Issue:

▶ If P<sub>j</sub> is not interested and P<sub>i</sub> tries to enter, because flag[j] was set to FALSE when P<sub>j</sub> exited, P<sub>i</sub> enters. No outsider issues!

## Attempt IV: 6/8

```
Bool flag[2] = { FALSE, FALSE };
int
       turn;
Process i
flag[i] = TRUE;
if (flag[j]) {
   if (turn == j) {
       flag[i] = FALSE;
       while (turn == j)
       flag[i] = TRUE;
 Critical Section
turn = j;
flag[i] = FALSE;
```

#### Progress: 2/2

#### Finite Decision Time:

- If P<sub>i</sub> and P<sub>j</sub> are both waiting to enter, and the CS is empty, then flag[i] and flag[j] are both TRUE and the determining factor is the value of turn.
- Only the while can cause infinite decision time.
- Because the value of turn is not modified before exit, the test in the while takes finite time, and decision time is finite (i.e., deadlock free).

## Attempt IV: 7/8

```
Bool flag[2] = { FALSE, FALSE };
int
       turn;
Process i
flag[i] = TRUE;
if (flag[j]) {
   if (turn == j) {
       flag[i] = FALSE;
       while (turn == j)
       flag[i] = TRUE;
 Critical Section
turn = j;
flag[i] = FALSE;
```

context switch can happen here

- Upon exit, P<sub>j</sub> sets turn to i and flag[j] to FALSE.
- When P<sub>i</sub> sees turn being i, before P<sub>i</sub> can reset flag[i] back to TRUE,
   P<sub>i</sub> may be switched out and a fast P<sub>j</sub> may come back and enter again.
  - This can happen over and over.
- Thus, there is no way to determine a possible bound.

## Attempt IV: 8/8

```
Bool flag[2] = { FALSE, FALSE }; Bounded Waiting: 2
int
      turn;
Process i
flag[i] = TRUE;
if (flag[j]) {
   if (turn == j) {
      flag[i] = FALSE;
      while (turn == j)
      flag[i] = TRUE;
Critical Section
turn = j;
flag[i] = FALSE;
        context switch can happen here
```

**Bounded waiting fails** 

P <sub>i</sub>	P <sub>j</sub>	turn	fi	fj
	CS	j	Т	Т
f <sub>i</sub> =F		j	F	T
	t=i	i	F	T
t=i?	fj=F	i	F	F
	come back	i	F	F
	f <sub>j</sub> =T	i	F	T
	enter			
	****	3	6	

## Attempt V: A Combination 1/12 Peterson's Algorithm

```
bool flag[2] = FALSE; // process P_i
           int
                 turn;
                        I am interested
                                          yield to you first
          do
                         = TRUE
                                                      enter
                       (flag[j
                                   & &
I am done
                critical section
                                            wait while you are
                                            interested and it is
                         = FALSE;
                                            your turn.
```

#### **Attempt V: Mutual Exclusion 2/12**

```
\begin{array}{lll} & & & & & & & & & & & \\ \text{flag[i]} = & & & & & & & \\ \text{flag[j]} = & & & & & & \\ \text{turn} = & & & & & \\ \text{while (flag[j] &&& turn == j);} & & & & \\ \text{while (flag[i] &&& turn == i);} \end{array}
```

- If  $P_i$  is in its critical section, then it sets
  - flag[i] to TRUE
  - \*turn to j (but turn may not be j after this point because  $P_i$  may set it to i later).
  - \*and waits until flag[j] && turn == j
    becomes FALSE

#### **Attempt V: Mutual Exclusion 3/12**

```
\begin{array}{lll} process \ P_i & process \ P_j \\ \\ flag[i] = TRUE; & flag[j] = TRUE; \\ turn = j; & turn = i; \\ while (flag[j] && turn == j); & while (flag[i] && turn == i); \end{array}
```

- If  $P_j$  is in its critical section, then it sets
  - flag[j] to TRUE
  - \*turn to i (but turn may not be i after this point because  $P_i$  may set it to j later).
  - \*and waits until flag[i] && turn == i
    becomes FALSE

#### **Attempt V: Mutual Exclusion 4/12**

```
process P<sub>i</sub>

flag[i] = TRUE; 
turn = j;
while (flag[j] && turn == j); while (flag[i] && turn == i);
```

- If processes  $P_i$  and  $P_j$  are both in their critical sections, then we have:

  they are both TRUE
  - \*flag[i] and flag[j] are both TRUE.
  - flag[i] && turn == i and flag[j] &&
    turn == j are both FALSE.
  - ❖Therefore, turn == i and turn == j must both be FALSE.

#### **Attempt V: Mutual Exclusion 5/12**

 $\begin{array}{lll} & & & & & & & & & & \\ \text{flag[i]} = & & & & & & & \\ \text{flag[j]} = & & & & & & \\ \text{turn} = & & & & & \\ \text{while (flag[j] &&& turn == j);} & & & \text{while (flag[i] && turn == i);} \\ \end{array}$ 

- Because turn == i and turn == j are both FALSE, turn == j and turn == i are both TRUE.
- This is impossible, because a variable (i.e., turn) cannot hold two different values at the same time (i.e., i and j).
- Therefore, we have a contradiction and mutual exclusion holds.

#### **Attempt V: Mutual Exclusion 6/12**

- We normally use the proof-by-contradiction technique to establish the mutual exclusion condition.
- To do so, follow the procedure below:
  - $\bullet$  Find the condition  $C_0$  for  $P_0$  to enter its CS
  - $\bullet$  Find the condition  $C_1$  for  $P_1$  to enter its CS
  - **If**  $\mathbf{P}_0$  and  $\mathbf{P}_1$  are in their critical sections,  $\mathbf{C}_0$  and  $\mathbf{C}_1$  must both be true.
  - **Trom**  $C_0$  and  $C_1$  being both true, we should be able to derive an absurd result.
  - \*Therefore, mutual exclusion holds.

#### **Attempt V: Mutual Exclusion 7/12**

- We care about the conditions  $C_0$  and  $C_1$ . The way of reaching these conditions via instruction execution is usually un-important.
- Never use an execution sequence to prove mutual exclusion. In doing so, you make a serious mistake, which is referred to as proof-by-example.
- You may use a single example to show a proposition being false. However, you cannot use a single example to show a proposition being true. That is, 3<sup>2</sup> + 4<sup>2</sup> = 5<sup>2</sup> cannot be used to prove a<sup>2</sup> + b<sup>2</sup> = c<sup>2</sup> for any right triangles.

#### Attempt V: Progress 8/12

# $\begin{array}{lll} & & & & & & & & & & \\ \text{flag[i]} = & & & & & & & \\ \text{flag[j]} = & & & & & & \\ \text{turn} = & & & & & \\ \text{while (flag[j] &\&\& turn} == & & & );} & & \text{while (flag[i] &\&\& turn} == & i);} \end{array}$

- If  $P_i$  and  $P_j$  are both waiting to enter their critical sections, since the value of turn can only be i or j but not both, one process can pass its while loop with one comparison (i.e., decision time is finite).
- If  $P_i$  is waiting and  $P_j$  is not interested in entering its CS:
  - Since  $P_j$  is not interested in entering, flag[j] was set to FALSE when  $P_i$  exits, and  $P_i$  enters.
  - **❖**Thus, the process that is not entering does not influence the decision. ⁴⁴

#### **Attempt V: Bounded Waiting 9/12**

# $\begin{array}{lll} & & & & & & & & & & & \\ \text{flag[i]} = & & & & & & & \\ \text{flag[j]} = & & & & & & \\ \text{turn} = & & & & & \\ \text{while (flag[j] && turn} == & & ); & \text{while (flag[i] && turn} == & i); \\ \end{array}$

- If  $P_i$  wishes to enter, we have three cases:
  - 1.  $P_i$  is *outside* of its critical section.
  - 2.  $P_i$  is in the entry section.
  - 3.  $P_i$  is *in* its critical section.

#### **Attempt V: Bounded Waiting 10/12**

```
\begin{array}{lll} & & & & & & & & & & \\ \text{flag[i]} = & & & & & & & \\ \text{flag[j]} = & & & & & & \\ \text{turn} = & & & & & \\ \text{while (flag[j] &\& turn} == & & );} & & & \text{while (flag[i] &\& turn} == & i);} \end{array}
```

- CASE I: If  $P_j$  is *outside* of its critical section,  $P_j$  sets flag[j] to FALSE when it exits its critical section, and  $P_i$  may enter.
- In this case,  $P_i$  does not wait. Or,  $P_i$  waits for 0 turn.

#### **Attempt V: Bounded Waiting 11/12**

```
\begin{array}{lll} & & & & & & & & & & \\ & & & & & & & \\ & & & & & \\ & & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\
```

- CASE 2: If  $P_j$  is in the entry section, depending on the value of turn, we have two cases:
  - If turn is i (e.g.,  $P_i$  sets turn to j before  $P_j$  sets turn to i),  $P_i$  enters immediately.  $P_i$  waits for 0 turn.
  - **\***Otherwise,  $P_j$  enters, and  $P_i$  stays in the while loop, and we have **CASE 3**. In this case,  $P_i$  waits for at least one turn.

#### **Attempt V: Bounded Waiting 12/12**

 $\begin{array}{ccc} \mathbf{process} \ \mathbf{P}_i & & & & \\ \mathbf{process} \ \mathbf{P}_j & & & \\ \end{array}$ 

```
flag[i] = TRUE;
turn = j;
while (flag[j] && turn == j);
flag[j] = TRUE;
turn = i;
while (flag[i] && turn == i);
```

**CASE 3**: If  $P_j$  is *in* its critical section, turn must be j and  $P_i$  waits for at most one round.

$P_i$	$P_{j}$	flag[i]	flag[j]	turn	Comments	
flag[i]=T	flag[j]=T	TRUE	TRUE	?		
while ()		TRUE	TRUE	j	$P_j$ enters	
If P <sub>i</sub> can see	<b>Critical Sec</b>				P. in CS	P <sub>i</sub> has chance
this, it enters	flag[j]=F	TRUE	FALSE	j	<b>T</b>	enter h
	flag[j]=T	TRUE	TRUE	j	$\mathbf{P}_{j}$ returns	if $P_j$ co
	turn = i	TRUE	TRUE	i	$P_j$ yields	back fa
	while ()	TRUE	TRUE	i	$P_j$ loops	
Critical Sec					P, enters	48

#### One More Example: 1/4

Consider the following simple algorithm:

```
Bool flag[2] = { FALSE, FALSE }; // global flags
  Bool turn[2] = { FALSE, TRUE }; // global turn variable
  \begin{array}{ccc} Process \ P_0 & Process \ P_1 & interested \end{array}
                        flag[1] = TRUE;
  flag[0] = TRUE;
  turn[0] = turn[1]; ----- turn[1] = !turn[0];
  repeat equal to the other repeat repeat not equal to the other
                                   until (!flag[0] ||
    until (!flag[1] ||
           turn[0] != turn[1]);
                                          turn[0] == turn[1]);
  Critical Section
  flag[0] = FALSE;
                               flag[1] = FALSE;
Po waits for the two turn values being not equal
```

P<sub>1</sub> waits for the two turn values being<sup>4</sup>equal

#### One More Example: 2/4

#### • Mutual Exclusion:

```
Bool flag[2] = { FALSE, FALSE }; // global flags
 Bool turn[2] = { FALSE, TRUE }; // global turn variable
                    Process P<sub>1</sub> interested
Process Pa
                             flag[1] = TRUE;
 flag[0] = TRUE;
 turn[0] = turn[1];
                                turn[1] = !turn[0];
 repeat
                                repeat
  until (!flag[1] ||
                       until (!flag[0] ||
          turn[0] != turn[1]);
                                         turn[0] == turn[1]);
Critical Section
                                                ____not interested
 flag[0] = FALSE; flag[1] = FALSE;
If P_0 is in CS, flag[0] is TRUE, flag[1] is FALSE OR turn[0] != turn[1]

ightharpoonup If P_1 is in CS, flag[1] is TRUE, flag[0] is FALSE OR turn[0] == turn[1]
 If P_0 and P_1 are in both in CS, flag[0] and flag[1] are TRUE in the until
> Thus, turn[0] and turn[1] are equal and not equal to each other 50
  This is a contradiction!
```

#### One More Example: 3/4

#### Progress:

```
Bool flag[2] = { FALSE, FALSE }; // global flags
Bool turn[2] = { FALSE, TRUE }; // global turn variable
Process Pa
                          Process P<sub>1</sub> interested
flag[0] = TRUE;
                           flag[1] = TRUE;
turn[0] = turn[1];
                             turn[1] = !turn[0];
repeat
                             repeat
 until (!flag[1] ||
                               until (!flag[0] ||
        turn[0] != turn[1]);
                                      turn[0] == turn[1]);
Critical Section
                                             ____not interested
flag[0] = FALSE;
                     flag[1] = FALSE;
```

- $\triangleright$  Outsider Issue: If  $P_1$  is not interested, it sets flag[1] to FALSE and  $P_0$  enters freely.
- Finite Decision Time: If both are trying to enter, testing whether turn[0] is equal to turn[1] takes finite time to choose a candidate to enter.

#### One More Example: 4/4

**Bounded Waiting:** Assume  $P_0$  is entering.

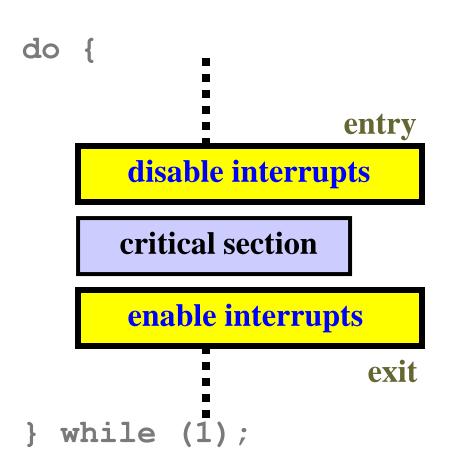
```
Bool flag[2] = { FALSE, FALSE }; // global flags
Bool turn[2] = { FALSE, TRUE }; // global turn variable
Process Pa
                        Process P<sub>1</sub>
                                                          interested
flag[0] = TRUE;
                                  flag[1] = TRUE;
turn[0] = turn[1];
                                     turn[1] = !turn[0];
repeat
                                     repeat
  until (!flag[1] ||
                                     until (!flag[0] ||
           turn[0] != turn[1]);
                                                turn[0] == turn[1]);
Critical Section
                                                        not interested
flag[0] = FALSE;
flag[1] = FALSE;

ightharpoonup If P_1 is not interested, P_0 waits for 0 round and enters.
  \rightarrow If P_1 is competing, P_1 enters if turn [0] = turn [1]. If P_0 detects flag [1] being
      changed to FALSE when P_1 exits, P_0 enters (P_0 waits for 1 round). Or, P_1 comes back
      to set flag[1] to TRUE and negate (i.e., modify) turn[1]. Then, P_0 enters.
   If P_1 is in CS, this is the second half of the above. This, P_0 waits for at most 1 round
```

## **Hardware Support**

- There are two types of hardware synchronization supports:
  - \*Disabling/Enabling interrupts: This is slow and difficult to implement on multiprocessor systems.
  - **❖**Special *privileged*, actually *atomic*, machine instructions:
    - **✓** Test and set (TS)
    - **✓** Compare and Swap (CS)
    - **√**Swap

## **Interrupt Disabling**



- Because interrupts are disabled, no context switch can occur in a critical section (why?).
- Infeasible in a multiprocessor system because all CPUs/cores must be informed.
- Some features that depend on interrupts (e.g., clock) may not work properly.

#### Test-and-Set: 1/2

```
bool TS(bool *key)
{
   bool save = *key;
   *key = TRUE;
   return save;
}
```

- **TS** is atomic.
- Mutual exclusion is met as the TS instruction is atomic. See next slide.
- However, bounded waiting may not be satisfied. Progress?

```
lock = FALSE;
bool
do {
                       entry
    while (TS(&lock));
       critical section
    lock = FALSE;
                        exit
   while (1);
 A process is in its critical section
    e TS instruction returns FALSE.
```

55

#### Test-and-Set: 2/2

- $\mathbf{P}_0$  is in its CS, if TS returns FALSE.
- $\blacksquare$   $P_1$  is in its CS, if TS returns FALSE.
- If P<sub>0</sub> and P<sub>1</sub> are in their critical sections, they both got the FALSE return value from TS.
- P<sub>0</sub> and P<sub>1</sub> cannot execute their TS instructions at the same time because TS is atomic. Their TS are executed sequentially.
- Hence, if P<sub>0</sub> executes the TS before the other, once P<sub>0</sub> finishes its TS, the value of lock becomes TRUE. P<sub>1</sub> cannot get a FALSE return value and cannot enter its CS.
- We have a contradiction!

```
bool lock = FALSE;
do {
   while (TS(&lock));
      critical section
   lock = FALSE;
  while (1);
```

## Compare-and-Swap: 1/2

```
bool CS(int *p,old,new)
{
   if (*p != old)
      return FALSE;
   *p = new;
   return TRUE;
}
```

- CS is atomic.
- Mutual exclusion is met as the CS instruction is atomic. See next slide.
- However, bounded waiting may not be satisfied. Progress?

```
bool lock = FALSE;

do {
    entry
    while(!CS(&lock,FALSE,TRUE))
    ;
```

#### critical section

```
lock = FALSE;
exit
while (1);
```

A process is in its critical section if the CS instruction returns TRUE.

#### Compare-and-Swap: 2/2

```
bool CS(int *p,old,new)
{
   if (*p != old)
      return FALSE;
   *p = new;
   return TRUE;
}
```

```
int count = 0;

done = FALSE;
while (!done) {
   val = *count;
   done = CS(&count, val, val+1);
}
```

- CS is useful for building mutual exclusion.
- Because CS is atomic, it offers a fast way for updating variables such as doing count++ and count-- in a mutually exclusive way.
- It is also very useful in a kernel for implementing locks. You will learn this in an *Operating Systems* course.

## Problems with Software and Hardware Solutions

- All these solutions use busy waiting.
- Busy waiting means a process waits by executing a tight loop to check the status/value of a variable.
- Busy waiting may be needed on a multiprocessor system; however, it wastes CPU cycles that some other processes may use productively.
- Even though some systems may allow users to use some atomic instructions, unless the system is lightly loaded, CPU and system performance can be low, although a programmer may "think" his/her program looks more efficient.
- So, we need better solutions.

## The End