*Impleme & Removing non-essential * No well defined structure * Powterione * Security * ethicing * File management *Implement & Removing non-essential * Small * Not well separ.

Thing non components from kernal Fit Simple structure

Besental * cholie * Control over System performance 1 Some System Objective calls. * Job accounting programs * Results Kernal +OS broken fork(), ext(). Interface who * For detection aids into pieces.
Layered Structure as roadable in smaller kernal read() withou hardware * (coordination blu) getpidu. * Kerval only Nam 2 other software and * Each layer can use 1 contain essential closely Modular programs function of lower 105/ Structure * best * Memory Management layer only. | Structure Structure ! * Processor management *Layer O- Hardware. Operations / * Device management * Whole OS works under Monotithic Kernal * Layer N. VI. Functions / Module * Handling I/o system € * Program Execution Flage kernal size Types / Services * Manipulation of file system due to large no. of modules. * Error detection & Handling Jobs → Os → To batches Batch Operating System * Resource allo cation System Call Multiprocessing * Accounting * Protection Hanipulation are System Each task is given Time Sharing 05 * Symmetric Interface blw process some time to operate, Multitasking BIOS * Assymmetric and 05 system. Process Real time os Distributed os - Process Control Boot Loader Network 05 > *Independent cpu & Memory and proceed - File management * Response time Kernal - Communication * Used in Strict * Run on a server Using Interconnection - Device management environment. * Shared Access is 05 69 * Timed system nlw - Information management enabled Tuner

3	time: Min	
RIMMEI MEMBELL	- Throughput: Max.	
	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	1841
	ICO	Quartury
* Computation speedup.	- [* KOUND KODIN
* Information	ت	
Cooperating process (requires Associated	* Automatic &	7
2 Communication	h, process Asynchronize	* 577 -
ent process Interprocess	* Sunchronics &	* FCF6: 1
Independ- Tt. (reation	* Direct or indirect	Scheduling Algo.
-Cooperating	receive (message)	1 MEGINAN JEIAN
	* send (message) Synchronisa-	* Modilian torm
erver) *I	oddr. Space.	* Long term
ale Y	sharing the same	
) * * single & Multi- / +	process without system	Schedulers
> Process > * Preemptive	markin *	- waiting for ILO
Process SyiThroads - *A path in CPU Scheduler	evel * Used in distributed system	Device Queue
1 switch	executed for read, white consuming from buffer	execute tor
* context Benefits	main to buffer consumer	waiting in
Register * Elon Scalore	. *	- Set of all pi
* Bestoming liter	neword buffer	D. J. D. IN
running waiting & * nonsivened Operations	- set of all process - Huban made all system of	- set of all
ر ھے	- Bour	Job Queue

Module * Several - Critical Section: Shared data & manipulate the same entru Critical - Section Kace data Terminder section Problems on * Dining philosophers * Readers and processes * Bounded Problem Synchionization Condition problem problem Problem access MUHEX Locks buffer section, Writers Synchronization * release () * acquire() Must satisfy Critical Section * turn, flag * Software based Hardware Peterson's * Mutual * Bounded accessina Chances Semaphore * Using * Deadlock called lock Simple Solution exclusion * Counting * Starvation * Binary * Progress からけらる Solution 8

Dynamic Linking > shared Logical address space: generated by a program set of all logical addi-Junamic Loadino horaries. * Second chance Algo * Optimal Page Replace * A page replace-* LRU Page Replacement Replacement Reference bit 0 = Remove the reference bit 1: * FCFS Page Replacement give one Memory 1 more chance Memory page memory Mais y is treated as ment scheme * Secondary memoa page fault whenever there is tage Demans. Memory Virtual bits lbytes, each, with its * Paging: Pages & Frames method another Rom one address space to Address * Browne Memory binding is a mapping *Internal Memory Aagmentation H. alverte Translation Look Alread R PRM partitioning ssarppo umo sized segments. Configuous merviory Allocation *Done by Minding (low adds) Variable LINKING chis whellen miso

* Segmentation. Views memory as a * Protection Registers - Base and Limit * External parthoning tragmentation - Dynamic -Static Physical address addr. collection of variable rated by cpu. Seen by memory unit + Compaction. *Best fil, worst At *Loading a program on demand > Hole (large size partition) Dynamic loading continued execution store and brought temp out of me-* process swaped Buiddoms (18175MACT)

Buffer

Logical address: addr. gene

1 - Compile time

- Execution time

