

# STM32 CubeMX

## 1. Description

### 1.1. Project

|                 |                    |
|-----------------|--------------------|
| Project Name    | ADC_DMA            |
| Board Name      | custom             |
| Generated with: | STM32CubeMX 6.13.0 |
| Date            | 04/15/2025         |

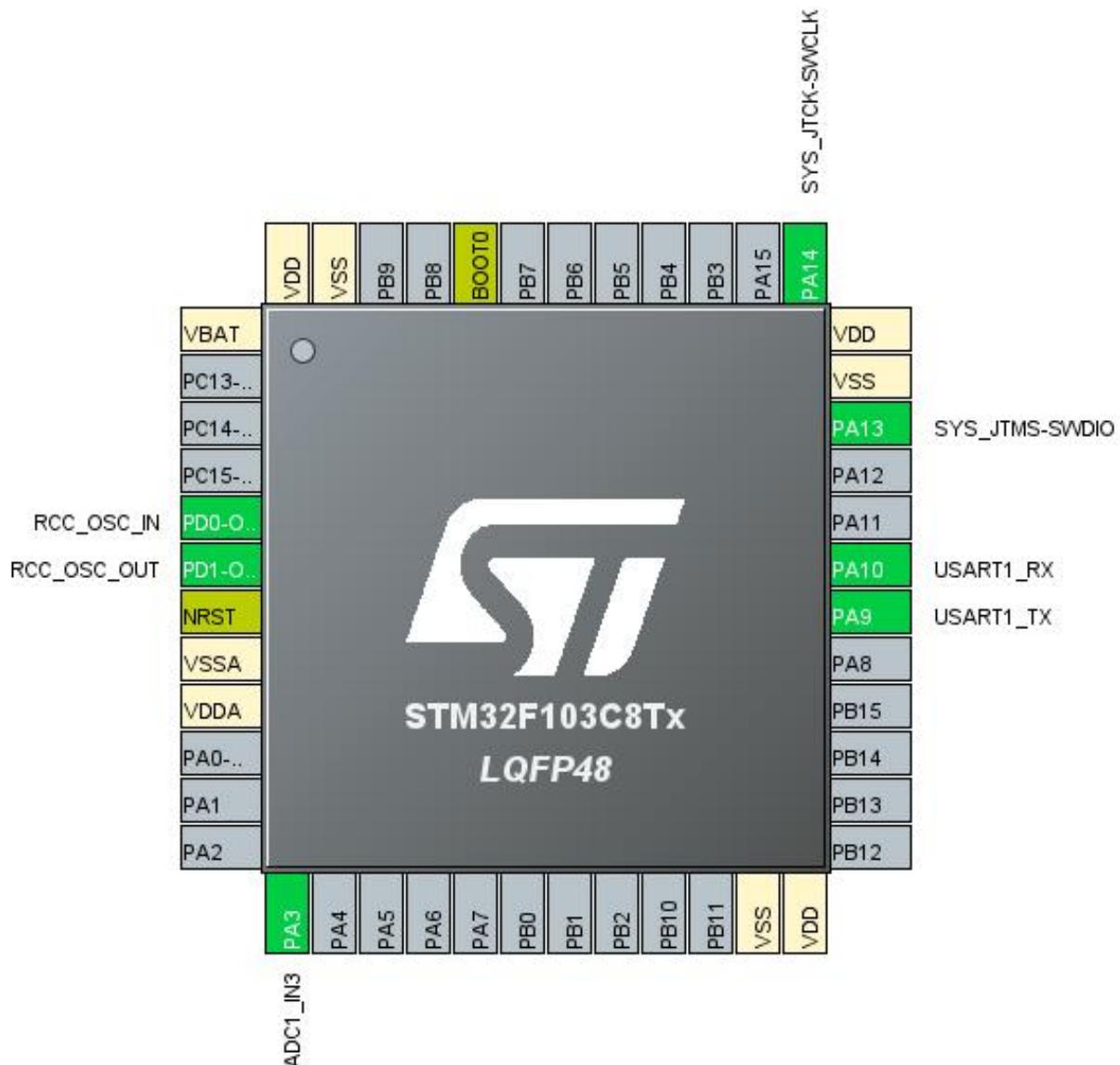
### 1.2. MCU

|                |               |
|----------------|---------------|
| MCU Series     | STM32F1       |
| MCU Line       | STM32F103     |
| MCU name       | STM32F103C8Tx |
| MCU Package    | LQFP48        |
| MCU Pin number | 48            |

### 1.3. Core(s) information

|         |               |
|---------|---------------|
| Core(s) | Arm Cortex-M3 |
|---------|---------------|

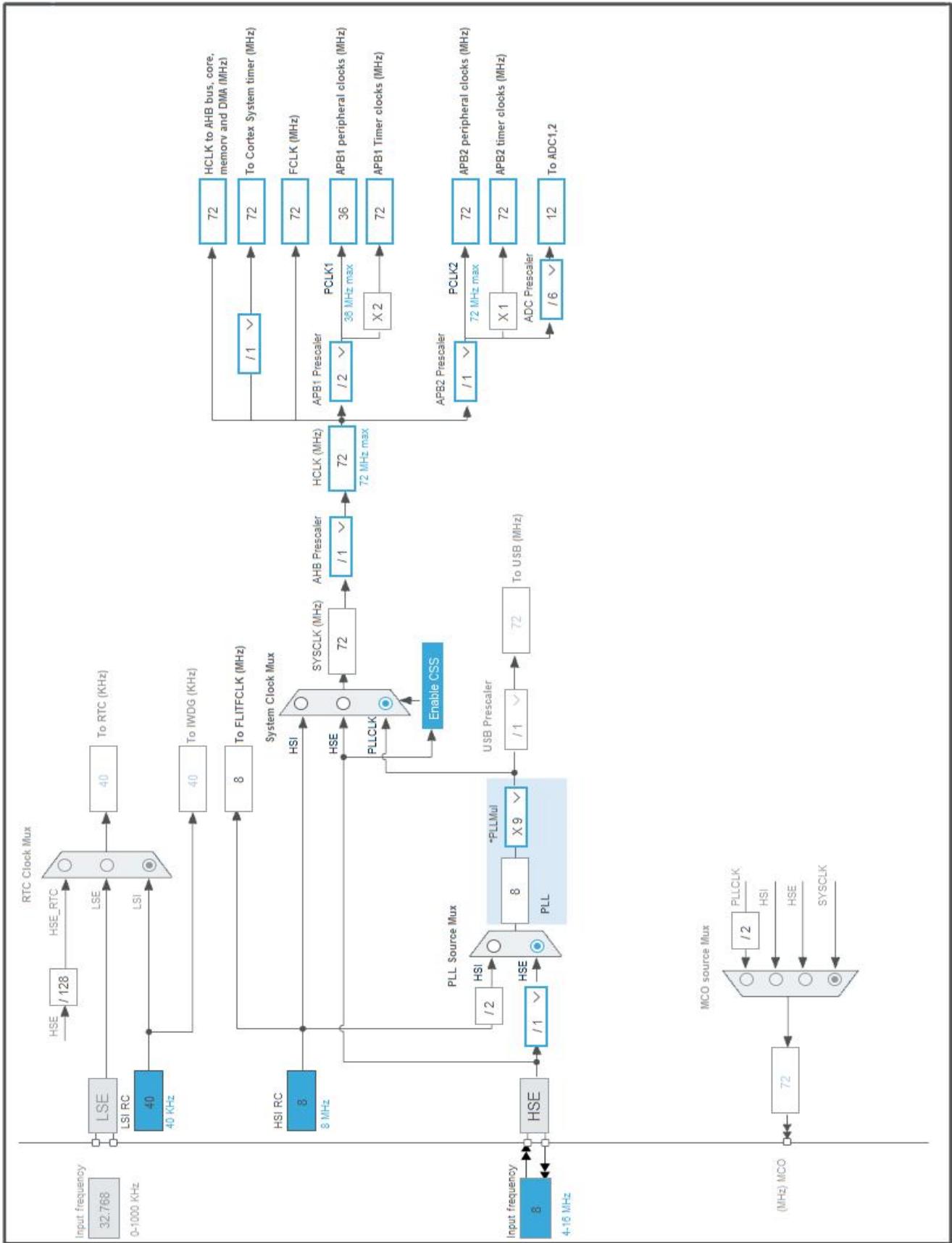
## 2. Pinout Configuration



### 3. Pins Configuration

| Pin Number<br>LQFP48 | Pin Name<br>(function after<br>reset) | Pin Type | Alternate<br>Function(s) | Label |
|----------------------|---------------------------------------|----------|--------------------------|-------|
| 1                    | VBAT                                  | Power    |                          |       |
| 5                    | PD0-OSC_IN                            | I/O      | RCC_OSC_IN               |       |
| 6                    | PD1-OSC_OUT                           | I/O      | RCC_OSC_OUT              |       |
| 7                    | NRST                                  | Reset    |                          |       |
| 8                    | VSSA                                  | Power    |                          |       |
| 9                    | VDDA                                  | Power    |                          |       |
| 13                   | PA3                                   | I/O      | ADC1_IN3                 |       |
| 23                   | VSS                                   | Power    |                          |       |
| 24                   | VDD                                   | Power    |                          |       |
| 30                   | PA9                                   | I/O      | USART1_TX                |       |
| 31                   | PA10                                  | I/O      | USART1_RX                |       |
| 34                   | PA13                                  | I/O      | SYS_JTMS-SWDIO           |       |
| 35                   | VSS                                   | Power    |                          |       |
| 36                   | VDD                                   | Power    |                          |       |
| 37                   | PA14                                  | I/O      | SYS_JTCK-SWCLK           |       |
| 44                   | BOOT0                                 | Boot     |                          |       |
| 47                   | VSS                                   | Power    |                          |       |
| 48                   | VDD                                   | Power    |                          |       |

## 4. Clock Tree Configuration



## 1. Power Consumption Calculator report

### 1.1. Microcontroller Selection

|           |               |
|-----------|---------------|
| Series    | STM32F1       |
| Line      | STM32F103     |
| MCU       | STM32F103C8Tx |
| Datasheet | DS5319_Rev17  |

### 1.2. Parameter Selection

|             |     |
|-------------|-----|
| Temperature | 25  |
| Vdd         | 3.3 |

### 1.3. Battery Selection

|                   |                 |
|-------------------|-----------------|
| Battery           | Li-SOCL2(A3400) |
| Capacity          | 3400.0 mAh      |
| Self Discharge    | 0.08 %/month    |
| Nominal Voltage   | 3.6 V           |
| Max Cont Current  | 100.0 mA        |
| Max Pulse Current | 200.0 mA        |
| Cells in series   | 1               |
| Cells in parallel | 1               |

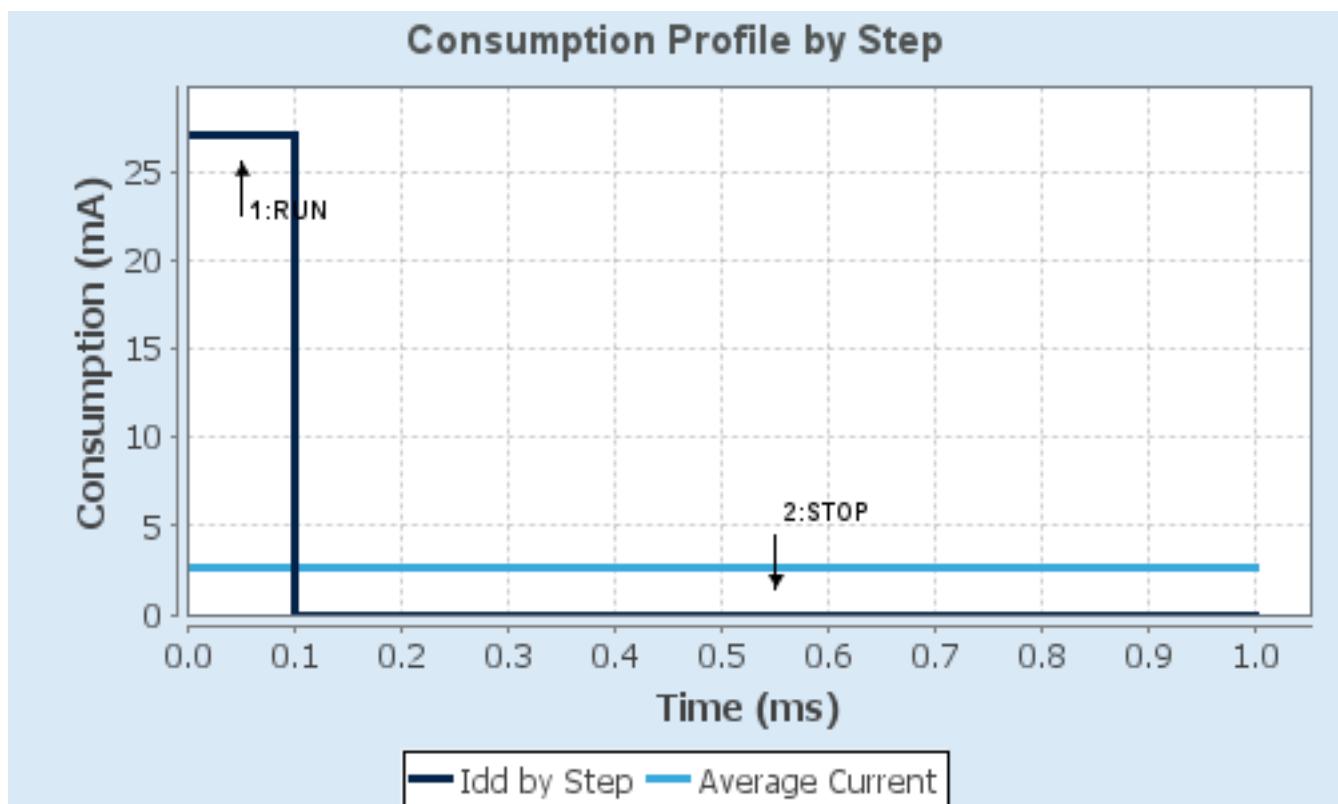
#### 1.4. Sequence

|                               |             |              |
|-------------------------------|-------------|--------------|
| <b>Step</b>                   | Step1       | Step2        |
| <b>Mode</b>                   | RUN         | STOP         |
| <b>Vdd</b>                    | 3.3         | 3.3          |
| <b>Voltage Source</b>         | Battery     | Battery      |
| <b>Range</b>                  | No Scale    | No Scale     |
| <b>Fetch Type</b>             | FLASH       | n/a          |
| <b>CPU Frequency</b>          | 72 MHz      | 0 Hz         |
| <b>Clock Configuration</b>    | HSE PLL     | Regulator LP |
| <b>Clock Source Frequency</b> | 8 MHz       | 0 Hz         |
| <b>Peripherals</b>            |             |              |
| <b>Additional Cons.</b>       | 0 mA        | 0 mA         |
| <b>Average Current</b>        | 27 mA       | 14 µA        |
| <b>Duration</b>               | 0.1 ms      | 0.9 ms       |
| <b>DMIPS</b>                  | 90.0        | 0.0          |
| <b>T<sub>a</sub> Max</b>      | 100.1       | 105          |
| <b>Category</b>               | In DS Table | In DS Table  |

#### 1.5. Results

|               |                               |                 |            |
|---------------|-------------------------------|-----------------|------------|
| Sequence Time | 1 ms                          | Average Current | 2.71 mA    |
| Battery Life  | 1 month, 21 days,<br>17 hours | Average DMIPS   | 61.0 DMIPS |

#### 1.6. Chart



## 2. Software Project

### 2.1. Project Settings

| Name                              | Value                  |
|-----------------------------------|------------------------|
| Project Name                      | ADC_DMA                |
| Project Folder                    | D:\New                 |
| Toolchain / IDE                   | STM32CubeIDE           |
| Firmware Package Name and Version | STM32Cube FW_F1 V1.8.6 |
| Application Structure             | Advanced               |
| Generate Under Root               | Yes                    |
| Do not generate the main()        | No                     |
| Minimum Heap Size                 | 0x200                  |
| Minimum Stack Size                | 0x400                  |

### 2.2. Code Generation Settings

| Name  | Value                                 |
|---|---------------------------------------|
| STM32Cube MCU packages and embedded software                    | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files   | No                                    |
| Backup previously generated files when re-generating            | No                                    |
| Keep User Code when re-generating                               | Yes                                   |
| Delete previously generated files when not re-generated         | Yes                                   |
| Set all free pins as analog (to optimize the power consumption) | No                                    |
| Enable Full Assert  | No                                    |

### 2.3. Advanced Settings - Generated Function Calls

| Rank | Function Name       | Peripheral Instance Name |
|------|---------------------|--------------------------|
| 1    | SystemClock_Config  | RCC                      |
| 2    | MX_GPIO_Init        | GPIO                     |
| 3    | MX_DMA_Init         | DMA                      |
| 4    | MX_ADC1_Init        | ADC1                     |
| 5    | MX_USART1_UART_Init | USART1                   |

### 3. Peripherals and Middlewares Configuration

#### 3.1. ADC1

**mode:** IN3

##### 3.1.1. Parameter Settings:

###### **ADCs\_Common\_Settings:**

|      |                  |
|------|------------------|
| Mode | Independent mode |
|------|------------------|

###### **ADC\_Settings:**

|                |                 |
|----------------|-----------------|
| Data Alignment | Right alignment |
|----------------|-----------------|

|                      |          |
|----------------------|----------|
| Scan Conversion Mode | Disabled |
|----------------------|----------|

|                            |                  |
|----------------------------|------------------|
| Continuous Conversion Mode | <b>Enabled *</b> |
|----------------------------|------------------|

|                               |          |
|-------------------------------|----------|
| Discontinuous Conversion Mode | Disabled |
|-------------------------------|----------|

###### **ADC-Regular\_ConversionMode:**

|                            |        |
|----------------------------|--------|
| Enable Regular Conversions | Enable |
|----------------------------|--------|

|                      |   |
|----------------------|---|
| Number Of Conversion | 1 |
|----------------------|---|

|                                    |   |
|------------------------------------|---|
| External Trigger Conversion Source | Regular Conversion launched by software |
|------------------------------------|---|

|             |   |
|-------------|---|
| <u>Rank</u> | 1 |
|-------------|---|

|         |           |
|---------|-----------|
| Channel | Channel 3 |
|---------|-----------|

|               |            |
|---------------|------------|
| Sampling Time | 1.5 Cycles |
|---------------|------------|

###### **ADC\_Injected\_ConversionMode:**

|                             |         |
|-----------------------------|---------|
| Enable Injected Conversions | Disable |
|-----------------------------|---------|

###### **WatchDog:**

|                             |       |
|-----------------------------|-------|
| Enable Analog WatchDog Mode | false |
|-----------------------------|-------|

#### 3.2. RCC

##### **High Speed Clock (HSE): Crystal/Ceramic Resonator**

##### 3.2.1. Parameter Settings:

###### **System Parameters:**

|                 |     |
|-----------------|-----|
| VDD voltage (V) | 3.3 |
|-----------------|-----|

|                 |         |
|-----------------|---------|
| Prefetch Buffer | Enabled |
|-----------------|---------|

|                   |                    |
|-------------------|--------------------|
| Flash Latency(WS) | 2 WS (3 CPU cycle) |
|-------------------|--------------------|

###### **RCC Parameters:**

|                       |    |
|-----------------------|----|
| HSI Calibration Value | 16 |
|-----------------------|----|

|                                |     |
|--------------------------------|-----|
| HSE Startup Timeout Value (ms) | 100 |
|--------------------------------|-----|

|                                |      |
|--------------------------------|------|
| LSE Startup Timeout Value (ms) | 5000 |
|--------------------------------|------|

### 3.3. SYS

**Debug: Serial Wire**

**Timebase Source: SysTick**

### 3.4. USART1

**Mode: Asynchronous**

#### 3.4.1. Parameter Settings:

##### **Basic Parameters:**

|             |                           |
|-------------|---------------------------|
| Baud Rate   | <b>9600 *</b>             |
| Word Length | 8 Bits (including Parity) |
| Parity      | None                      |
| Stop Bits   | 1                         |

##### **Advanced Parameters:**

|                |                      |
|----------------|----------------------|
| Data Direction | Receive and Transmit |
| Over Sampling  | 16 Samples           |

\* User modified value

## 4. System Configuration

### 4.1. GPIO configuration

| IP     | Pin         | Signal         | GPIO mode                    | GPIO pull/up pull down      | Max Speed     | User Label |
|--------|-------------|----------------|------------------------------|-----------------------------|---------------|------------|
| ADC1   | PA3         | ADC1_IN3       | Analog mode                  | n/a                         | n/a           |            |
| RCC    | PD0-OSC_IN  | RCC_OSC_IN     | n/a                          | n/a                         | n/a           |            |
|        | PD1-OSC_OUT | RCC_OSC_OUT    | n/a                          | n/a                         | n/a           |            |
| SYS    | PA13        | SYS_JTMS-SWDIO | n/a                          | n/a                         | n/a           |            |
|        | PA14        | SYS_JTCK-SWCLK | n/a                          | n/a                         | n/a           |            |
| USART1 | PA9         | USART1_TX      | Alternate Function Push Pull | n/a                         | <b>High *</b> |            |
|        | PA10        | USART1_RX      | Input mode                   | No pull-up and no pull-down | <b>n/a</b>    |            |

#### 4.2. DMA configuration

| DMA request | Stream        | Direction            | Priority |
|-------------|---------------|----------------------|----------|
| ADC1        | DMA1_Channel1 | Peripheral To Memory | Low      |

##### ADC1: DMA1\_Channel1 DMA request Settings:

Mode: Normal  
Peripheral Increment: Disable  
Memory Increment: **Enable \***  
Peripheral Data Width: Half Word  
Memory Data Width: Half Word

## 4.3. NVIC configuration

### 4.3.1. NVIC

| Interrupt Table                         | Enable | Preenemption Priority | SubPriority |
|---|--------|-----------------------|-------------|
| Non maskable interrupt                  | true   | 0                     | 0           |
| Hard fault interrupt                    | true   | 0                     | 0           |
| Memory management fault                 | true   | 0                     | 0           |
| Prefetch fault, memory access fault     | true   | 0                     | 0           |
| Undefined instruction or illegal state  | true   | 0                     | 0           |
| System service call via SWI instruction | true   | 0                     | 0           |
| Debug monitor                           | true   | 0                     | 0           |
| Pendable request for system service     | true   | 0                     | 0           |
| System tick timer                       | true   | 15                    | 0           |
| DMA1 channel1 global interrupt          | true   | 0                     | 0           |
| PVD interrupt through EXTI line 16      |        | unused                |             |
| Flash global interrupt                  |        | unused                |             |
| RCC global interrupt                    |        | unused                |             |
| ADC1 and ADC2 global interrupts         |        | unused                |             |
| USART1 global interrupt                 |        | unused                |             |

### 4.3.2. NVIC Code generation

| Enabled interrupt Table                 | Select for init sequence ordering | Generate IRQ handler | Call HAL handler |
|---|-----------------------------------|----------------------|------------------|
| Non maskable interrupt                  | false                             | true                 | false            |
| Hard fault interrupt                    | false                             | true                 | false            |
| Memory management fault                 | false                             | true                 | false            |
| Prefetch fault, memory access fault     | false                             | true                 | false            |
| Undefined instruction or illegal state  | false                             | true                 | false            |
| System service call via SWI instruction | false                             | true                 | false            |
| Debug monitor                           | false                             | true                 | false            |
| Pendable request for system service     | false                             | true                 | false            |
| System tick timer                       | false                             | true                 | true             |
| DMA1 channel1 global interrupt          | false                             | true                 | true             |

\* User modified value

## 5. System Views

### 5.1. Category view

#### 5.1.1. Current

#### Middleware

##### System Core

##### Analog

##### Timers

##### Connectivity

##### Computing

DMA

ADC1

USART1

GPIO

NVIC

RCC

SYS

## 6. Docs & Resources

| Type                   | Link  |
|------------------------|---|
| BSDL files             | <a href="https://www.st.com/resource/en/bsdl_model/stm32f1_bsdl.zip">https://www.st.com/resource/en/bsdl_model/stm32f1_bsdl.zip</a>   |
| IBIS models            | <a href="https://www.st.com/resource/en/ibis_model/stm32f1-ibis.zip">https://www.st.com/resource/en/ibis_model/stm32f1-ibis.zip</a>   |
| System View            | <a href="https://www.st.com/resource/en/svd/stm32f1_svd.zip">https://www.st.com/resource/en/svd/stm32f1_svd.zip</a>   |
| Description            |   |
| Presentations          | <a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf</a>   |
| Presentations          | <a href="https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf">https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf</a>   |
| Presentations          | <a href="https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf">https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf</a>   |
| Presentations          | <a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf</a>   |
| Presentations          | <a href="https://www.st.com/resource/en/product_presentation/microcontrollers-stm32-family-overview.pdf">https://www.st.com/resource/en/product_presentation/microcontrollers-stm32-family-overview.pdf</a>   |
| Brochures              | <a href="https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf">https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf</a>   |
| Flyers                 | <a href="https://www.st.com/resource/en/flyer/flstm32nucleo.pdf">https://www.st.com/resource/en/flyer/flstm32nucleo.pdf</a>   |
| Flyers                 | <a href="https://www.st.com/resource/en/flyer/fldpstpf11120.pdf">https://www.st.com/resource/en/flyer/fldpstpf11120.pdf</a>   |
| Product Certifications | <a href="https://www.st.com/resource/en/certification_document/1239988349.pdf">https://www.st.com/resource/en/certification_document/1239988349.pdf</a>   |
| Product Certifications | <a href="https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf">https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf</a>   |
| Application Notes      | <a href="https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf</a>             |
| Application Notes      | <a href="https://www.st.com/resource/en/application_note/an2586-getting-started-with-stm32f10xxx-hardware-development-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an2586-getting-started-with-stm32f10xxx-hardware-development-stmicroelectronics.pdf</a> |
| Application Notes      | <a href="https://www.st.com/resource/en/application_note/an2604-stm32f101xx-and-stm32f103xx-rtc-calibration-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an2604-stm32f101xx-and-stm32f103xx-rtc-calibration-stmicroelectronics.pdf</a>                     |
| Application Notes      | <a href="https://www.st.com/resource/en/application_note/an2606-stm32-">https://www.st.com/resource/en/application_note/an2606-stm32-</a>   |

- microcontroller-system-memory-boot-mode-stmicroelectronics.pdf
- Application Notes [https://www.st.com/resource/en/application\\_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an3095-stevalisv002v1-stevalisv002v2-3-kw-gridconnected-pv-system-based-on-the-stm32f103xx-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3095-stevalisv002v1-stevalisv002v2-3-kw-gridconnected-pv-system-based-on-the-stm32f103xx-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an3108-stlm75-firmware-library-for-the-stm32f10x-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3108-stlm75-firmware-library-for-the-stm32f10x-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an3128-stm32-embedded-graphic-objectstouchscreen-library-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3128-stm32-embedded-graphic-objectstouchscreen-library-stmicroelectronics.pdf)
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- Application Notes [https://www.st.com/resource/en/application\\_note/an3422-migration-of-microcontroller-applications-from-stm32f1-to-stm32l1-series-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3422-migration-of-microcontroller-applications-from-stm32f1-to-stm32l1-series-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an3427-migrating-a-microcontroller-application-from-stm32f1-to-stm32f2-series-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3427-migrating-a-microcontroller-application-from-stm32f1-to-stm32f2-series-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an3429-stm32-](https://www.st.com/resource/en/application_note/an3429-stm32-)

- proprietary-code-protection-overview-stmicroelectronics.pdf
- Application Notes [https://www.st.com/resource/en/application\\_note/an3961-stevalime003v1-demonstration-board-based-on-the-sthv748-ultrasound-pulser-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3961-stevalime003v1-demonstration-board-based-on-the-sthv748-ultrasound-pulser-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4070-250-w-grid-connected-microinverter-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4070-250-w-grid-connected-microinverter-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4076-two-or-three-shunt-resistor-based-current-sensing-circuit-design-in-3phase-inverters-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4076-two-or-three-shunt-resistor-based-current-sensing-circuit-design-in-3phase-inverters-stmicroelectronics.pdf)
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Errata Sheets [https://www.st.com/resource/en/errata\\_sheet/es096-stm32f101x8b-stm32f102x8b-and-stm32f103x8b-medium-density-device-limitations-stmicroelectronics.pdf](https://www.st.com/resource/en/errata_sheet/es096-stm32f101x8b-stm32f102x8b-and-stm32f103x8b-medium-density-device-limitations-stmicroelectronics.pdf)

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| Reference Manuals          | <a href="https://www.st.com/resource/en/reference_manual/rm0008-stm32f101xx-stm32f102xx-stm32f103xx-stm32f105xx-and-stm32f107xx-advanced-armbased-32bit-mcus-stmicroelectronics.pdf">https://www.st.com/resource/en/reference_manual/rm0008-stm32f101xx-stm32f102xx-stm32f103xx-stm32f105xx-and-stm32f107xx-advanced-armbased-32bit-mcus-stmicroelectronics.pdf</a> |
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