

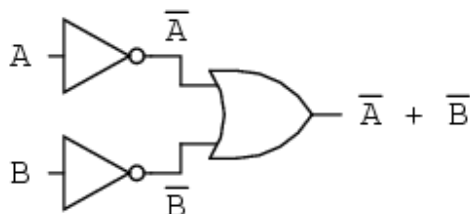
## DEMORGAN'S THEOREMS

A mathematician named DeMorgan developed a pair of important rules regarding group complementation in Boolean algebra. By group complementation, I'm referring to the complement of a group of terms, represented by a long bar over more than one variable.

You should recall from the chapter on logic gates that inverting all inputs to a gate reverses that gate's essential function from AND to OR, or vice versa, and also inverts the output. So, an OR gate with all inputs inverted (a Negative-OR gate) behaves the same as a NAND gate, and an AND gate with all inputs inverted (a Negative-AND gate) behaves the same as a NOR gate. DeMorgan's theorems state the same equivalence in "backward" form: that inverting the output of any gate results in the same function as the opposite type of gate (AND vs. OR) with inverted inputs:



*... is equivalent to ...*



$$\overline{AB} = \overline{A} + \overline{B}$$

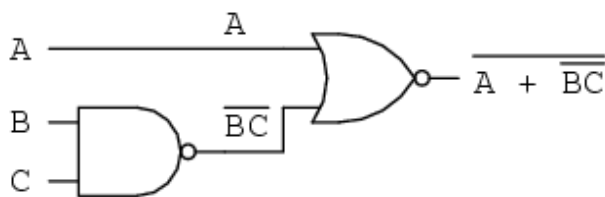
A long bar extending over the term AB acts as a grouping symbol, and as such is entirely different from the product of A and B independently inverted. In other words, (AB)' is not equal to A'B'. Because the "prime" symbol (') cannot be stretched over two variables like a bar can, we are forced to use parentheses to make it apply to the whole term AB in the previous sentence. A bar, however, acts as its own grouping symbol when stretched over more than one variable. This has profound impact on how Boolean expressions are evaluated and reduced, as we shall see.

DeMorgan's theorem may be thought of in terms of breaking a long bar symbol. When a long bar is broken, the operation directly underneath the break changes from addition to multiplication, or vice versa, and the broken bar pieces remain over the individual variables. To illustrate:

### DeMorgan's Theorems



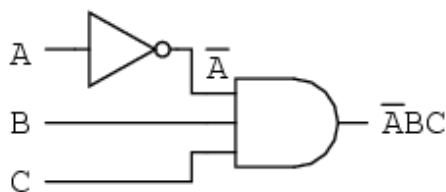
When multiple "layers" of bars exist in an expression, you may only break one bar at a time, and it is generally easier to begin simplification by breaking the longest (uppermost) bar first. To illustrate, let's take the expression  $(A + (BC))'$  and reduce it using DeMorgan's Theorems:



Following the advice of breaking the longest (uppermost) bar first, I'll begin by breaking the bar covering the entire expression as a first step:

$$\begin{array}{l}
 \overline{A + \overline{BC}} \\
 \downarrow \text{Breaking longest bar} \\
 \overline{A} \overline{\overline{BC}} \quad \text{(addition changes to multiplication)} \\
 \downarrow \text{Applying identity } \overline{\overline{A}} = A \\
 \overline{A} BC \quad \text{to } \overline{BC}
 \end{array}$$

As a result, the original circuit is reduced to a three-input AND gate with the A input inverted:



You should never break more than one bar in a single step, as illustrated here:

$$\begin{array}{l}
 \overline{A + \overline{BC}} \\
 \downarrow \text{Incorrect step!} \quad \text{Breaking long bar between A and B; Breaking both bars between B and C} \\
 \overline{A} \overline{\overline{B}} + \overline{\overline{C}} \\
 \downarrow \text{Applying identity } \overline{\overline{A}} = A \text{ to } \overline{\overline{B}} \text{ and } \overline{\overline{C}} \\
 \text{Incorrect answer: } \overline{A} B + C
 \end{array}$$

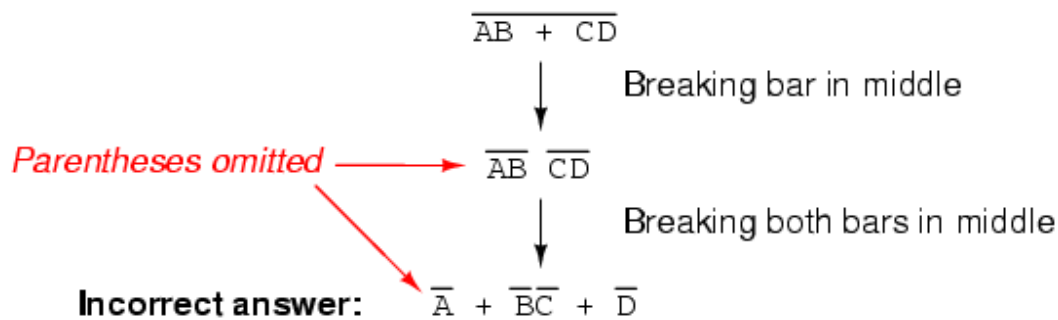
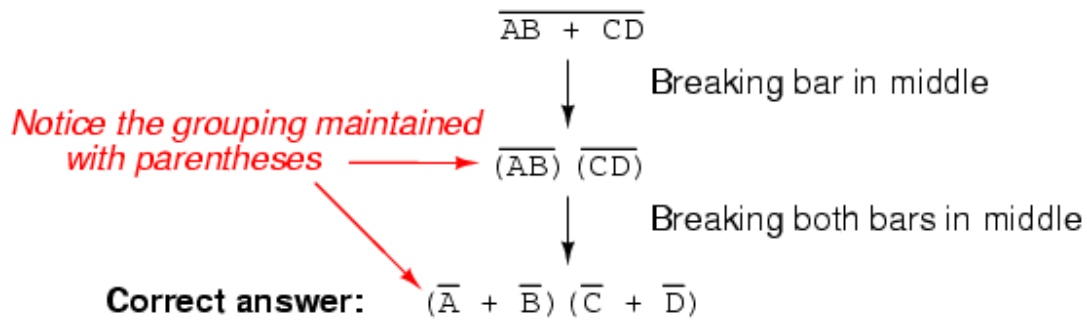
As tempting as it may be to conserve steps and break more than one bar at a time, it often leads to an incorrect result, so don't do it!

It is possible to properly reduce this expression by breaking the short bar first, rather than the long bar first:

$$\begin{array}{l}
 \overline{A + \overline{BC}} \\
 \downarrow \text{Breaking shortest bar} \\
 \overline{A + (\overline{B} + \overline{C})} \quad \text{(multiplication changes to addition)} \\
 \downarrow \text{Applying associative property} \\
 \overline{A + \overline{B} + \overline{C}} \quad \text{to remove parentheses} \\
 \downarrow \text{Breaking long bar in two places,} \\
 \overline{A} \quad \overline{\overline{B}} \quad \overline{\overline{C}} \quad \text{between 1st and 2nd terms;} \\
 \downarrow \text{between 2nd and 3rd terms} \\
 \overline{A} \quad \overline{\overline{B}} \quad \overline{\overline{C}} \\
 \downarrow \text{Applying identity } \overline{\overline{A}} = A \\
 \overline{A}BC \quad \text{to } \overline{\overline{B}} \text{ and } \overline{\overline{C}}
 \end{array}$$

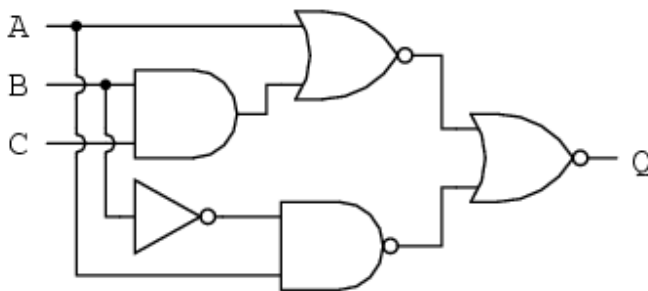
The end result is the same, but more steps are required compared to using the first method, where the longest bar was broken first. Note how in the third step we broke the long bar in two places. This is a legitimate mathematical operation, and not the same as breaking two bars in one step! The prohibition against breaking more than one bar in one step is not a prohibition against breaking a bar in more than one place. Breaking in more than one place in a single step is okay; breaking more than one bar in a single step is not.

You might be wondering why parentheses were placed around the sub-expression  $\overline{B} + \overline{C}$ , considering the fact that I just removed them in the next step. I did this to emphasize an important but easily neglected aspect of DeMorgan's theorem. Since a long bar functions as a grouping symbol, the variables formerly grouped by a broken bar must remain grouped lest proper precedence (order of operation) be lost. In this example, it really wouldn't matter if I forgot to put parentheses in after breaking the short bar, but in other cases it might. Consider this example, starting with a different expression:

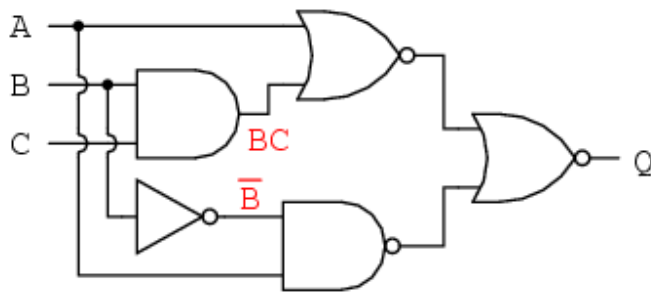


As you can see, maintaining the grouping implied by the complementation bars for this expression is crucial to obtaining the correct answer.

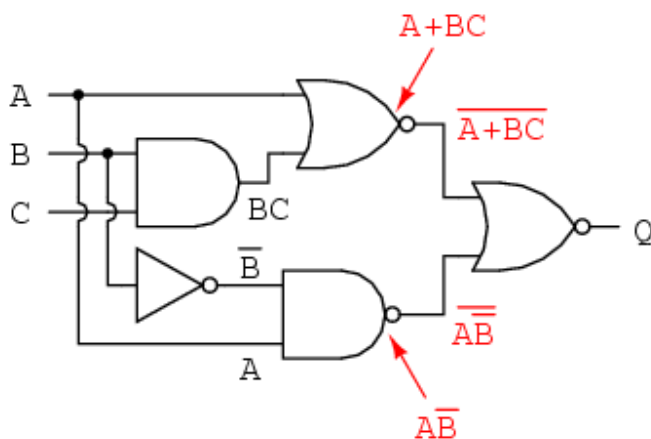
Let's apply the principles of DeMorgan's theorems to the simplification of a gate circuit:



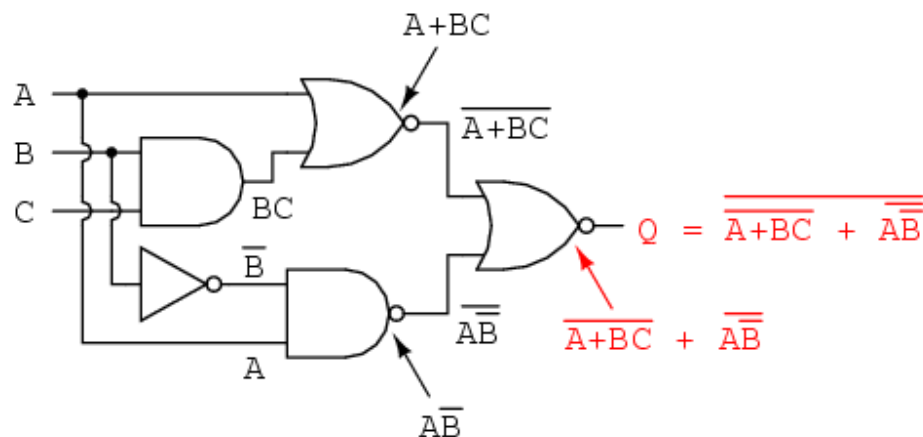
As always, our first step in simplifying this circuit must be to generate an equivalent Boolean expression. We can do this by placing a sub-expression label at the output of each gate, as the inputs become known. Here's the first step in this process:



Next, we can label the outputs of the first NOR gate and the NAND gate. When dealing with inverted-output gates, I find it easier to write an expression for the gate's output without the final inversion, with an arrow pointing to just before the inversion bubble. Then, at the wire leading out of the gate (after the bubble), I write the full, complemented expression. This helps ensure I don't forget a complementing bar in the sub-expression, by forcing myself to split the expression-writing task into two steps:



Finally, we write an expression (or pair of expressions) for the last NOR gate:



Now, we reduce this expression using the identities, properties, rules, and theorems (DeMorgan's) of Boolean algebra:

$$\begin{aligned}
 & \overline{\overline{A + BC} + \overline{A'B}} \\
 & \quad \downarrow \text{Breaking longest bar} \\
 & \overline{(\overline{A + BC})} \quad \overline{(\overline{A'B})} \\
 & \quad \downarrow \text{Applying identity } \overline{\overline{A}} = A \text{ wherever double bars of equal length are found} \\
 & (A + BC) (\overline{A'B}) \\
 & \quad \downarrow \text{Distributive property} \\
 & A\overline{A'B} + BC\overline{A'B} \\
 & \quad \downarrow \text{Applying identity } \overline{AA} = A \text{ to left term; applying identity } \overline{AA} = 0 \text{ to B and } \overline{B} \text{ in right term} \\
 & \overline{A'B} + 0 \\
 & \quad \downarrow \text{Applying identity } A + 0 = A \\
 & \overline{A'B}
 \end{aligned}$$

The equivalent gate circuit for this much-simplified expression is as follows:

## DEMORGAN'S THEOREMS

## UNIT-2

