In the following α , β , γ , and δ are strings of zero or more terminals and/or <nonterminals>

Left Recursion

Left Factorization

$$A \rightarrow \alpha\beta \mid \alpha\gamma \mid \delta$$

Rewrite by combining the common prefix:

$$A \rightarrow \alpha A' \mid \delta$$
$$A' \rightarrow \beta \mid \gamma$$

Nullable

$$\begin{array}{lll} \textit{Nullable}(\epsilon) & = & \textit{true} \\ \textit{Nullable}(a) & = & \textit{false} \\ \textit{Nullable}(\alpha\beta) & = & \textit{Nullable}(\alpha) \land \textit{Nullable}(\beta) \\ \textit{Nullable}(N) & = & \textit{Nullable}(\alpha_1) \lor \ldots \lor \textit{Nullable}(\alpha_n), \\ & & \textit{where the productions for N are} \\ & & \textit{N} \rightarrow \alpha_1, \ \ldots, \ \textit{N} \rightarrow \alpha_n \end{array}$$

where a is a terminal, N is a nonterminal, α and β are sequences of grammar symbols and ϵ represents the empty sequence of grammar symbols.

First Sets

$$\begin{array}{lll} \mathit{FIRST}(\epsilon) & = & \emptyset \\ \mathit{FIRST}(\mathtt{a}) & = & \{\mathtt{a}\} \\ \mathit{FIRST}(\alpha\beta) & = & \left\{ \begin{array}{ll} \mathit{FIRST}(\alpha) \cup \mathit{FIRST}(\beta) & \mathit{if Nullable}(\alpha) \\ \mathit{FIRST}(\alpha) & \mathit{if not Nullable}(\alpha) \\ \mathit{FIRST}(N) & = & \mathit{FIRST}(\alpha_1) \cup \ldots \cup \mathit{FIRST}(\alpha_n) \\ & \mathit{where the productions for N are} \\ N \rightarrow \alpha_1, & \ldots, & N \rightarrow \alpha_n \end{array} \right.$$

Follow Sets

FOLLOW(A) algorithm:

If A=S then put end marker (e.g., EOFtk) into FOLLOW(A) and continue

Find all productions with A on rhs: Q -> $\alpha A\beta$

- if β begins with a terminal q then q is in FOLLOW(A)
- if β begins with a nonterminal then FOLLOW(A) includes FIRST(β) $\{\epsilon\}$
- if $\beta = \epsilon$ or when β is nullable then include FOLLOW(Q) in FOLLOW(A)

Course grammar:

```
<S>
       ->
           program <V> <B>
<B>
           begin < V > < Q > end
      ->
<V>
           empty | var identifier . <V>
      ->
           <H> + <M> | <H> - <M> | <H> / <M> | <H> * <M> | <H>
<M>
<H>
            & < R > | < R >
       ->
            identifier | number
<R>
       ->
            <T># <Q> | empty
<Q>
       ->
<T>
            <A>, |<W>, |<B> |<I>, |<G>, |<E>,
       ->
            scan identifier | scan number
<A>
       ->
<W>
       ->
            write <M>
            if [ <M> <Z> <M> ] <T>
<I>
        ->
            repeat [ <M > <Z > <M > ] <T >
<G>
       ->
<E>
       -> let identifier : <M>
<Z>
            < | > | : | = | ==
       ->
```

Grammar semantics:

- Delimiters:
 - o .,#[]
- Less than and greater than are standard symbols
 - o <>
- '&' means absolute value for number
 - o Number may be immediate or stored at location indicated by identifier
- The following operators have standard definitions, except no precedence
 - 0 +-*/
 - Operations are applied from left to right
- 'let identifier: M' assigns the value of M to the given identifier
- '=' and '==' are both treated like standard '=='
 - o Assignment of values to variables can only be performed using '[let] identifier : M'
- 'if [M Z M] T'
 - Means to do T if and only if (M Z M) is true (always true if Z is ':')
- 'repeat [M Z M] T'
 - o Means to repeat T until (M Z M) is false
- When scanning an identifier, allocate memory using the identifier as the variable name and set value to zero
- When scanning a number, allocate memory using a temporary variable name and set value to the number

Assembly Language:

| 0 | BR | (1, jump to arg) |
|---|--------|-----------------------------------|
| 0 | BRNEG | (1, jump to arg if ACC <0) |
| 0 | BRZNEG | (1, jump to arg if ACC <=0) |
| О | BRPOS | (1, jump to arg if ACC >0) |
| 0 | BRZPOS | (1, jump to arg if ACC >=0) |
| 0 | BRZERO | (1, jump to arg if ACC ==0) |
| 0 | COPY | (2, arg1 = arg2) |
| 0 | ADD | (1, ACC = ACC +arg |
| О | SUB | (1, ACC = ACC - arg) |
| 0 | DIV | (1, ACC = ACC / arg) |
| 0 | MULT | (1, ACC = ACC * arg) |
| 0 | READ | (1, arg=input integer) |
| 0 | WRITE | (1, put arg to output as integer) |
| 0 | STOP | (0, stop program) |
| 0 | STORE | (1, arg = ACC) |
| 0 | LOAD | (1, ACC=arg) |
| О | NOOP | (0, nothing) |
| | | |

Virtual Machine Architecture:

