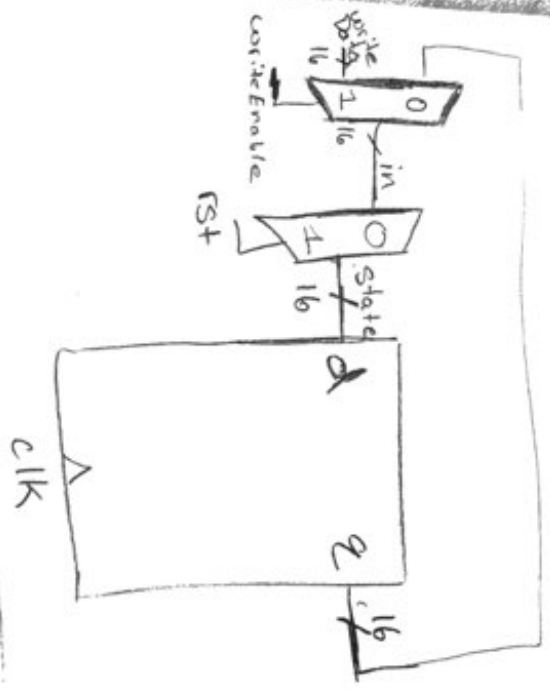
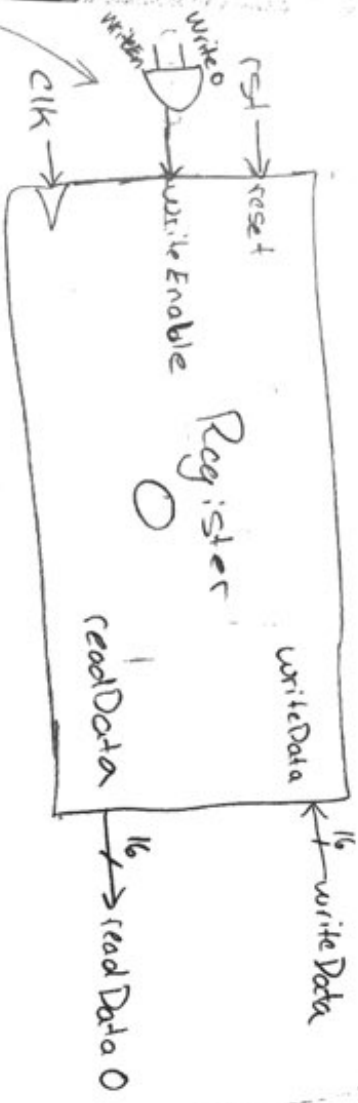


16 bit DFF

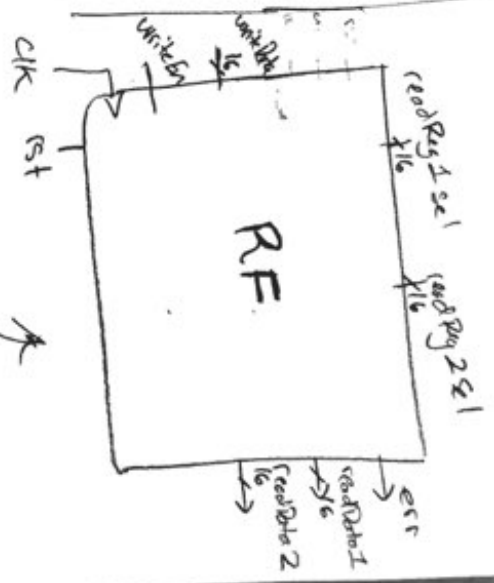
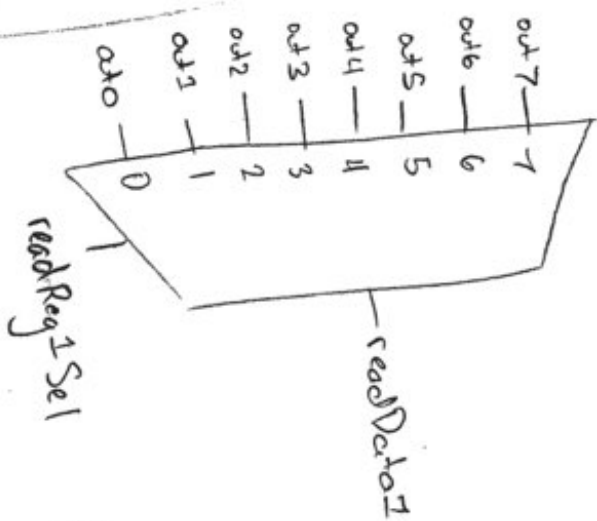
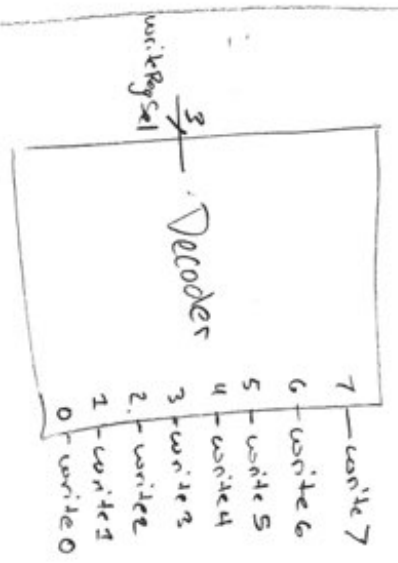


8 instantiations of 16 bit DFF



∴ X8 modules.

AND with writeEn



input write signals not shown

Same for readData2

Use bitwise OR on all inputs and see if equals X. If so, err is high.