

<u>Cycle</u>	<u>Inst</u>	<u>Reason</u>
1		
2		
3		
4		
5	lbi R0, 0	
6	lbi R5, 43	
7	lbi R6, 43	no forwarding + dependent instruction + RF bypassing
8	lbi R7, 43	no forwarding + dependent instruction + RF bypassing
9	ld R1, R0, 0	
10		
11		no forwarding + dependent instruction + RF bypassing
12	st R5, R1, 0	no forwarding + dependent instruction + RF bypassing
13	ld R1, R0, 2	
14		
15		no forwarding + dependent instruction + RF bypassing
16	st R6, R1, 1	no forwarding + dependent instruction + RF bypassing
17	ld R1, R0, 4	
18		
19		
20	st R7, R1, 1	
21	halt	

[illegible]