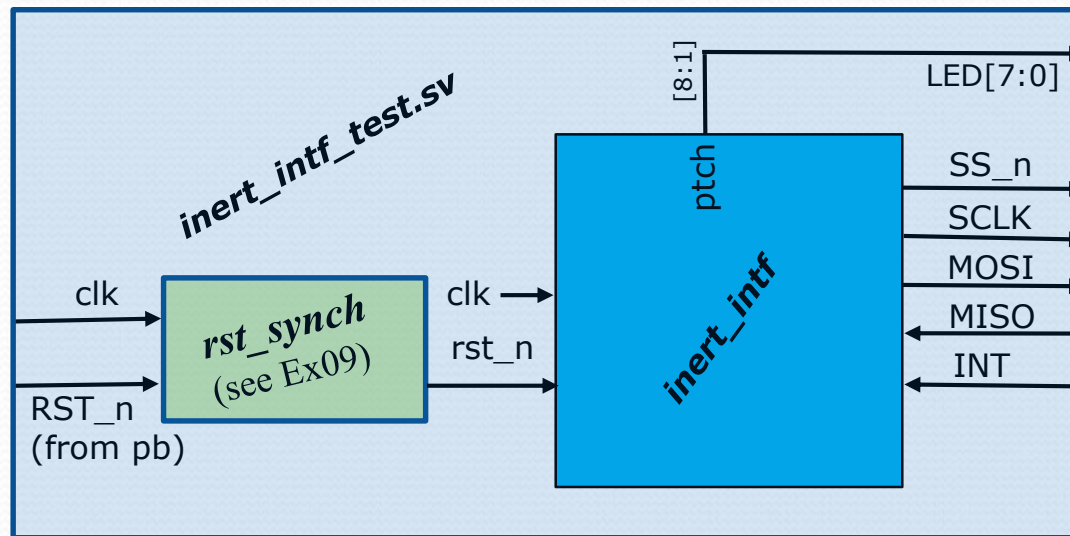


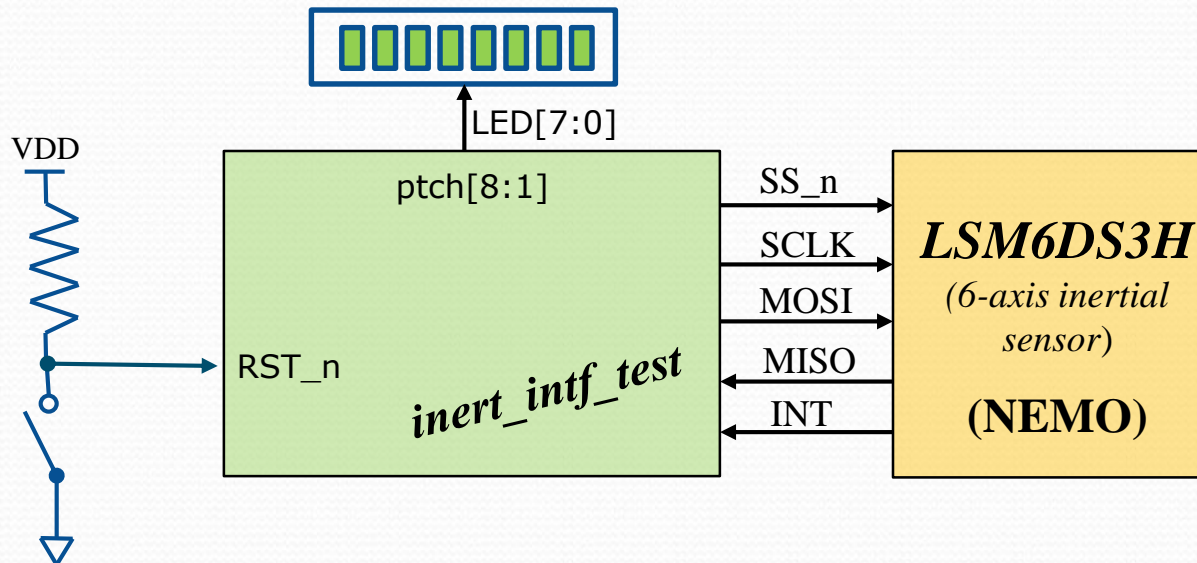
Exercise 22: Mapping inert_intf to DE0 Nano

- In exercise 21 you finished inert_intf.sv and tested it. Now you will map it to the DE0-Nano board so you can test it “for real”.



Exercise 22: Mapping inert_intf_test to DE0 Nano

Signal:	Dir:	Description:
clk	in	50MHz clock
RST_n	in	Unsynchronized input from push button
LED[7:0]	out	Bits [8:1] of ptch
<i>SPI Intf</i>	out/ in	The SS_n, SCLK, MOSI, MISO and INT of SPI interface to 6-axis inertial sensor



Exercise 22: Mapping inert_intf_test to DE0 Nano

- There are Quartus project file and settings file available for download: (**inert_intf_test.qpf**, **inert_intf_test.qsf**).
- Open the .qpf and **ensure you add** all necessary files to the project.
- Ensure the project builds with no errors
- Once it does program your DE0-Nano and call Fego or Eric over to demo.

