E85: Digital Design and Computer Engineering Problem Set 1

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A)		te the powers of 2 from 2^0 to 2^{16} . Commit these numbers to memory because you will use them uently in digital design.
B)		e conversions. If you do these properly, you shouldn't have any difficult arithmetic when verting between bases 2, 8, and/or 16.
	i.	Convert the following numbers to base 2. $13_{10},87_{10},1000_{10},5_8,654_8,B_{16},FEED_{16}$
	ii.	Convert the following numbers to base 10. $1001_2, 1100100_2, 5_8, 654_8, B_{16}, FEED_{16}$
	iii.	Convert the following numbers to base 16. $1001_2, 1100100_2, 5_8, 654_8, 17_{10}, 200_{10}, 1000_{10}$
C)	Nun	nber systems
	i.	Convert the following numbers to 8-bit 2's complement and sign-magnitude format: $69_{10}, -2_{10}, -37_{10}$
	ii.	Convert the following 6-bit 2's complement numbers to base 10: $100100_2,011111_2$
	iii.	Convert the following 6-bit sign-magnitude numbers to base 10: $100100_2,011111_2$
	iv.	Write the most positive and most negative 8-bit numbers in binary and decimal for each of the following formats: unsigned, 2's complement, sign-magnitude.
D)	Arit	hmetic:
	i.	Assuming unsigned format:
		a) Compute $1010_2 + 0111_2$. Convert the addends and the sum to decimal and check your results.
		b) Extend 101111 ₂ to 8 bits properly. Convert the input and result to decimal and check your result.

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0)	Toris makes
2)	Logic gates
	Write the symbol, Boolean equation, truth table, and Verilog code for a 3-input NAND gate.
3)	Impact on Society

workload.