

Kwonyoung Kim

JOINT M.S & PH.D. STUDENT · YONSEI UNIVERSITY

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Research Interests

Computer Vision, Deep learning, Depth Estimation, Meta-learning, Light-weight Model, Multi-modal

Work Experience

Neurocle Inc.

SOFTWARE ENGINEER INTERN

- Implemented Class Activation Map function for users and improved performance of the product.

Seoul, S.Korea

Jun. 2019 - Jan. 2020

Education

Yonsei University

JOINT M.S & PH.D. CANDIDATE, ELECTRICAL AND ELECTRONIC ENGINEERING

- Supervisor: Prof. Kwanghoon Sohn
- Overall GPA: 4.09/4.3

Seoul, S.Korea

Mar. 2020 - Present

Yonsei University

B.S. IN ELECTRICAL AND ELECTRONIC ENGINEERING

- Major: Electrical and Electronic Engineering
- Overall GPA: 3.39/4.3

Seoul, S.Korea

Mar. 2012 - Jan. 2020

Publications

“PointFix: Learning to Fix Domain Bias for Robust Online Stereo Adaptation”

KWONYOUNG KIM, JUNGIN PARK, JIYOUNG LEE, DONGBO MIN, KWANGHOON SOHN

- European Conference on Computer Vision (ECCV 2022).

Jul. 2022

Patent

Method and Device for Robust Depth Estimation to Domain Shift

KWONYOUNG KIM, KWANGHOON SOHN

- Korea Patent, 10-2021-0177205

Dec. 2021

Research Experiences

To create AI systems that act appropriately and effectively in novel situations that occur in open worlds

INSTITUTE FOR INFORMATION & COMMUNICATIONS TECHNOLOGY PROMOTION (IITP), KOREA

- Developed an algorithm for Online Stereo Adaptation
- Data acquisition with delivery robot and pre-processing.

Seoul, S.Korea

Apr. 2020 - Dec. 2021

Teaching Assistants

DEPT. OF ELECTRICAL AND ELECTRONIC ENGINEERING, YONSEI UNIVERSITY

- Signals and System, Fall, 2020
- Lab. Internship assistant, Summer, 2021
- Signals and System, Fall, 2021
- Electrical and Electronic Engineering Capstone Design, Fall, 2022

Seoul, S.Korea

Skills

Programming Python, MATLAB, C/C++

Deep learning Pytorch, Tensorflow

Languages Korean, English