

Ising machines as hardware solvers of combinatorial optimization problems

by Naeimeh Mohseni, Peter L. McMahon, and Tim Byrnes
(paper review)

Hongyu Hè

hhy@princeton.edu

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HP-Hard Problems

- Traveling salesman problem (TSP)
- Bin packing/Knapsack
- Boolean satisfiability (SAT)
- Graph coloring
- Subset sum
- Max cut
- ...

Non-digital approaches to
solving them?

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- Graph coloring
- Subs
- Max
- ...

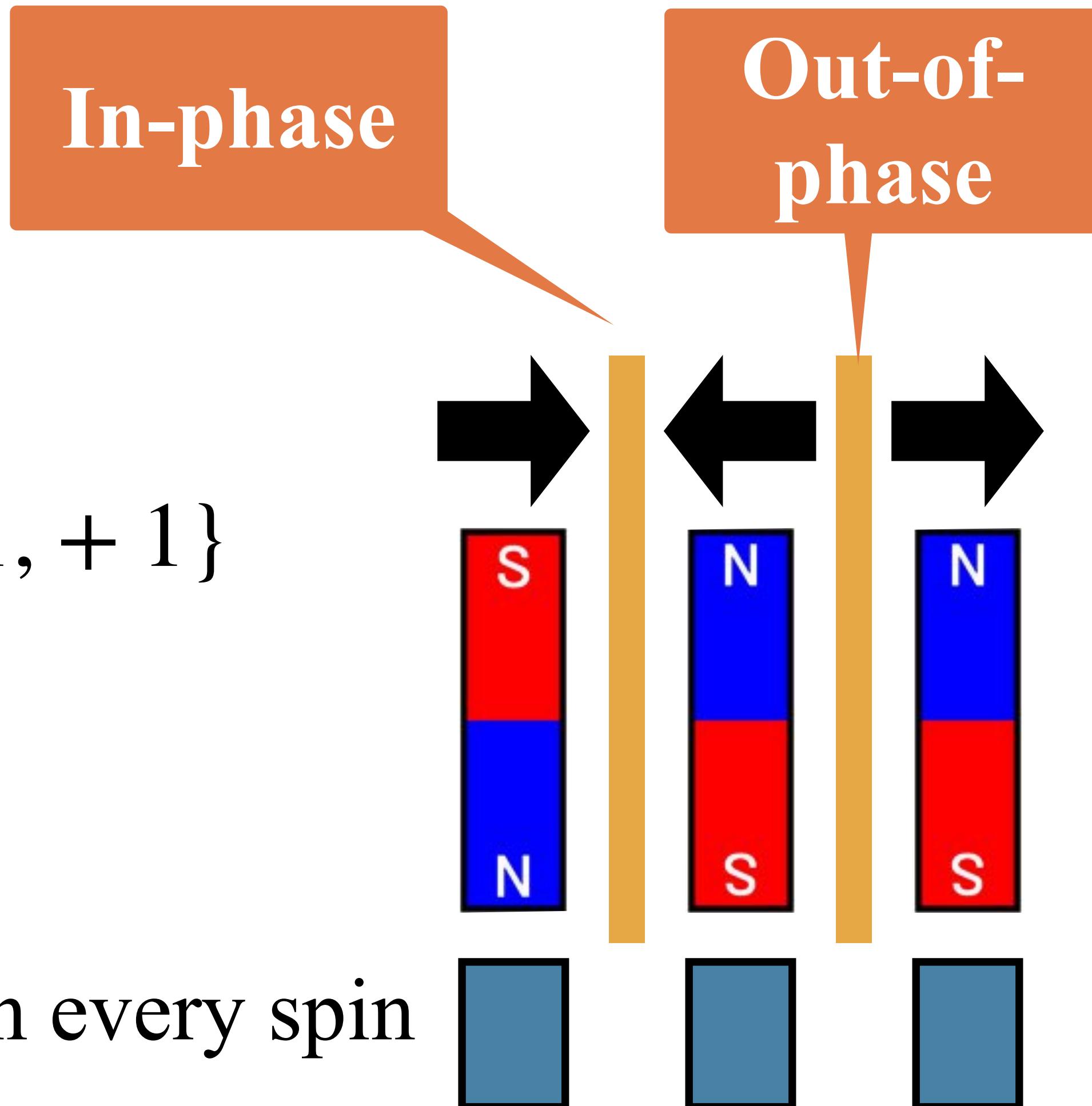
Non-digital approaches to solving them?

Can be mapped to ‘Ising Hamiltonian’ in polynomial time!

Ising Hamiltonian

$$H = - \sum_{i,j=1}^N J_{i,j} \cdot \delta_i \delta_j - \sum_{i=1}^N h_i \delta_i$$

- “Spins”: $\vec{\delta} \triangleq (\delta_1, \delta_2, \dots, \delta_N)$ where $\delta_k \in \{-1, +1\}$
- Interaction $\delta_i \delta_j$: Bistable energy states
- Connectivity/Coupling: $J \in \mathbb{R}^{N \times N}$
- Bias terms \vec{h} : The external energy applied on every spin

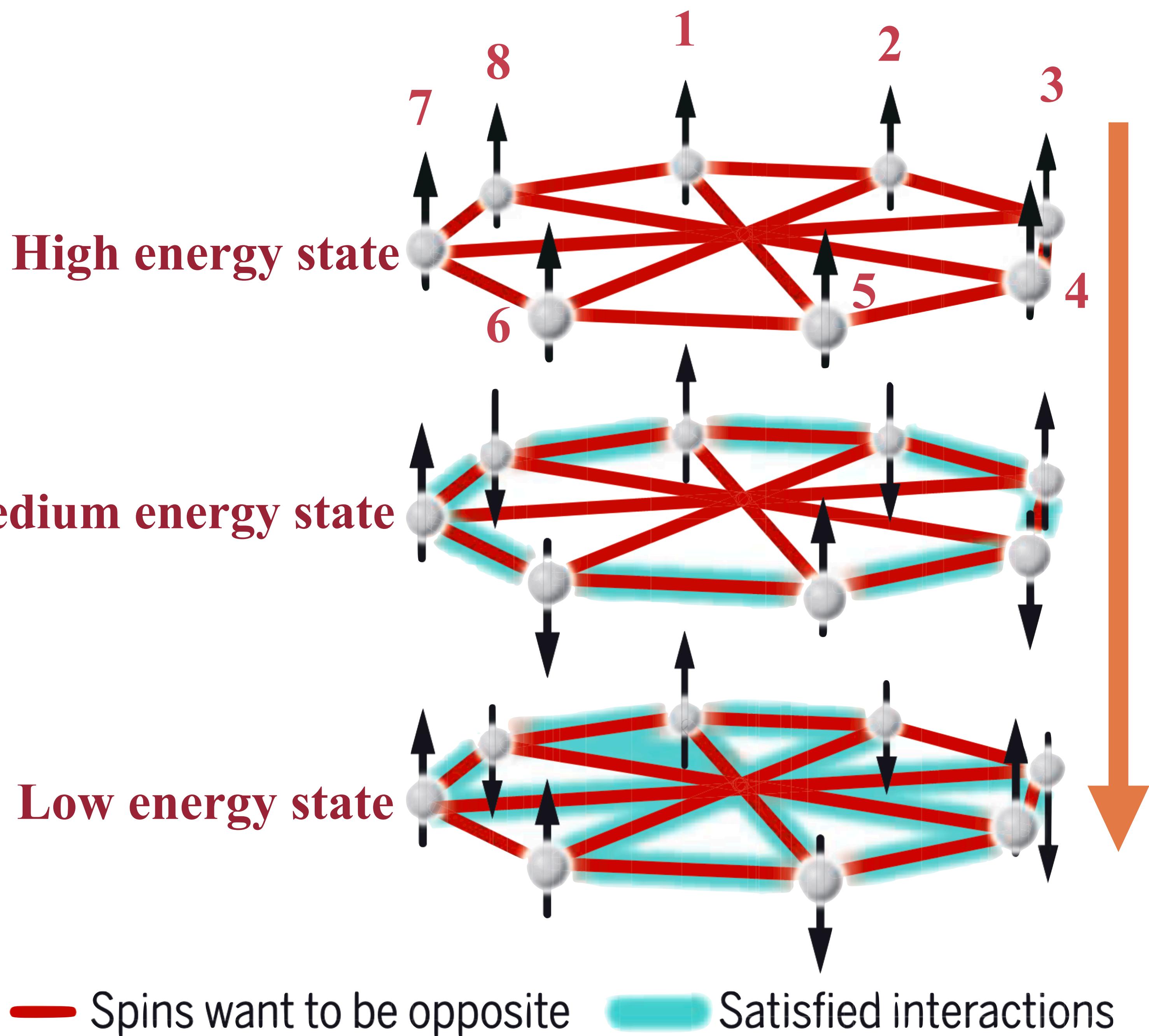


Ising Hamiltonian

$$H = - \sum_{i,j=1}^N J_{i,j} \cdot \delta_i \delta_j$$

$$J = -$$

1	2	3	4	5	6	7	8	1
0	1	0	0	1	0	0	1	2
0	0	1	0	0	1	0	0	3
0	0	0	1	0	0	1	0	4
0	0	0	0	1	0	0	1	5
0	0	0	0	0	1	0	0	6
0	0	0	0	0	0	1	0	7
0	0	0	0	0	0	0	1	8
0	0	0	0	0	0	0	0	0



Ising Hamiltonian

$$H = - \sum_{i,j=1}^N J_{i,j} \cdot \delta_i \delta_j$$

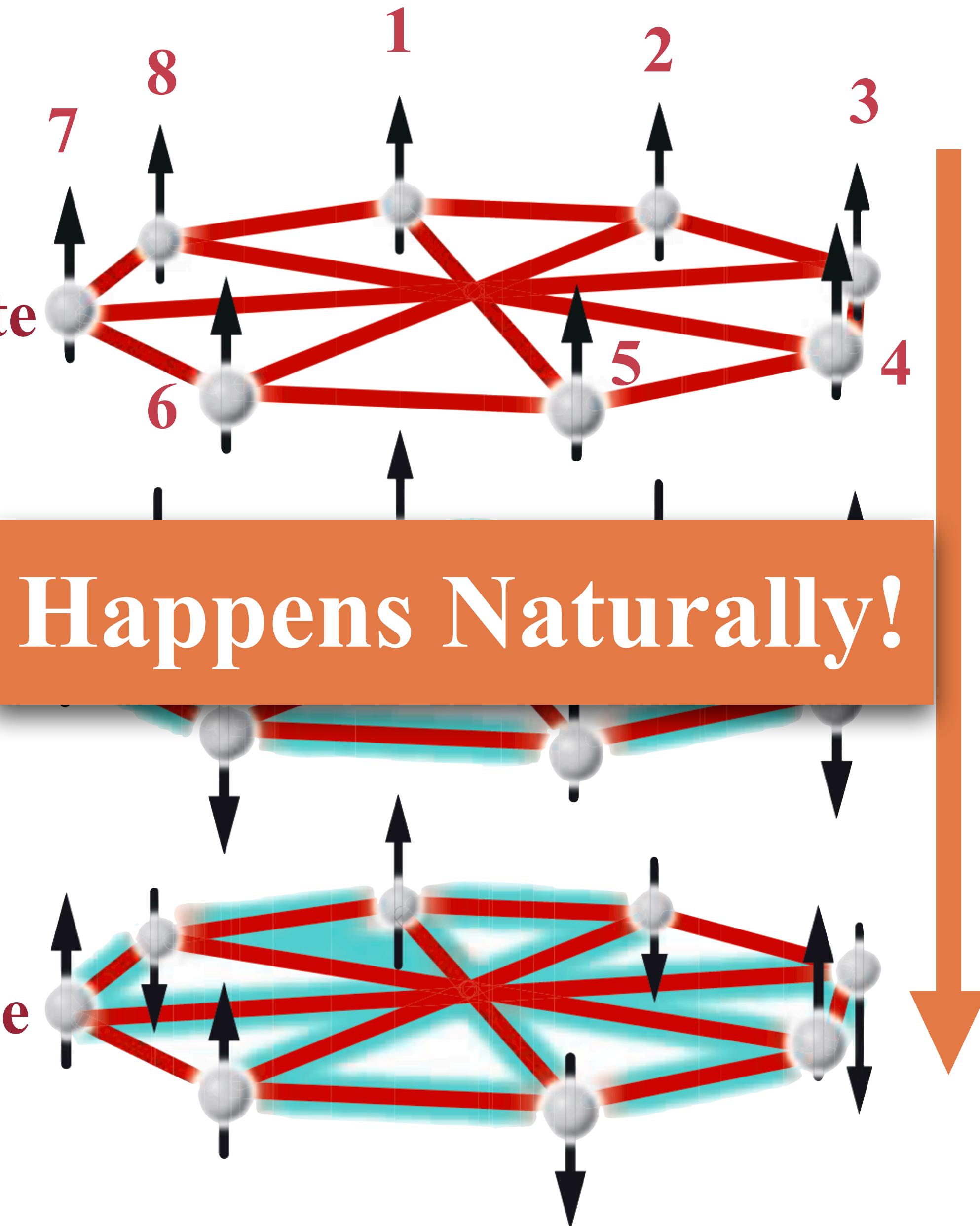
$$J = -$$

1	2	3	4	5	6	7	8	1
0	1	0	0	1	0	0	1	2
0	0	1	0	0	1	0	0	3
0	0	0	1	0	0	1	0	4
0	0	0	0	1	0	0	1	5
0	0	0	0	0	1	0	0	6
0	0	0	0	0	0	1	0	7
0	0	0	0	0	0	0	1	8
0	0	0	0	0	0	0	0	0

High energy state

Medium energy state

Low energy state

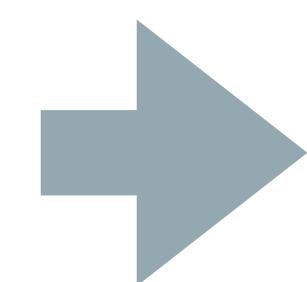


— Spins want to be opposite

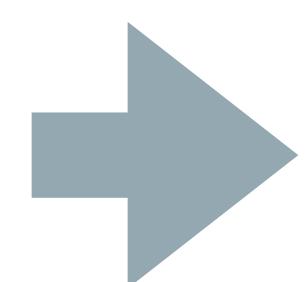
Cyan Satisfied interactions

Ising Machine

Ising
Hamiltonian

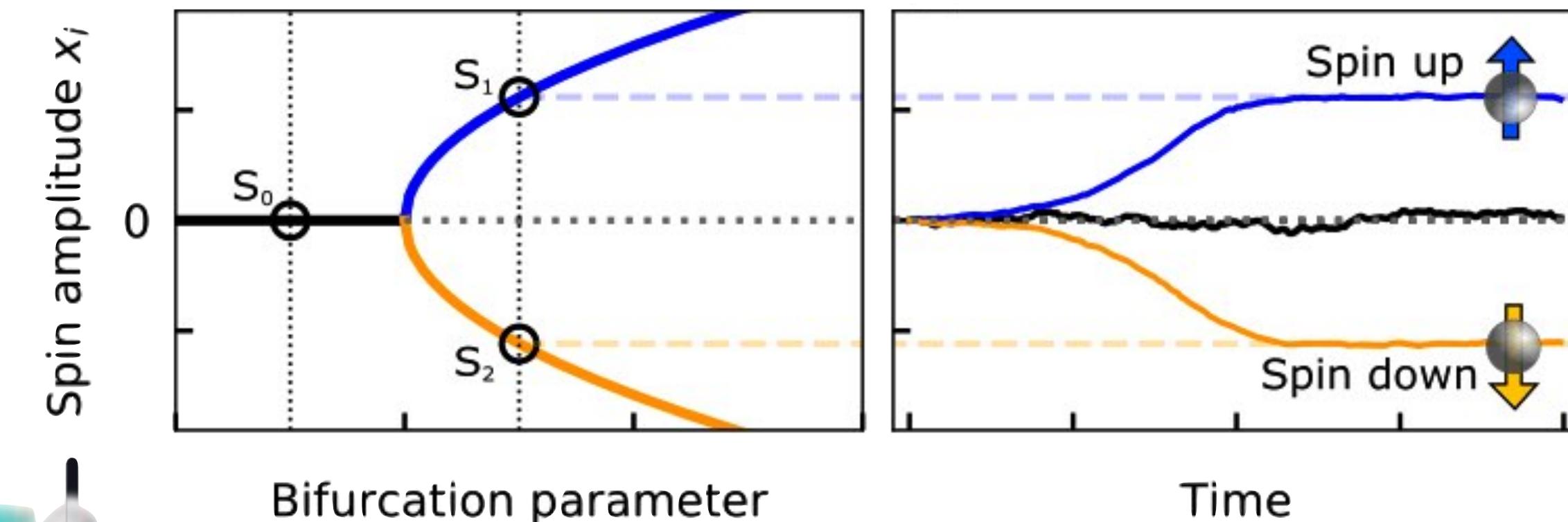
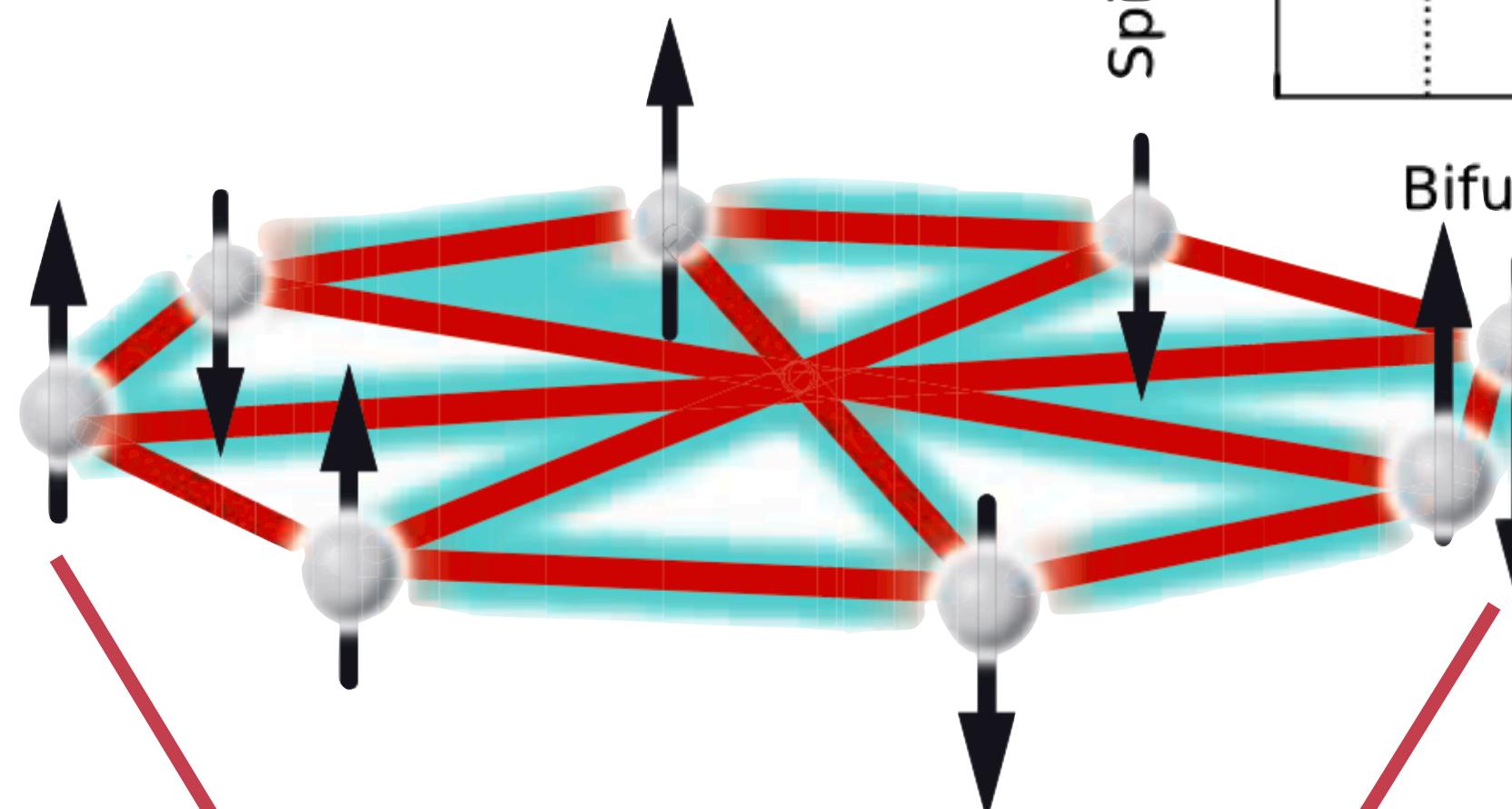


Physical
System



Measurement

Energy



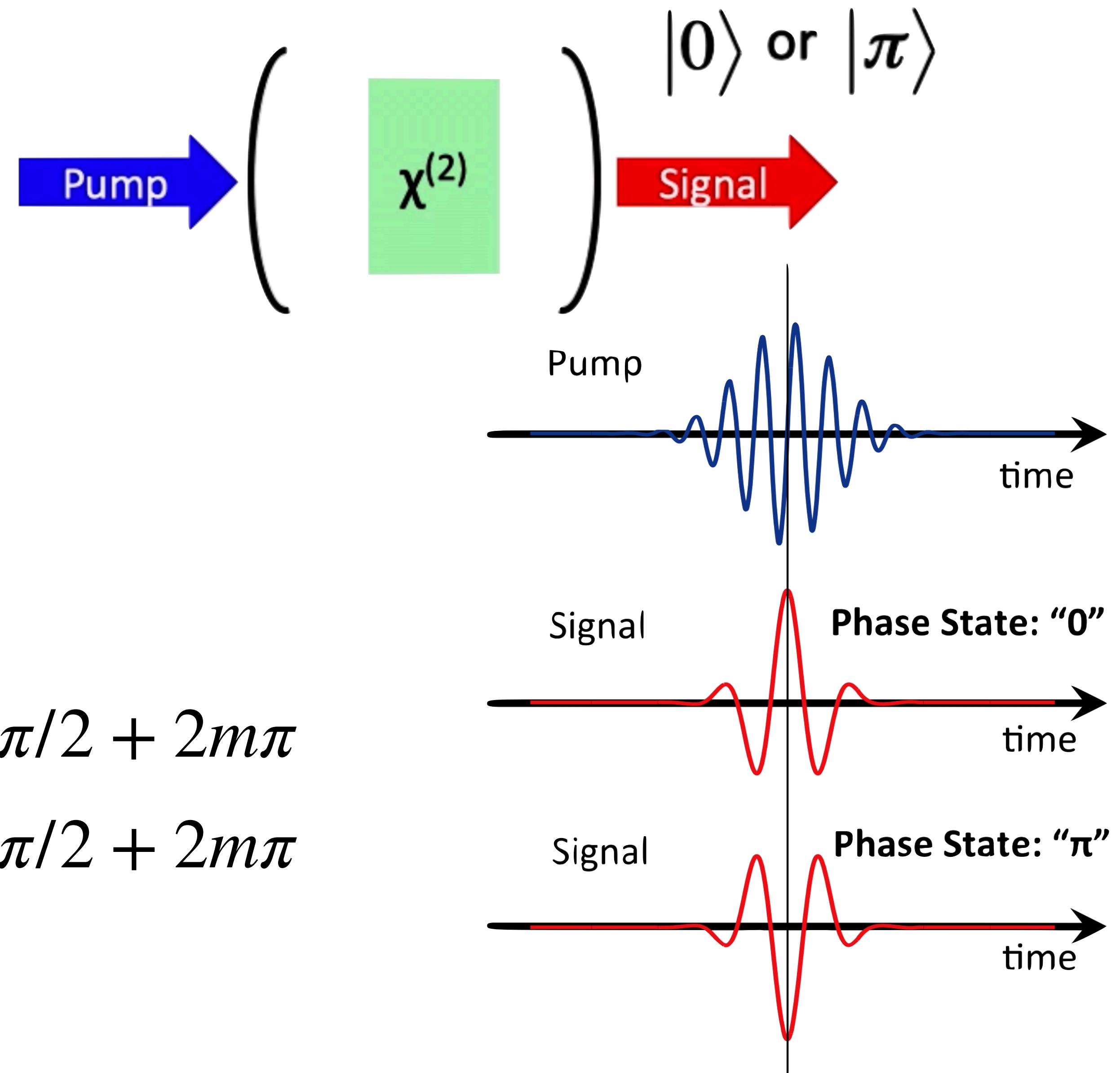
[Fabian Böhm et al., Communications Physics '21]

Oscillator-Based Ising Machines

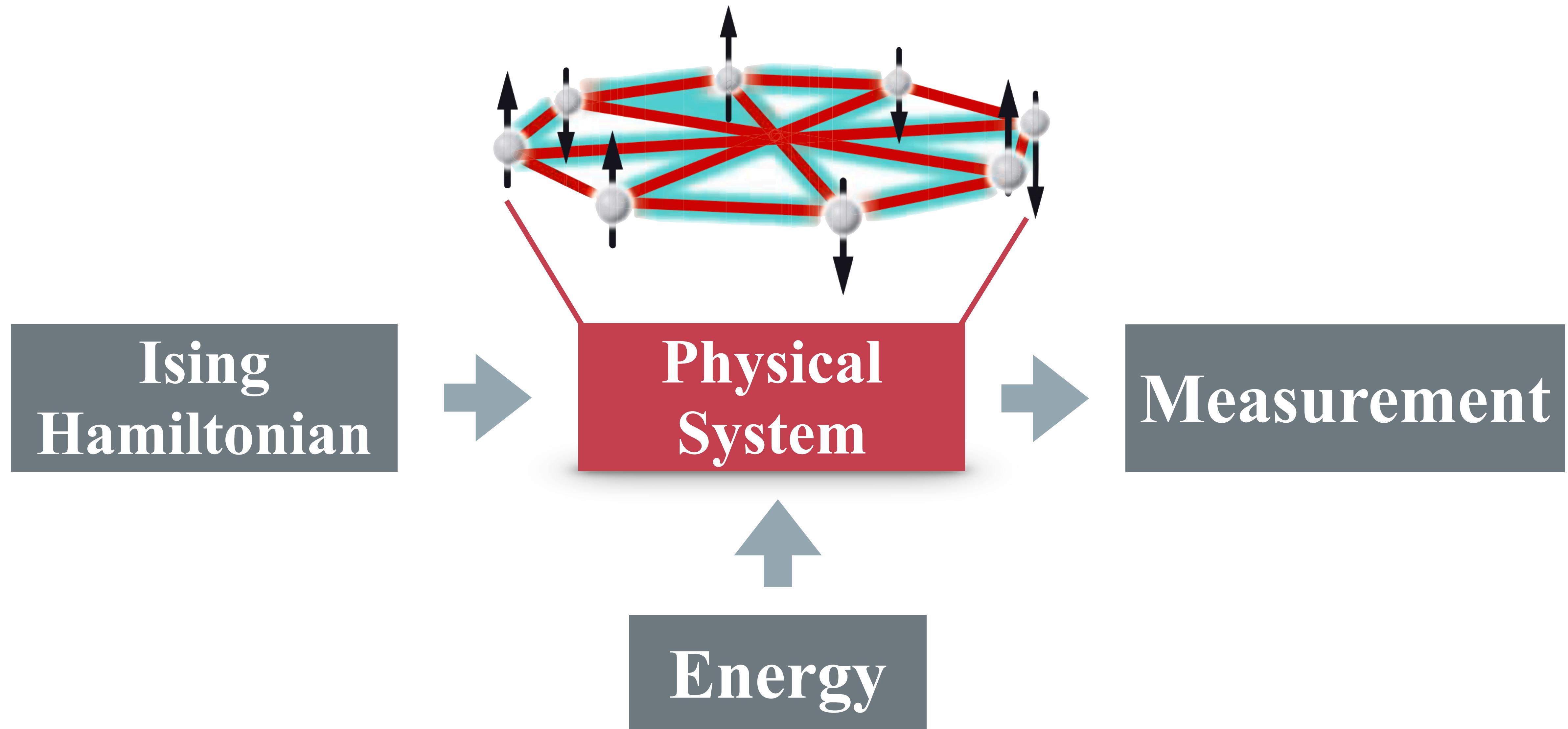
Degenerate Optical Parametric Oscillator (DOPO)

- $\vec{\delta}$: Phases of the oscillators
 - Resonant at ω_s
 - Bi-phase states: $|0\rangle$ or $|\pi\rangle$
- Properties
 - Frequency locking: $\omega_p = 2 \cdot \omega_s$
 - Phase locking:
$$\phi_p = 2(\phi_s + 0) + \pi/2 + 2m\pi$$

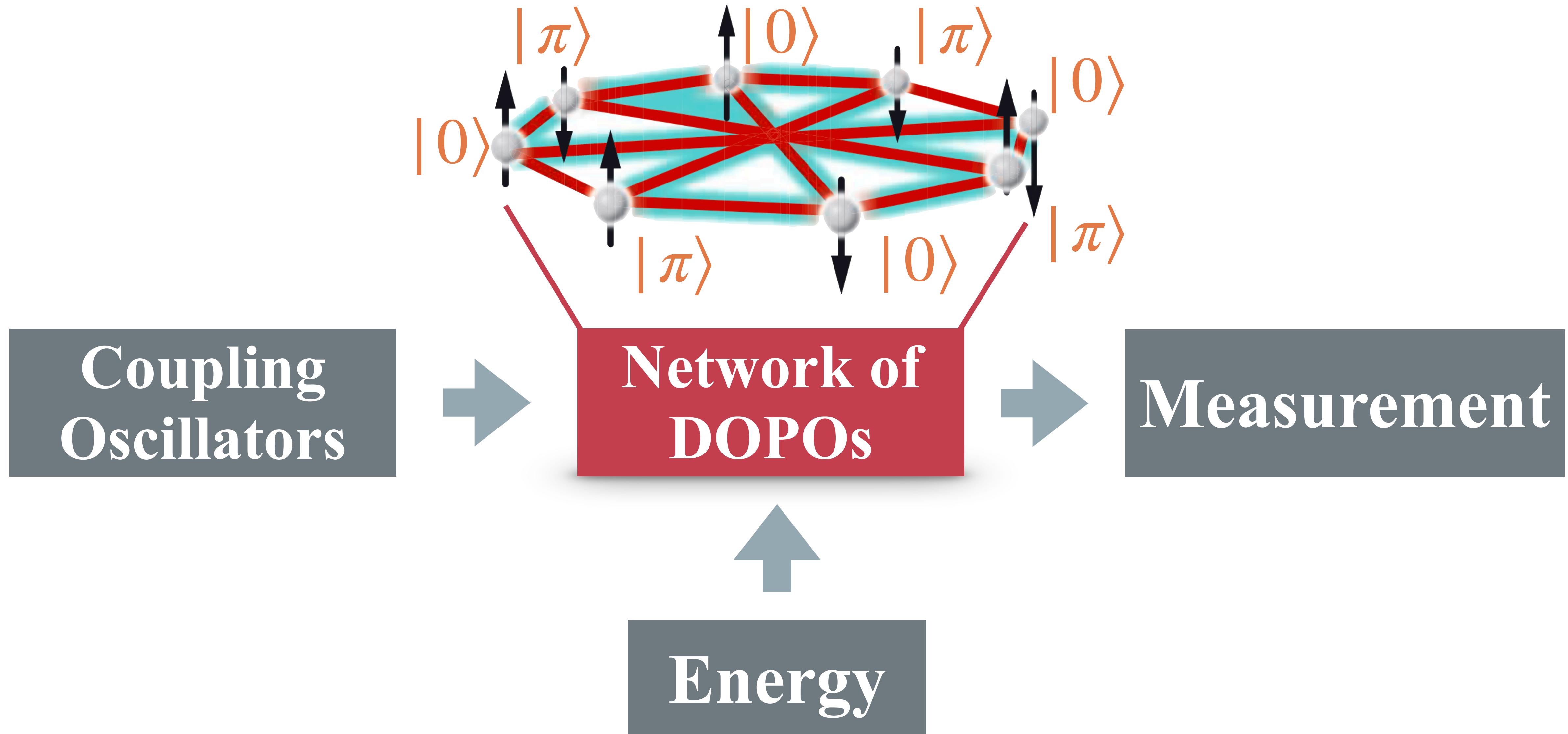
$$\phi_p = 2(\phi_s + \pi) + \pi/2 + 2m\pi$$



Coherent Ising Machine (CIM)



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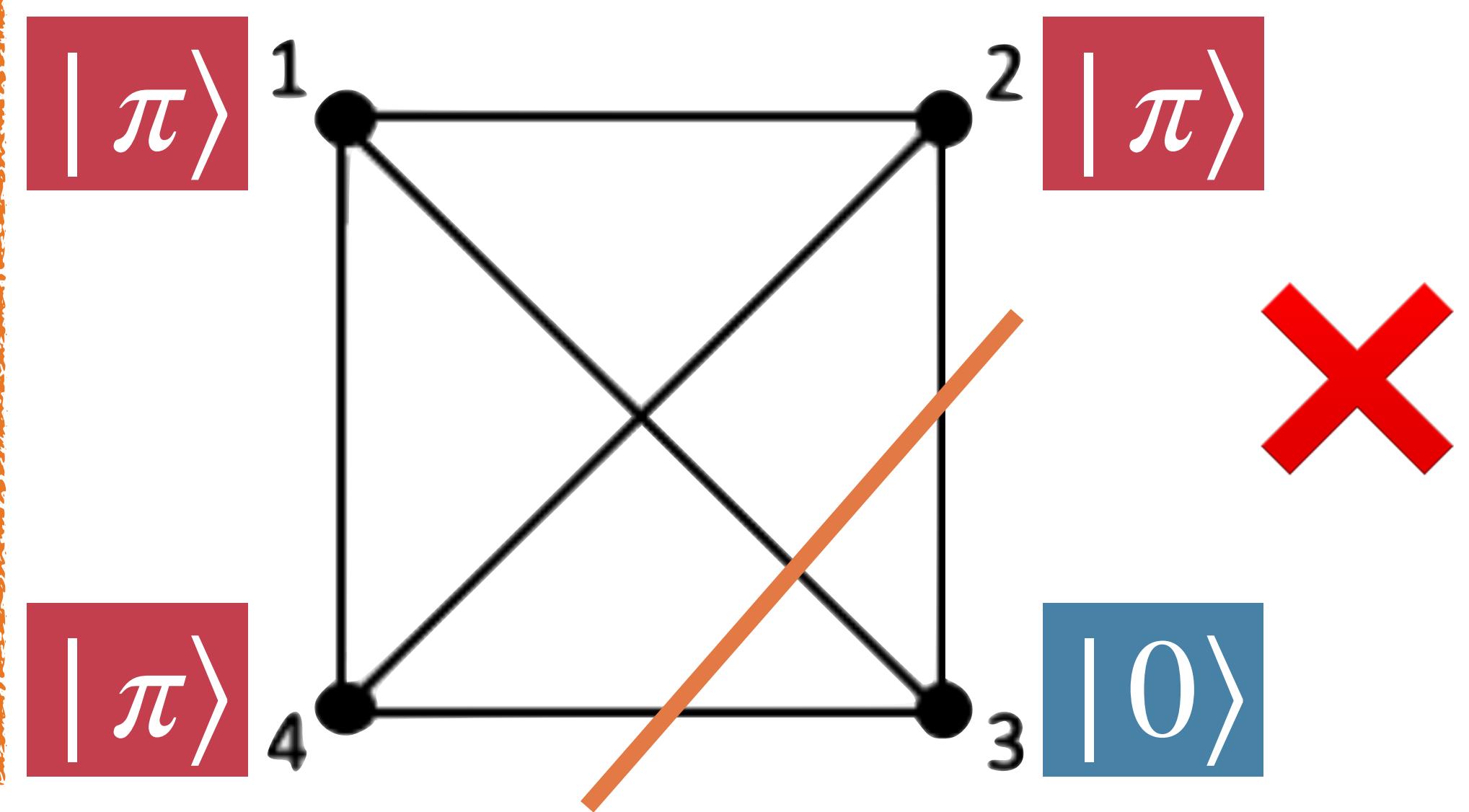
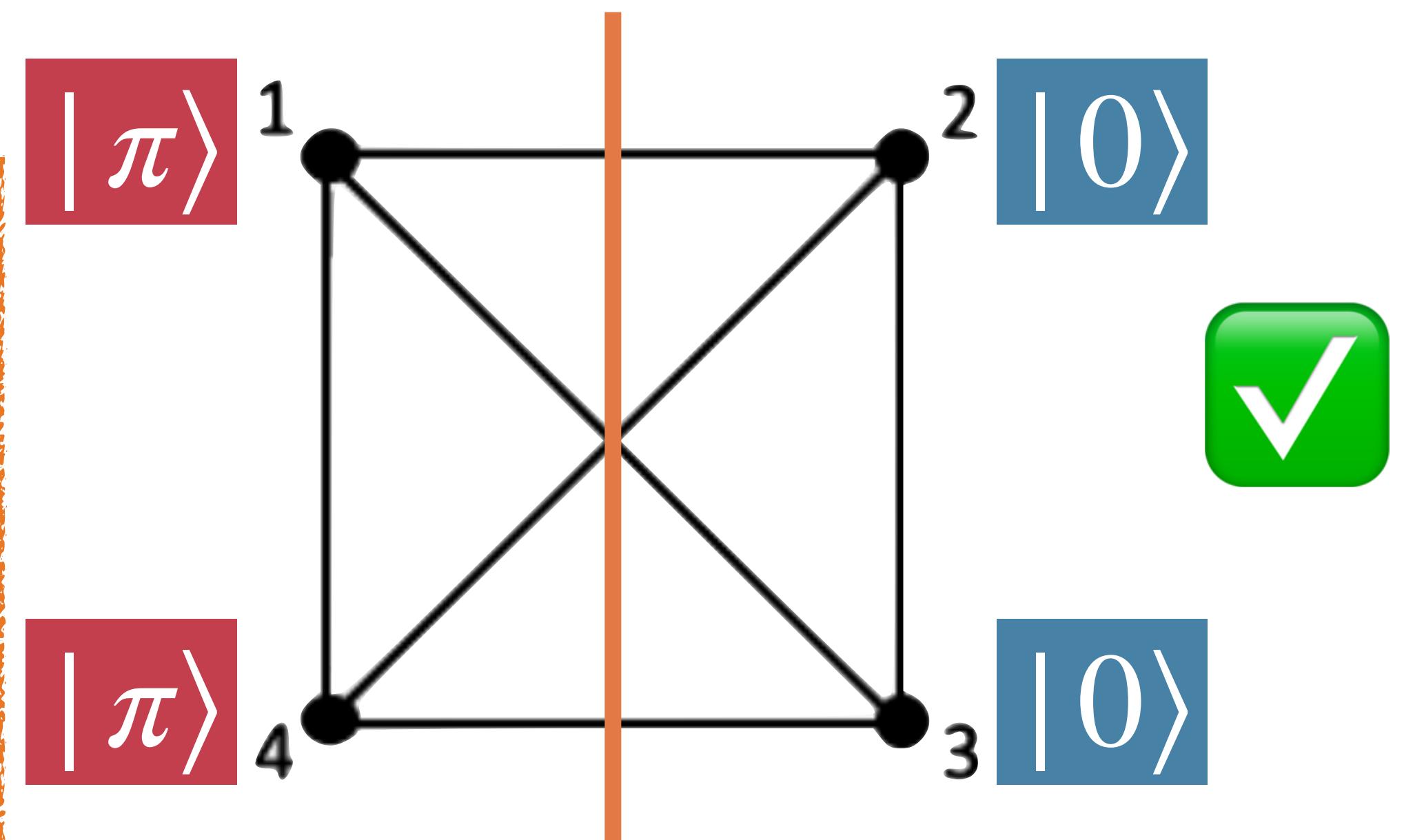
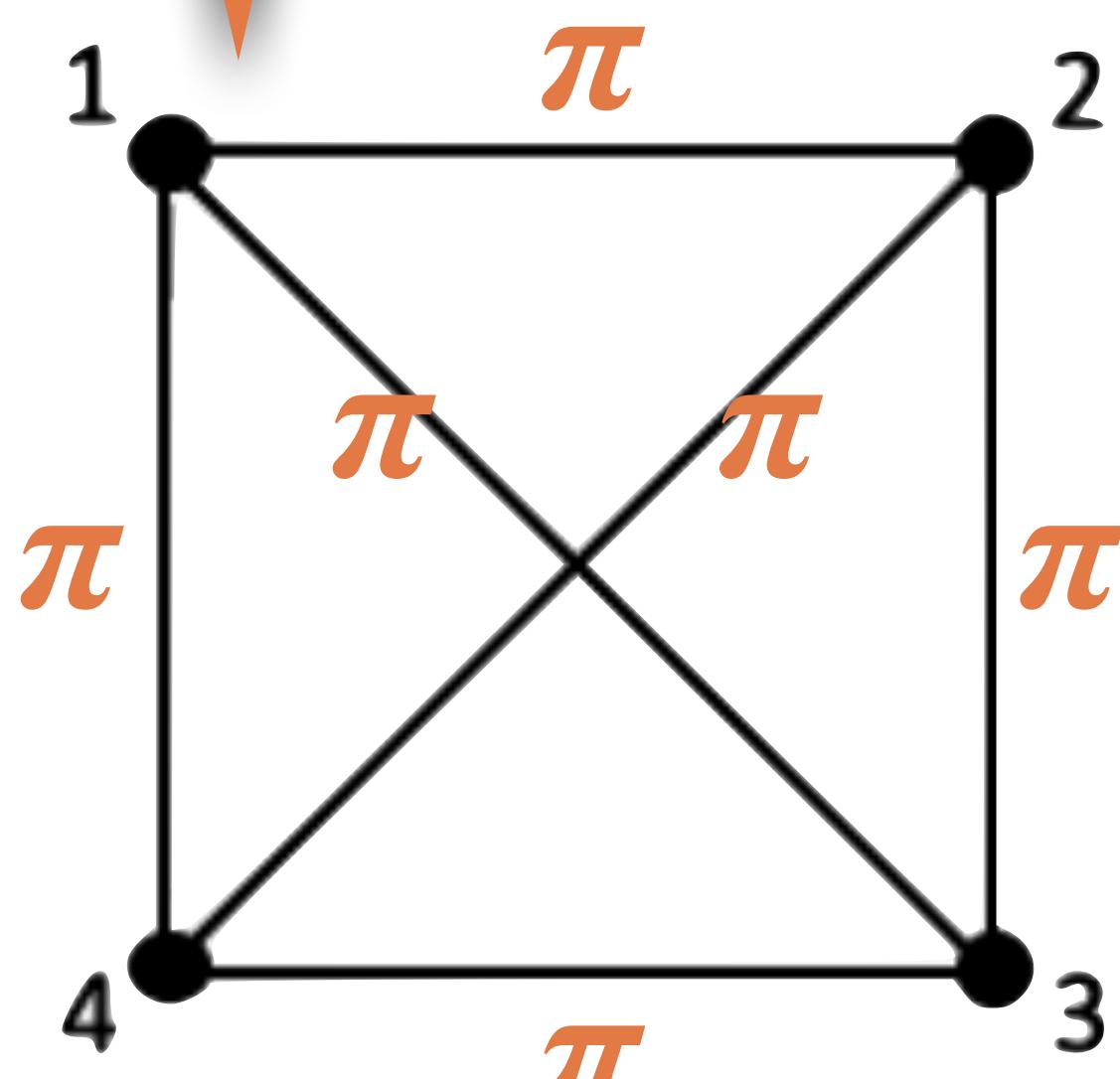
Mapping Max Cut to CIM

$$H = - \sum_{i,j=1}^N J_{i,j} \cdot \delta_i \delta_j$$

Initial coupling:

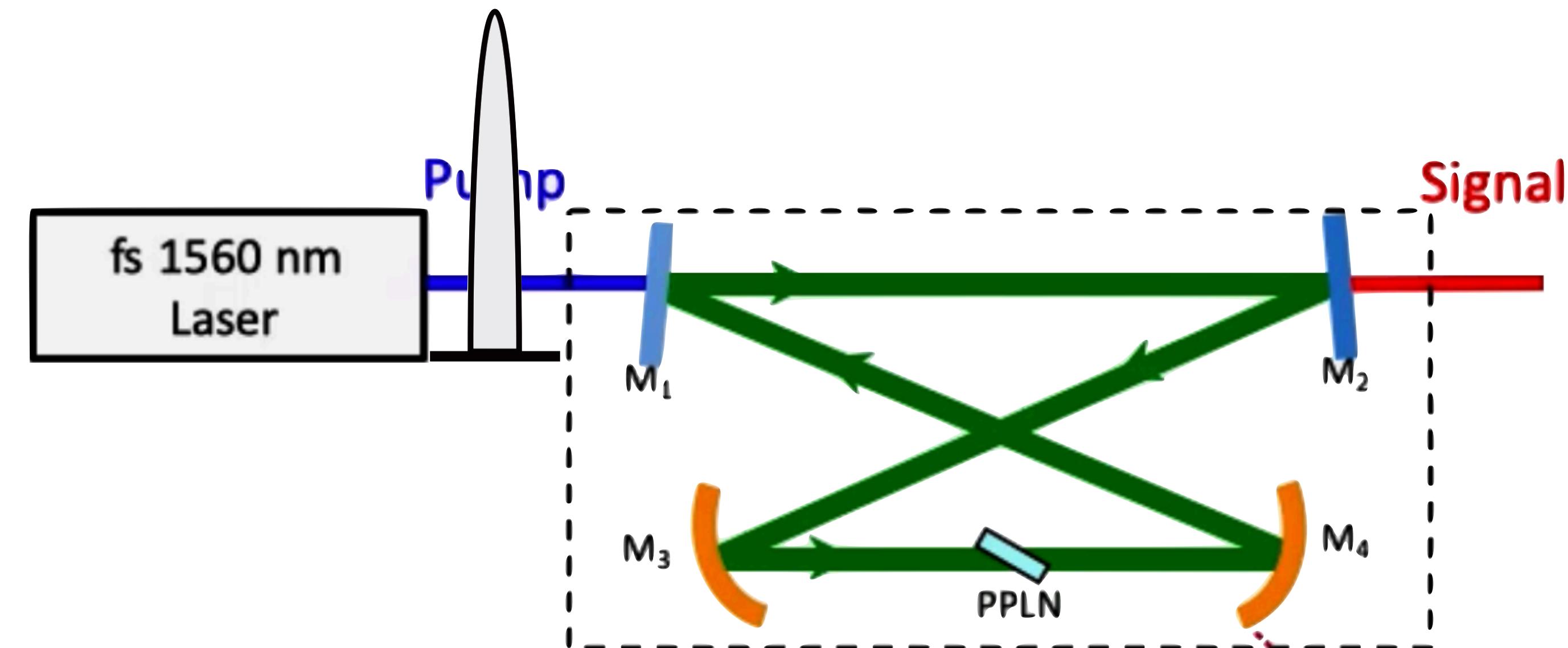
$$J = - \begin{bmatrix} 0 & \pi & \pi & \pi \\ 0 & 0 & \pi & \pi \\ 0 & 0 & 0 & \pi \\ 0 & 0 & 0 & 0 \end{bmatrix}$$

All out of phase



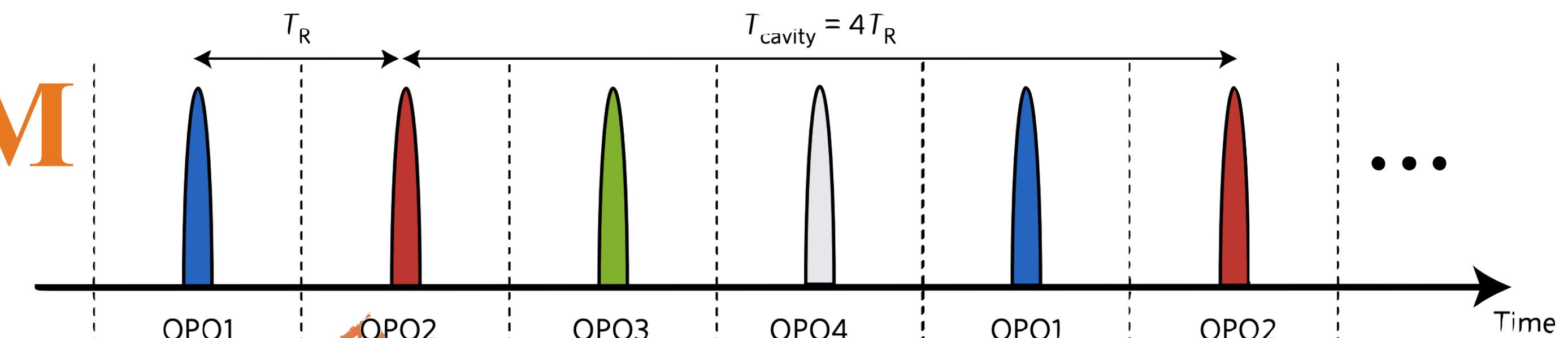
Programming the CIM

A single DOPD

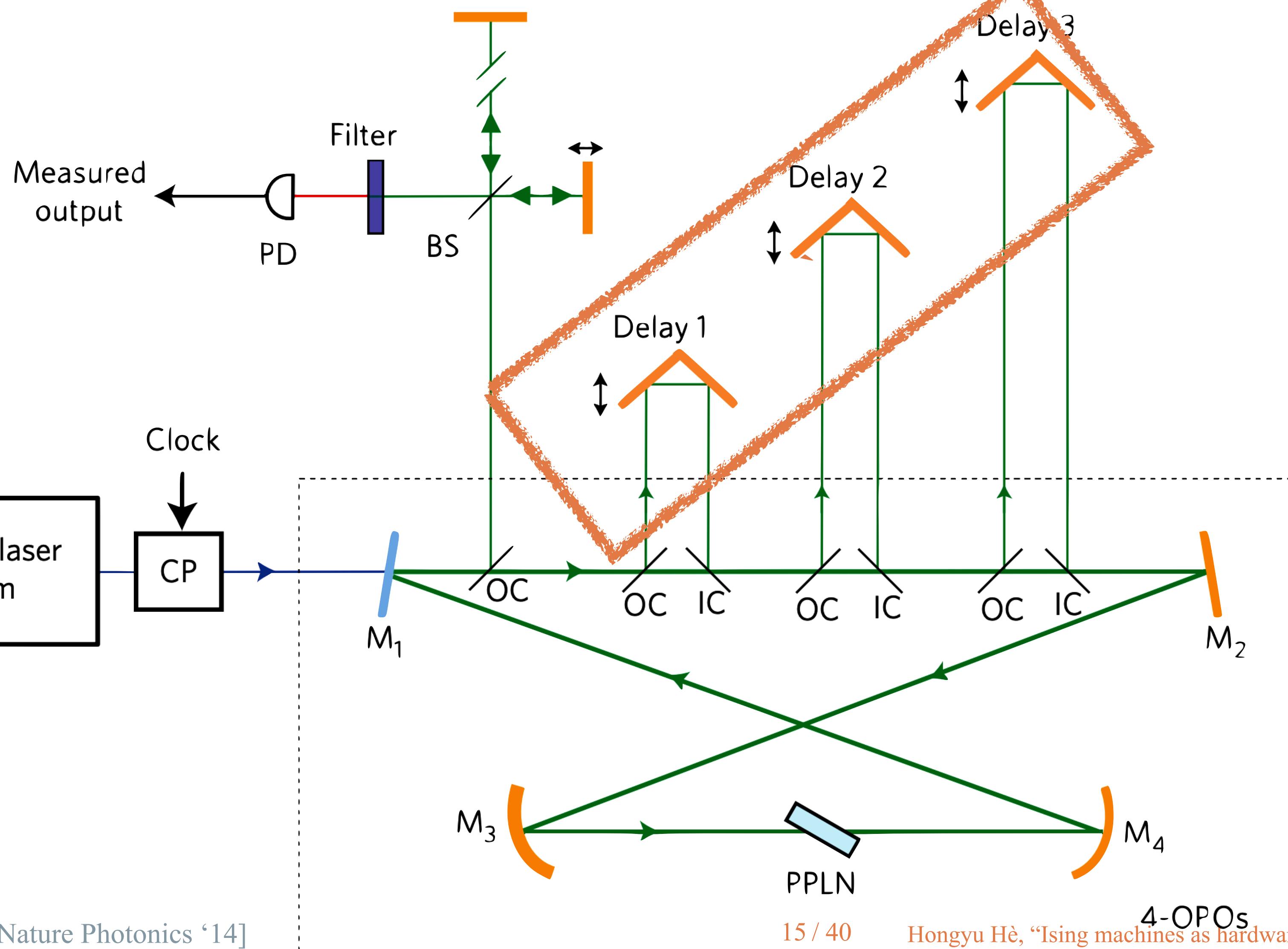


Programming the CIM

Coupled four DPOOs

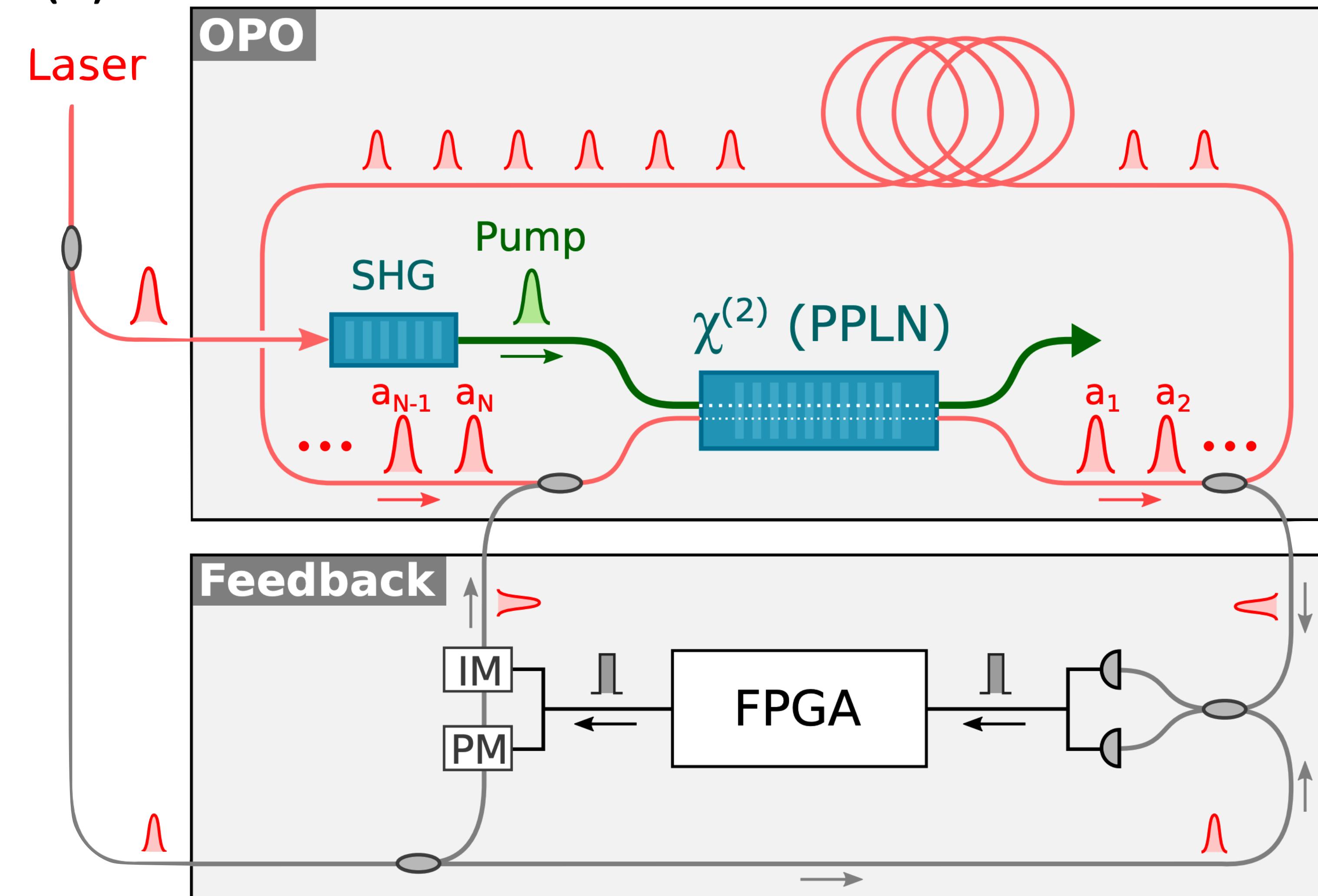


a



Simulating “Beam Splitters” Using FPGAs

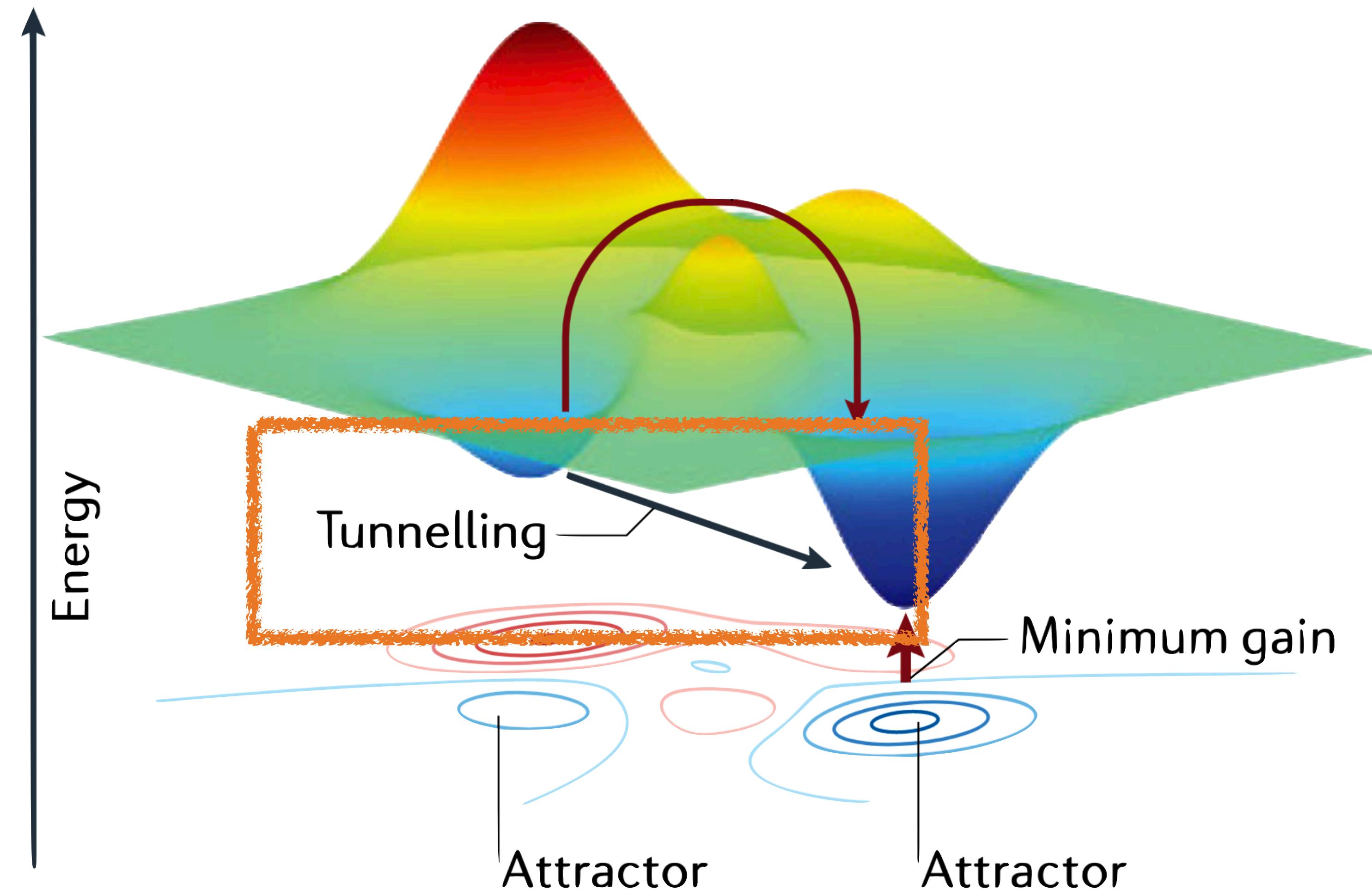
Program arbitrary Ising Hamiltonian without creating optical systems
(a)



Different but still Difficult

CIM does not change the optimization landscape

Nor does it improve its complexity class



Quantum Approaches

Quantum Annealing

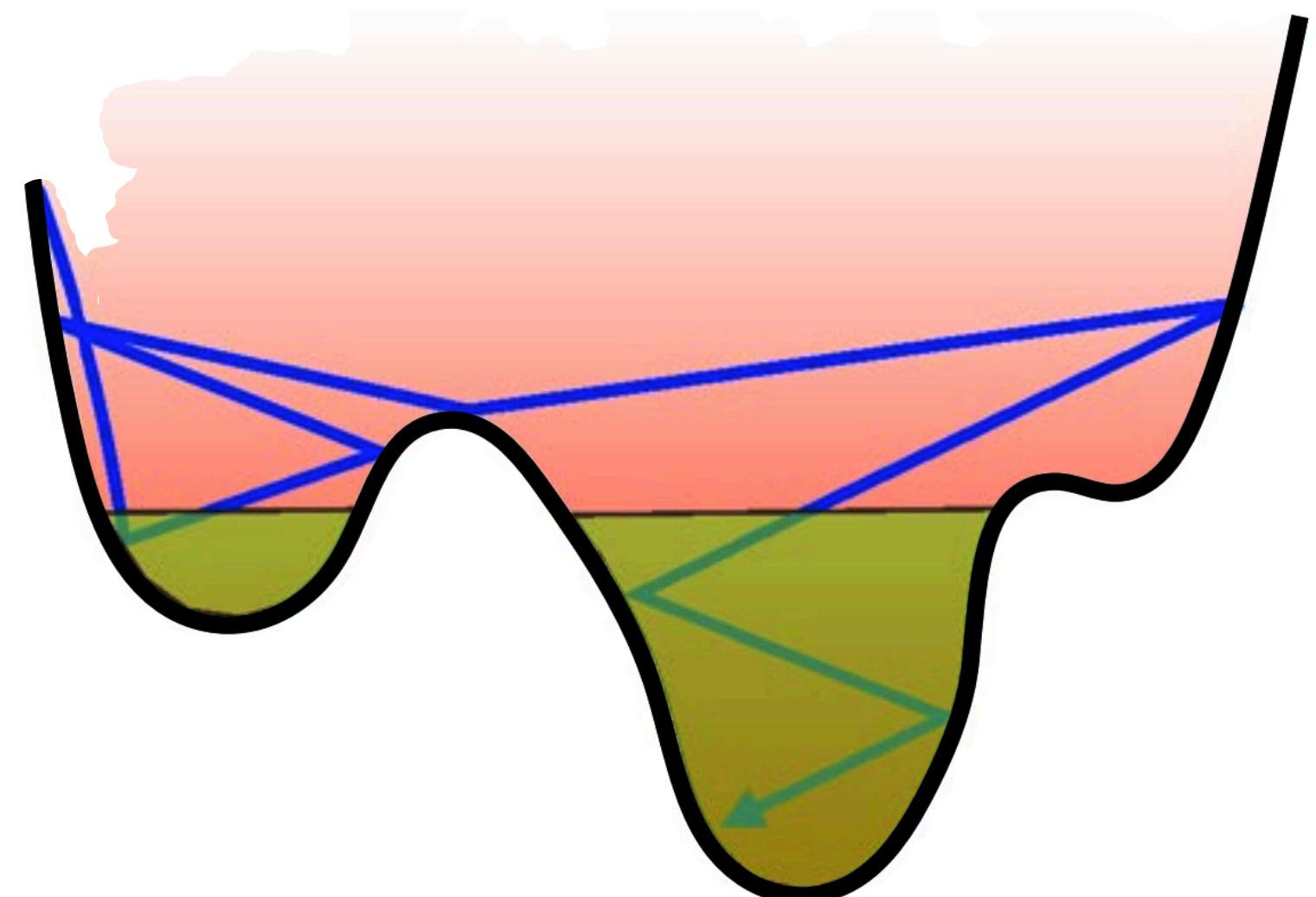
$$H(s) = -A(s) \left[\sum_i^N \delta_i^x \right] + B(s) \left[\sum_{i,j}^N J_{i,j} \cdot \delta_i^z \delta_j^z + \sum_i^N h_i \delta_i^z \right]$$

- $\vec{\delta}$: Qubits controlled by quantum operators
- Classical Ising Hamiltonian (the problem)
 - Pauli-Z operator $\sigma^z = \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix}$: Induces no quantum effect
 - Transverse field
 - Pauli-X operator: $\sigma^x = \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix}$: Leads to superposition

Quantum Annealing

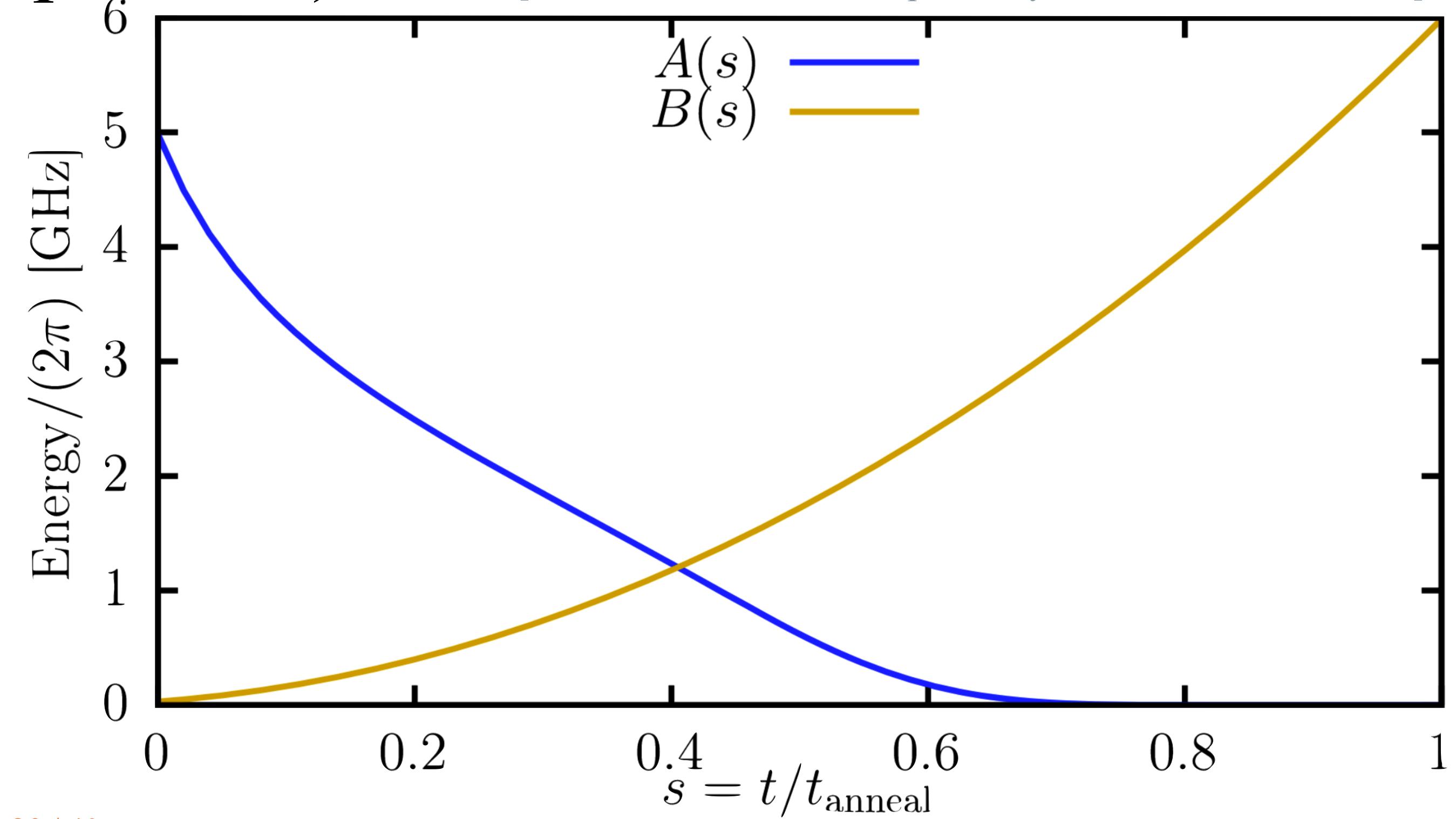
$$H(s) = -A(s) \left[\sum_i^N \delta_i^x \right] + B(s) \left[\sum_{i,j}^N J_{i,j} \cdot \delta_i^z \delta_j^z + \sum_i^N h_i \delta_i^z \right]$$

- Classical Ising Hamiltonian (the problem)
- Transverse field



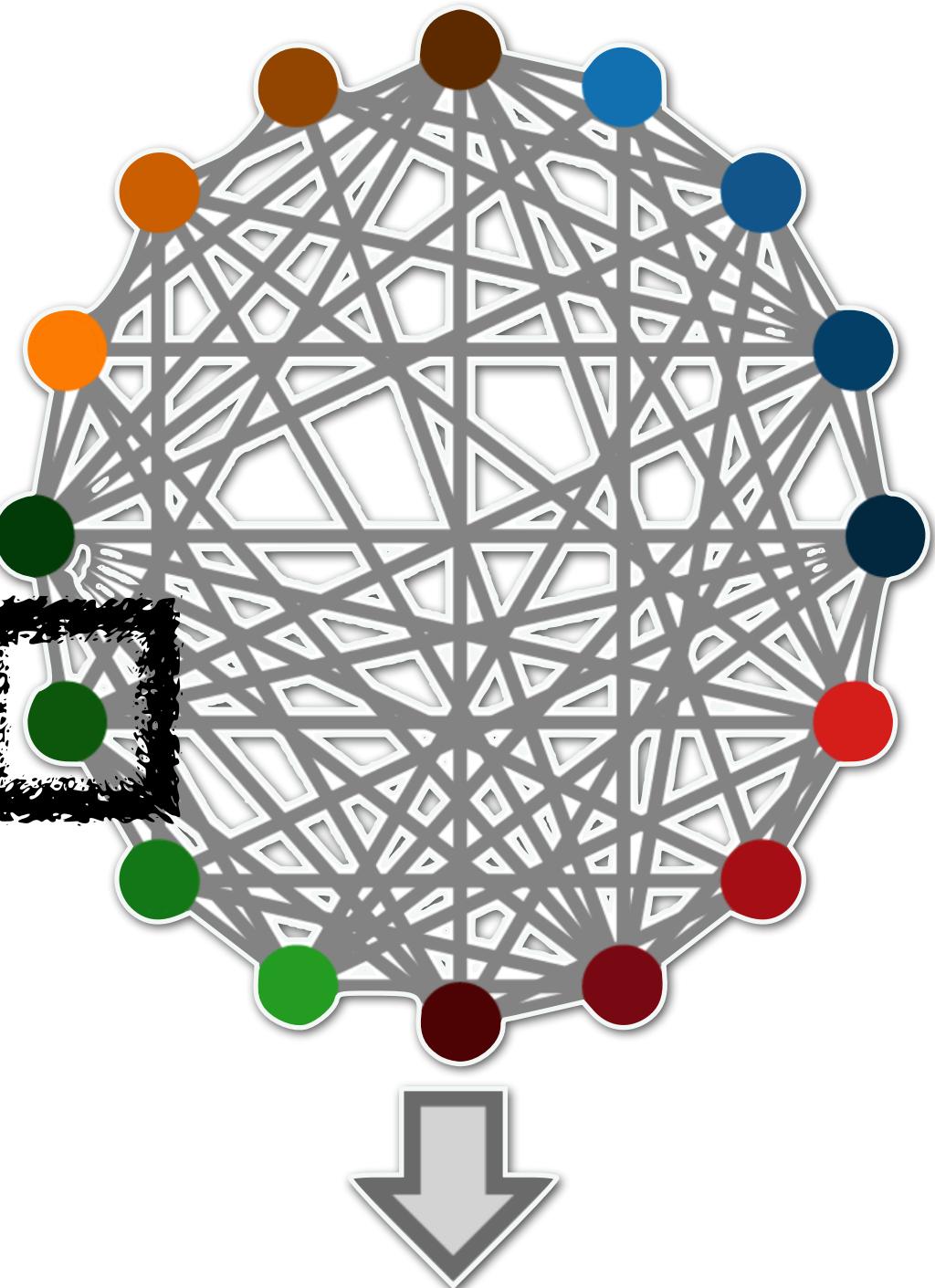
[Roland Sandt et al. Scientific Reports '23]

[Dennis Willsch et al. Computer Physics Communications '22]

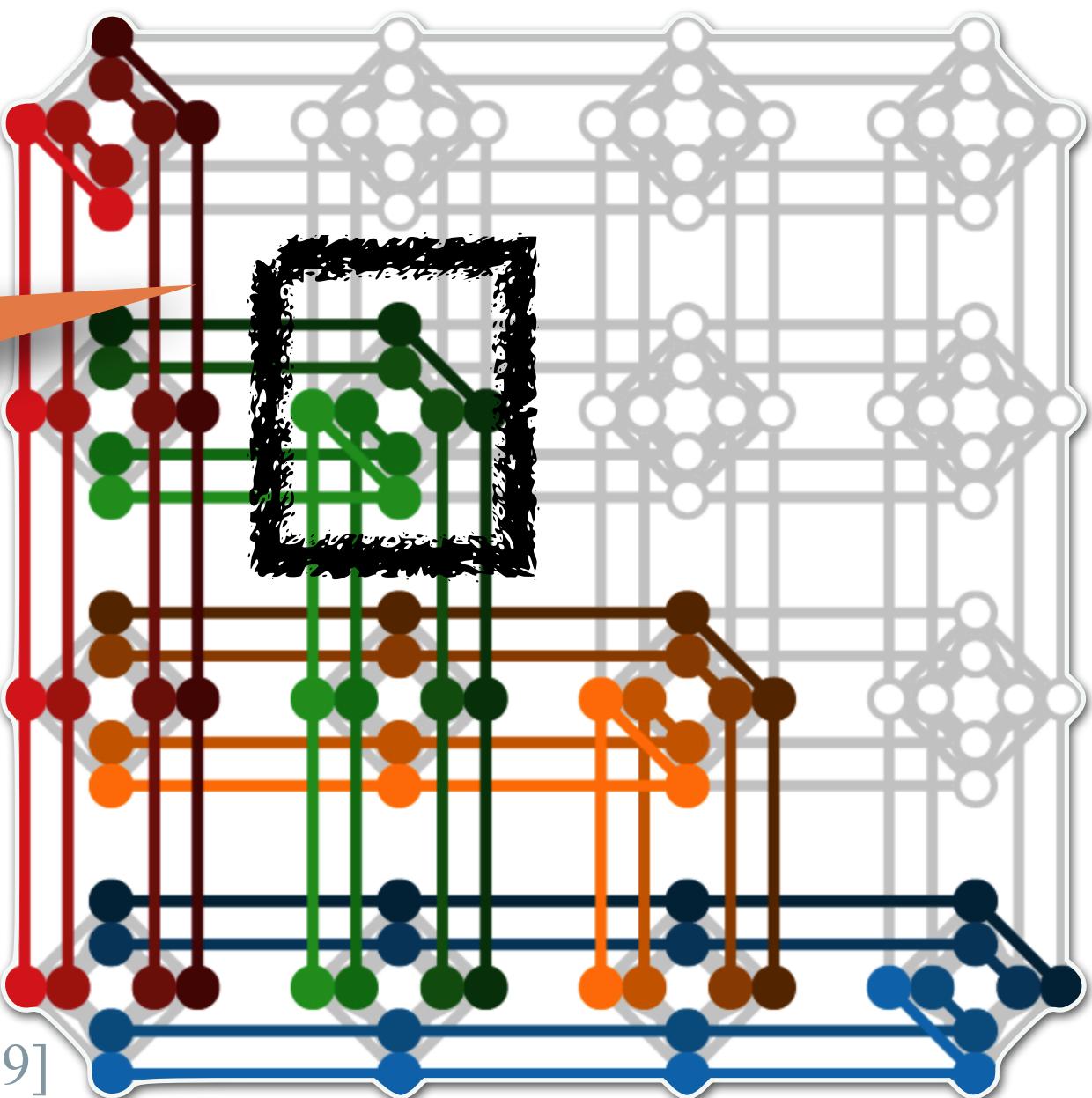


Problems with Quantum Annealing

- Ising problems often require **all-to-all** dense connections
- (D-Wave) Hardware limited to **sparse** connectivity
- One node mapped to a Chimera graph
 - ▶ 1 Ising spin : N hardware qubits (often quadratic)
 - ▶ $2K$ qubits $\Rightarrow \leq 50$ spins
- **Quadratic atop exponential runtime!**



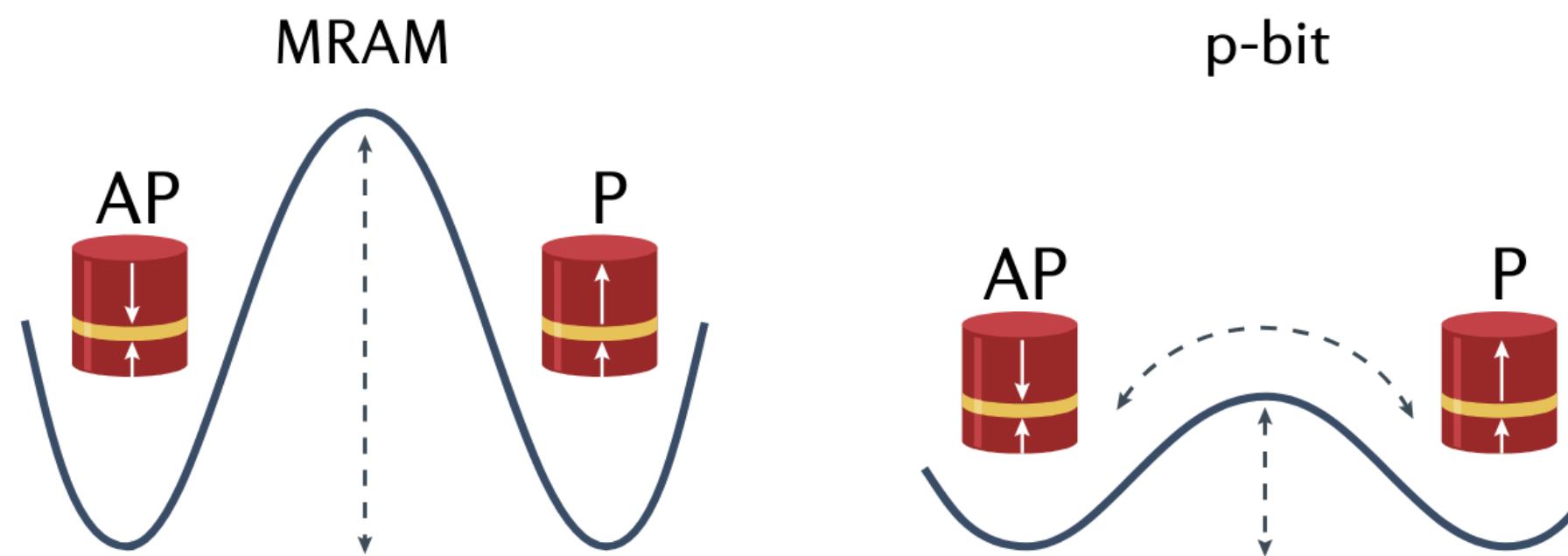
Unit Cell



[Ryan Hamerly et al. Science Advances '19]

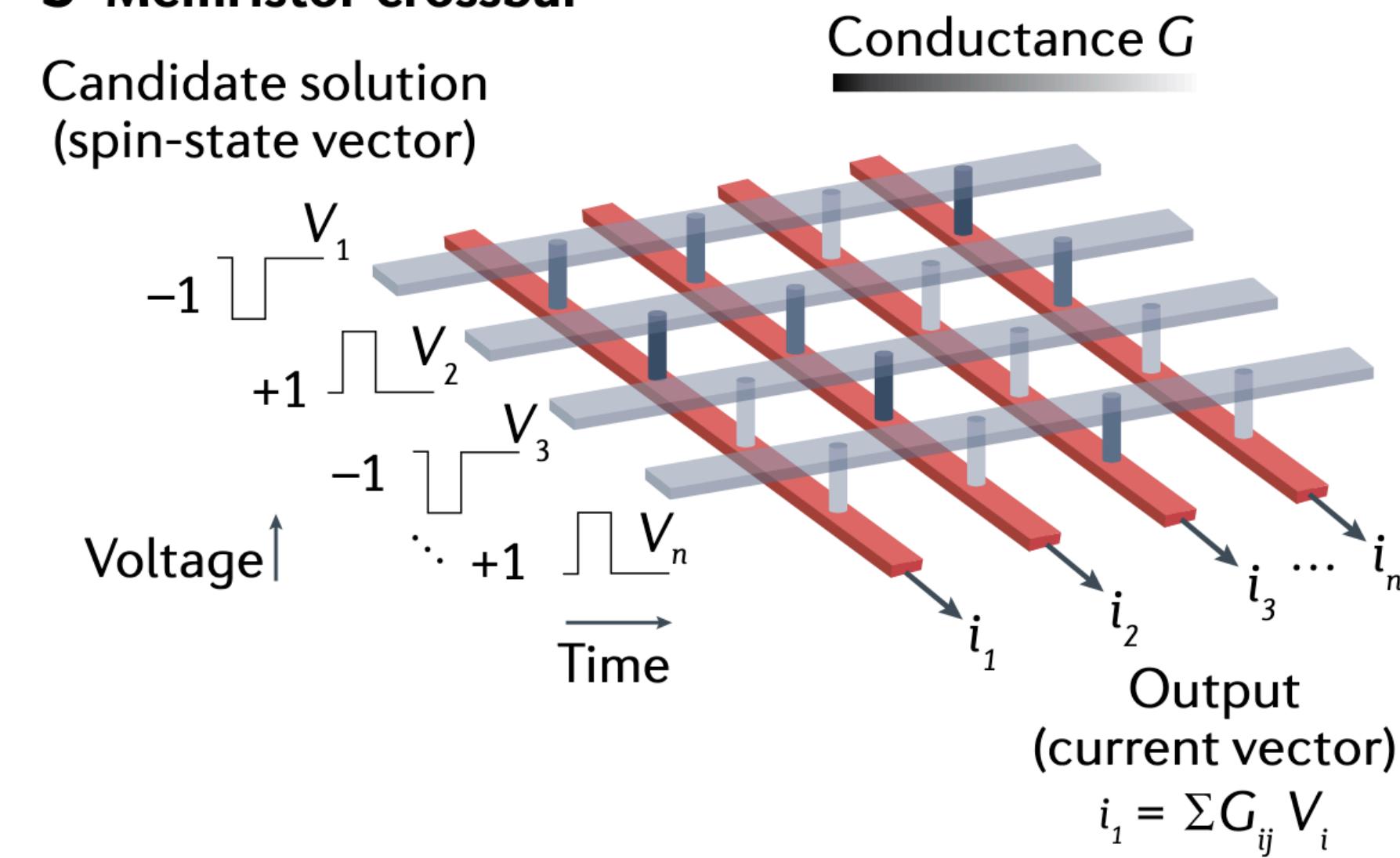
Other Approaches

a Stochastic magnetic tunnel junctions

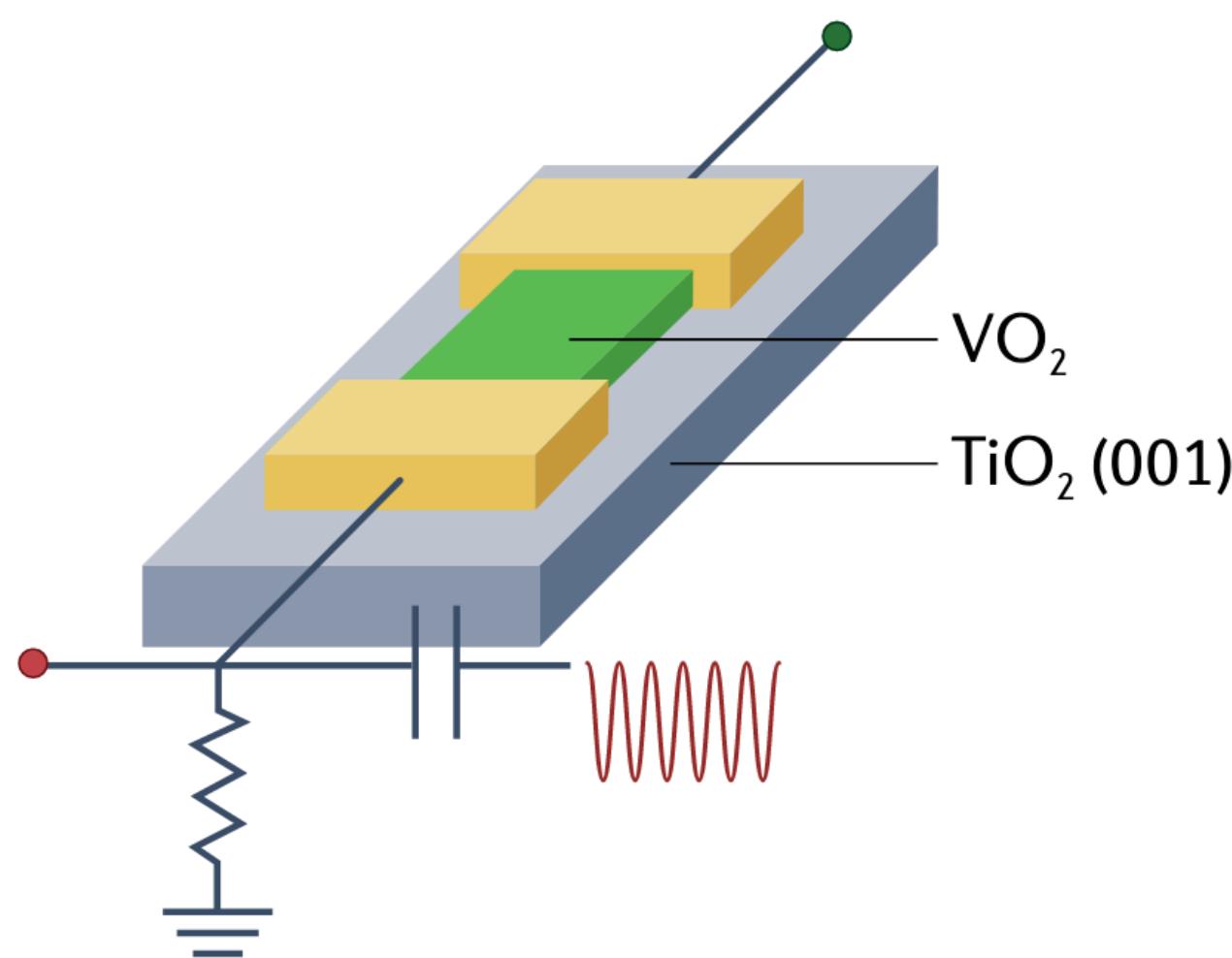


b Memristor crossbar

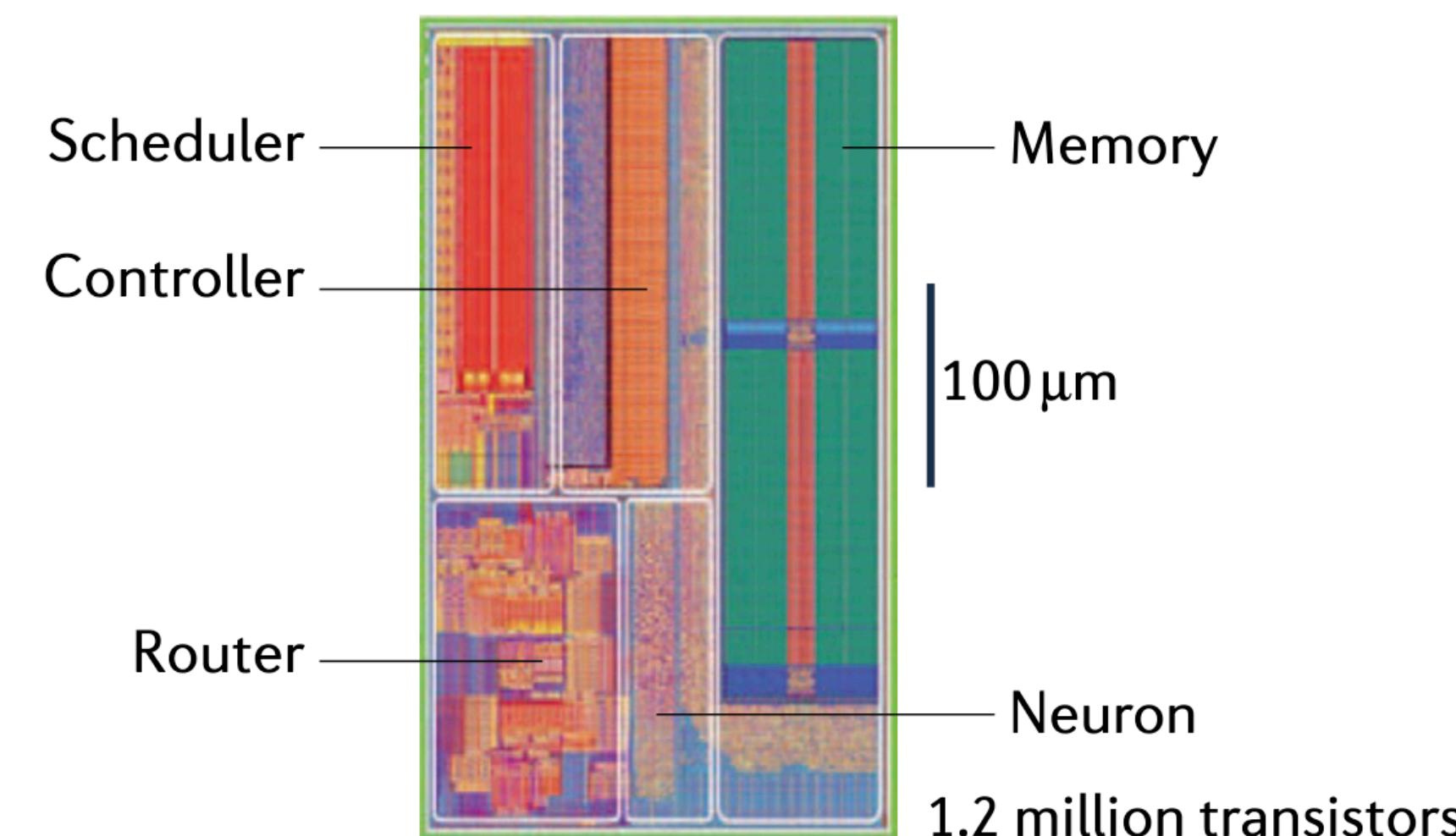
Candidate solution
(spin-state vector)



c Coupled electrical relaxation oscillators



d CMOS



Evaluation & Comparisons

Evaluation Metrics

- Computational complexity X
 - Most Ising machines are heuristic solvers (no theoretical guarantees)
 - Still $O(b^{a \cdot n})$ for near-optimal solutions
 - Improve on b or a
- Empirical performance
 - p_{Suc} : Probability of finding exact ground state in one shot
 - Not time consideration (longer run, $p_{\text{Suc}} \uparrow$)
 - Time-to-solution $T_{\text{sol}} = \tau \frac{\ln 0.01}{\ln(1 - p_{\text{Suc}})}$

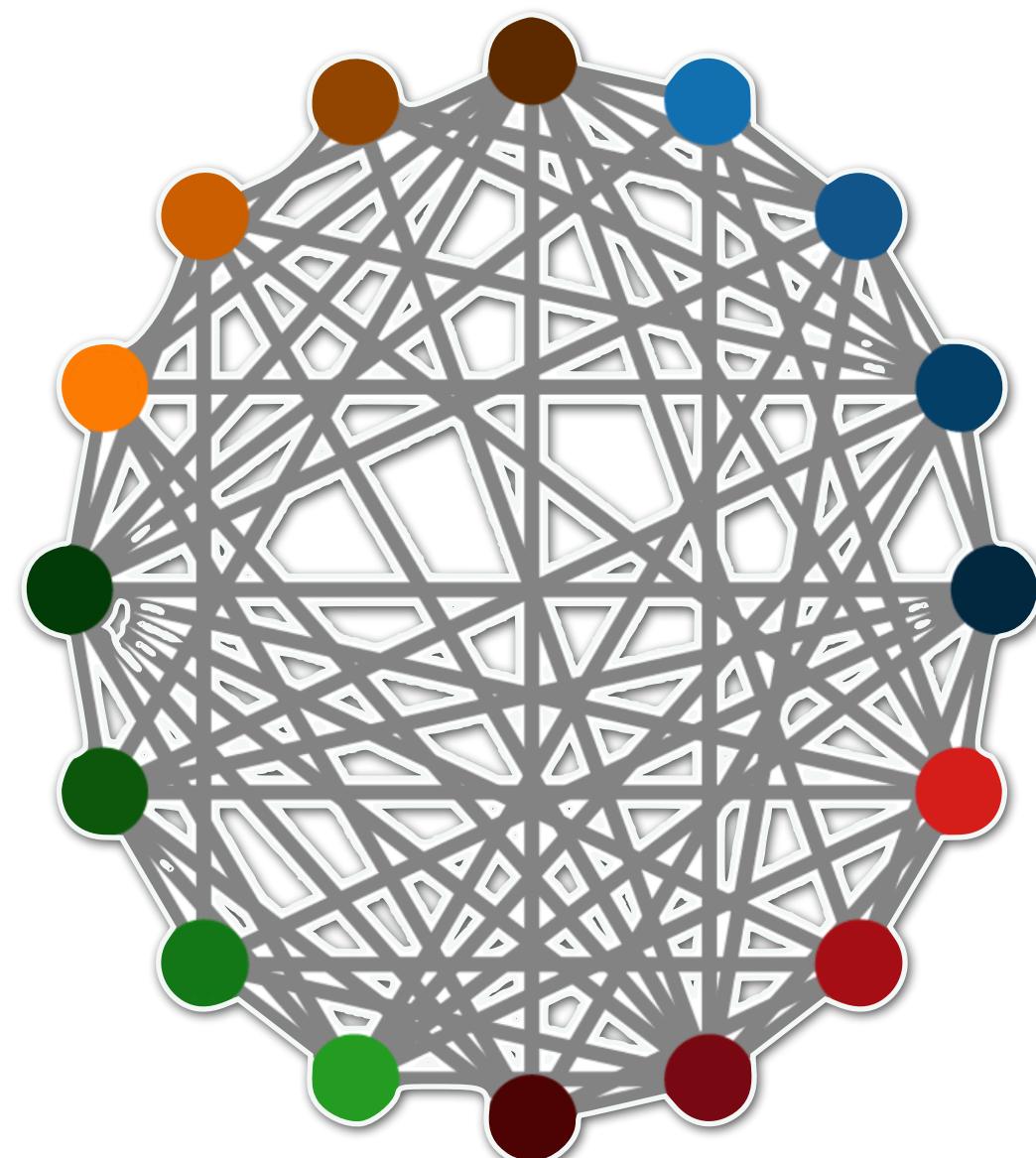
Evaluation Workloads

- 1) Dense MaxCut instances
- 2) Sherrington-Kirkpatrick (SK) problems

$$H_{\text{SK}} = - \sum_{i < j} J_{ij} \cdot s_i s_j$$

(compared to classical Hamiltonian)

- Fixed positive values in $J \rightarrow$ Random +1 or -1 couplings
- Local interactions \rightarrow Full connectivity
- ...



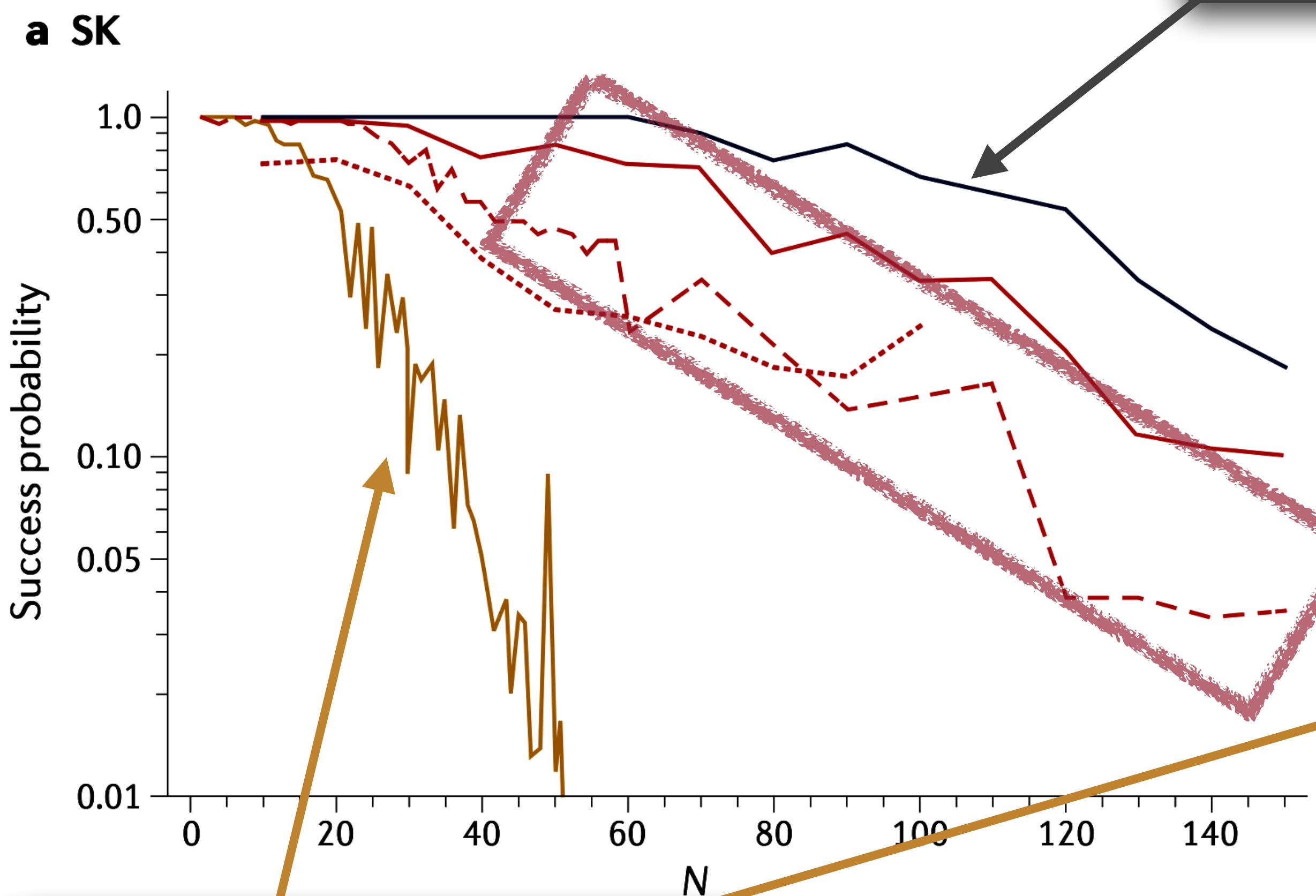
Results are **collected** from various original studies

Compared Ising Machines (1)

Ising machine/ algorithm	Acronym	Operating principle	Hardware	Hardware connectivity	Parallelization
Coherent Ising machine (NTT)	CIM1	Dynamical oscillator	Hybrid (optical/ FPGA)	All-to-all	Yes
Coherent Ising machine (Stanford)	CIM2	Dynamical oscillator	Hybrid (optical/ FPGA)	All-to-all	Yes
Coherent Ising machine	CIM3	Dynamical oscillator algorithm	Predicted ^b	All-to-all	Yes
D-Wave quantum annealer 2Q	DWAV1	Quantum annealer	Superconducting qubits	Chimera	Yes
D-Wave quantum annealer Advantage1.1	DWAV2	Quantum annealer	Superconducting qubits	Chimera	Yes
D-Wave quantum annealer 2KQ	DWAV3	Quantum annealer	Superconducting qubits	Chimera	Yes
D-Wave quantum annealer 2KQ	DWAV4	Quantum annealer	Superconducting qubits	Chimera	Yes
Restricted Boltzmann machine	RBM	Simulated annealing algorithm	FPGA	All-to-all	Yes
Memristor annealing	MRT	Simulated annealing algorithm	Predicted ^b	All-to-all	Yes

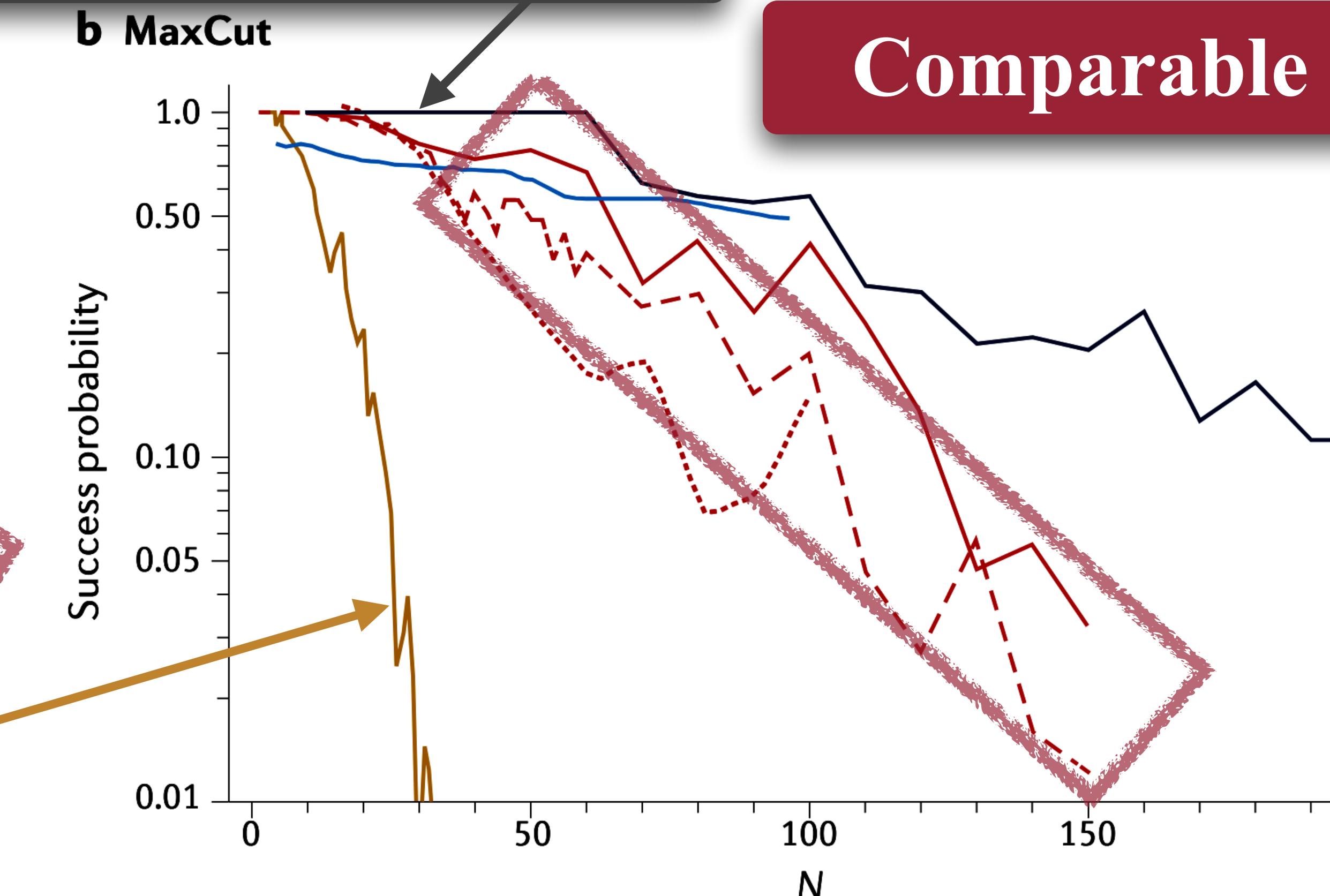
Success Probability

Best scaling with N :
 $p_{\text{Suc}} \propto \exp\{-bN\}$



Worst:
 $p_{\text{Suc}} \propto \exp\{-bN^2\}$

(Quadratic atop exponential)



Comparable

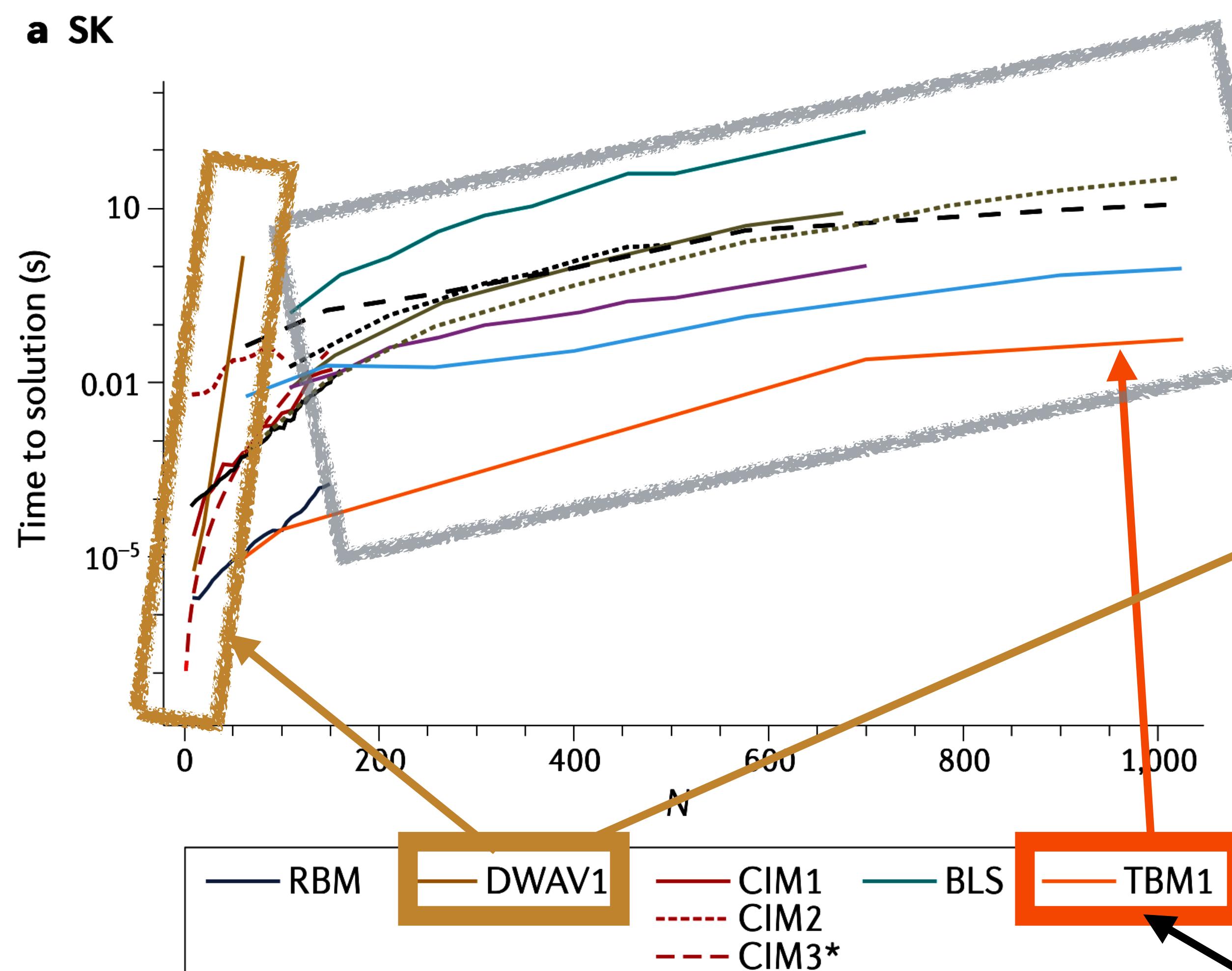
Compared Ising Machines (2)

Ising machine/ algorithm	Acronym	Operating principle	Hardware	Hardware connectivity	Parallelization ^a
Breakout local search	BLS	Local search and simulated annealing algorithm	CPU	All-to-all	No
Chaotic amplitude control	CAC	Dynamical chaotic algorithm	FPGA	All-to-all	Yes
Toshiba bifurcation machine	TBM1	Discrete simulated bifurcation algorithm	FPGA	All-to-all	Yes
Fujitsu digital annealer	FDA1	Simulated annealing algorithm	ASIC	All-to-all	Yes
Simulated annealing	SA1	Simulated annealing algorithm	CPU	All-to-all	Yes
Simulated annealing	SA2	Simulated annealing algorithm	CPU	All-to-all	No
Parallel tempering	PT1	Simulated annealing algorithm	CPU	All-to-all	No
Parallel tempering	PT2	Simulated annealing algorithm	CPU	All-to-all	No
Parallel tempering	PT3	Simulated annealing algorithm	CPU	All-to-all	No
Photonic recurrent Ising sampler	PRIS	Oscillator-based annealer	Predicted ^b	All-to-all	Yes

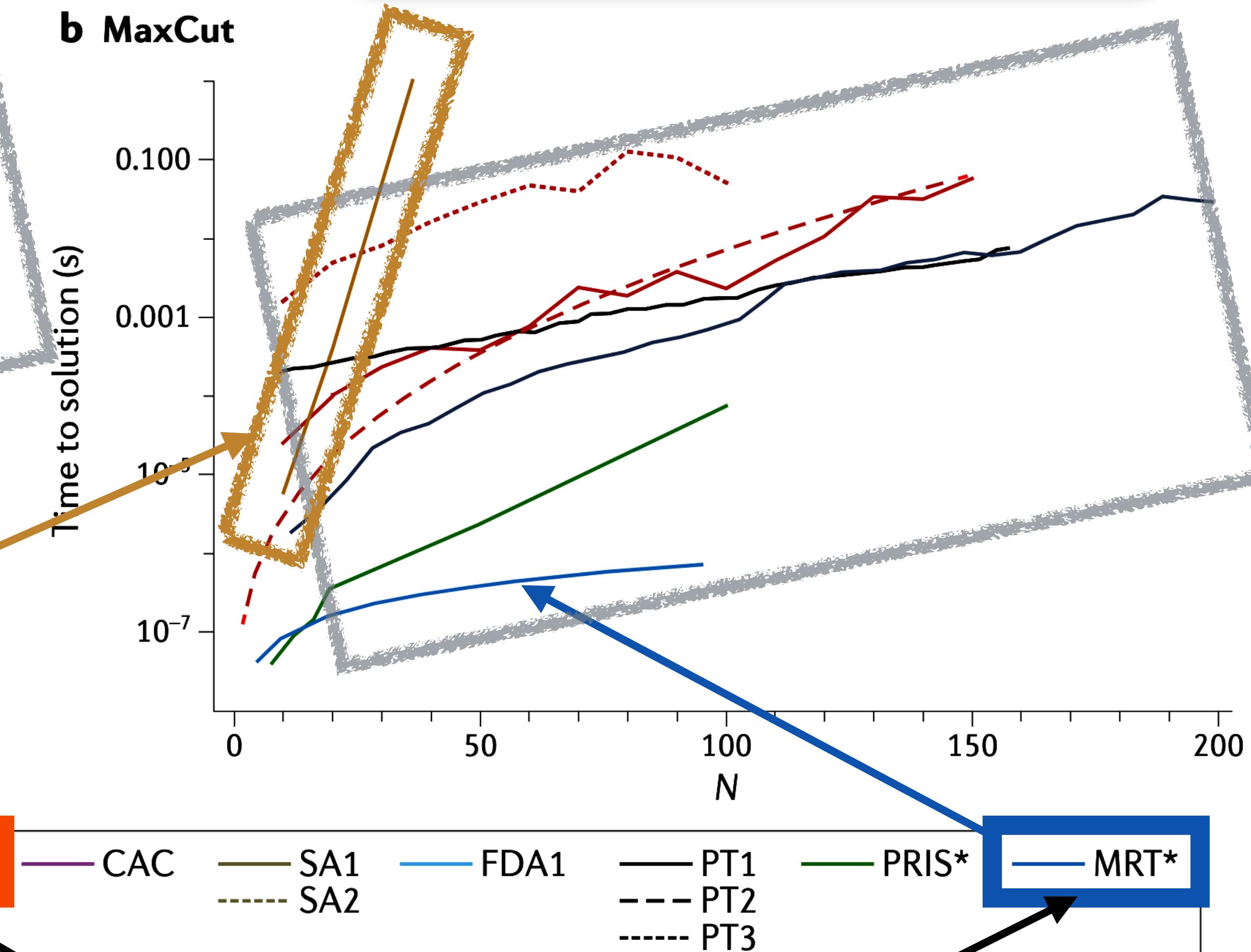
Time To Solution ($p_{\text{Suc}} = 0.99$)

$$T_{\text{sol}} \propto \exp\{c\sqrt{N}\}$$

a SK



b MaxCut



$$T_{\text{sol}} \propto \exp\{cN\}$$

Classical digital hardware

Class Discussion

Conclusion

- Connectivity is crucial for Ising machines
- Quantum annealing (QA) is limited by implementation
 - QA computational mechanism works in simulation ✓
 - D-Wave hardware does not scale ✗
 - Quantum mechanics (e.g., entanglement) in QA ?
- The best: Classical digital methods ('currently')
 - Analogue and quantum approaches rapidly developing
 - QA is new; Quantum+Classical ?

Backup slides ...