

STM32F100RB Peripheral Base Address Map

<u>Bus</u>	<u>Base address</u>	<u>Peripheral</u>
AHB	0x4002 3000	CRC
	0x4002 2400	Reserved
	0x4002 2000	Flash memory interface
	0x4002 1400	Reserved
	0x4002 1000	Reset and clock control (RCC)
	0x4002 0400	Reserved
	0x4002 0000	DMA1
	0x4001 4C00	Reserved
APB	0x4001 4800	TIM17 timer
	0x4001 4400	TIM16 timer
	0x4001 4000	TIM15 timer
	0x4001 3C00	Reserved
	0x4001 3800	USART1
	0x4001 3400	Reserved
	0x4001 3000	SPI1
	0x4001 2C00	TIM1 timer
	0x4001 2800	Reserved
	0x4001 2400	ADC1
	0x4001 1C00	Reserved
	0x4001 1800	GPIO Port E
	0x4001 1400	GPIO Port D
	0x4001 1000	GPIO Port C
	0x4001 0C00	GPIO Port B
	0x4001 0800	GPIO Port A
	0x4001 0400	EXTI
	0x4001 0000	AFIO

<u>Bus</u>	<u>Base address</u>	<u>Peripheral</u>
APB1	0x4000 7C00	Reserved
	0x4000 7800	CEC
	0x4000 7400	DAC
	0x4000 7000	Power control PWR
	0x4000 6C00	Backup registers (BKP)
	0x4000 5C00	Reserved
	0x4000 5800	I2C2
	0x4000 5400	I2C1
	0x4000 4C00	Reserved
	0x4000 4800	USART3
	0x4000 4400	USART2
	0x4000 3C00	Reserved
	0x4000 3800	SPI2
	0x4000 3400	Reserved
	0x4000 3000	Independent watchdog (IWDG)
	0x4000 2C00	Window watchdog (WWDG)
	0x4000 2800	RTC
	0x4000 1800	Reserved
	0x4000 1400	TIM7 timer
	0x4000 1000	TIM6 timer
	0x4000 0C00	Reserved
	0x4000 0800	TIM4 timer
	0x4000 0400	TIM3 timer
	0x4000 0000	TIM2 timer