

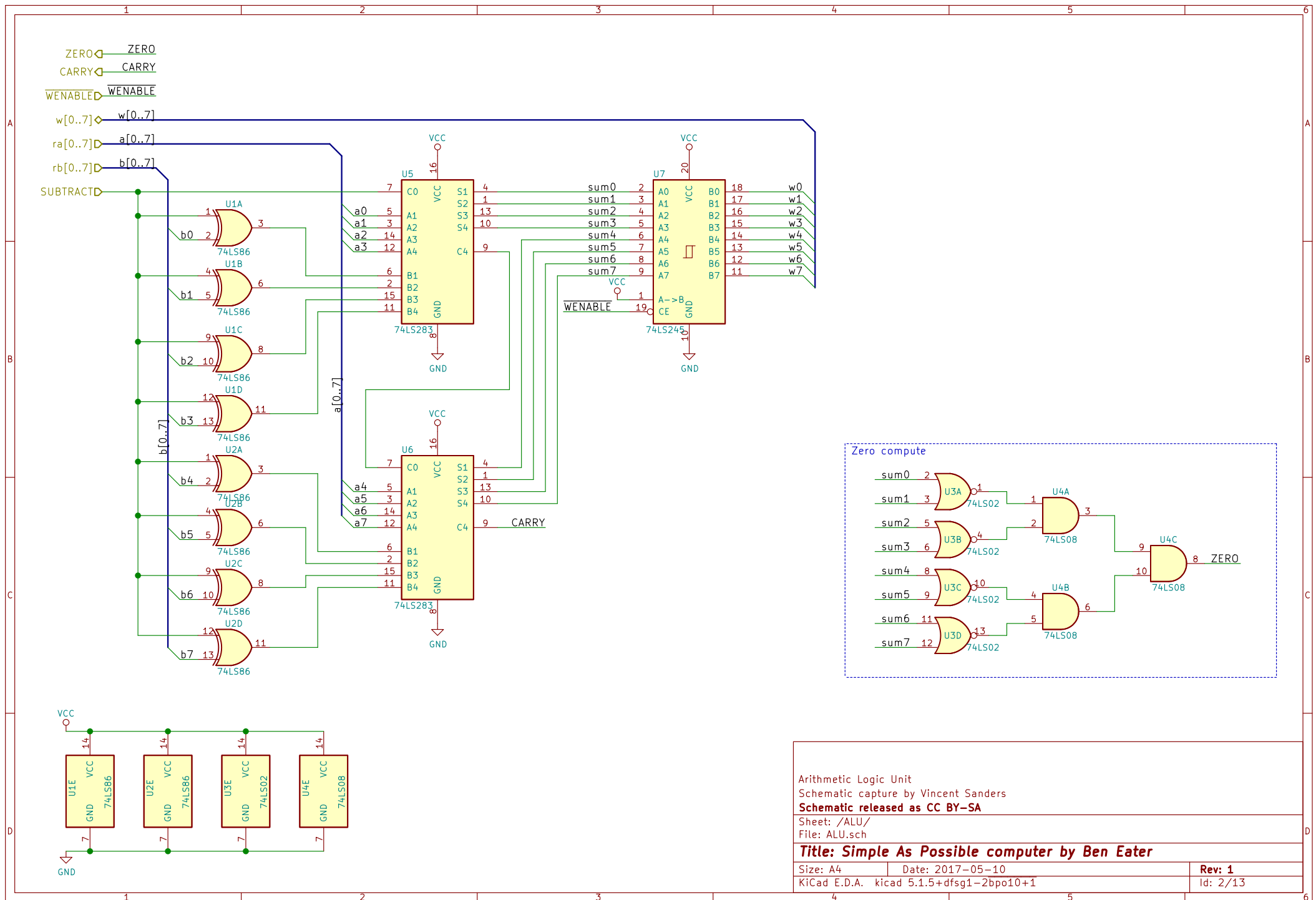
Main w bus and top level view
Schematic capture by Vincent Sanders
Schematic released as CC BY-SA

Sheet: /
File: sap-be.sch

Title: Simple As Possible computer by Ben Eater

Size: A4 Date: 2017-05-10
KiCad E.D.A. kicad 5.1.5+dfsg1-2bpo10+1

Rev: 1
Id: 1/13

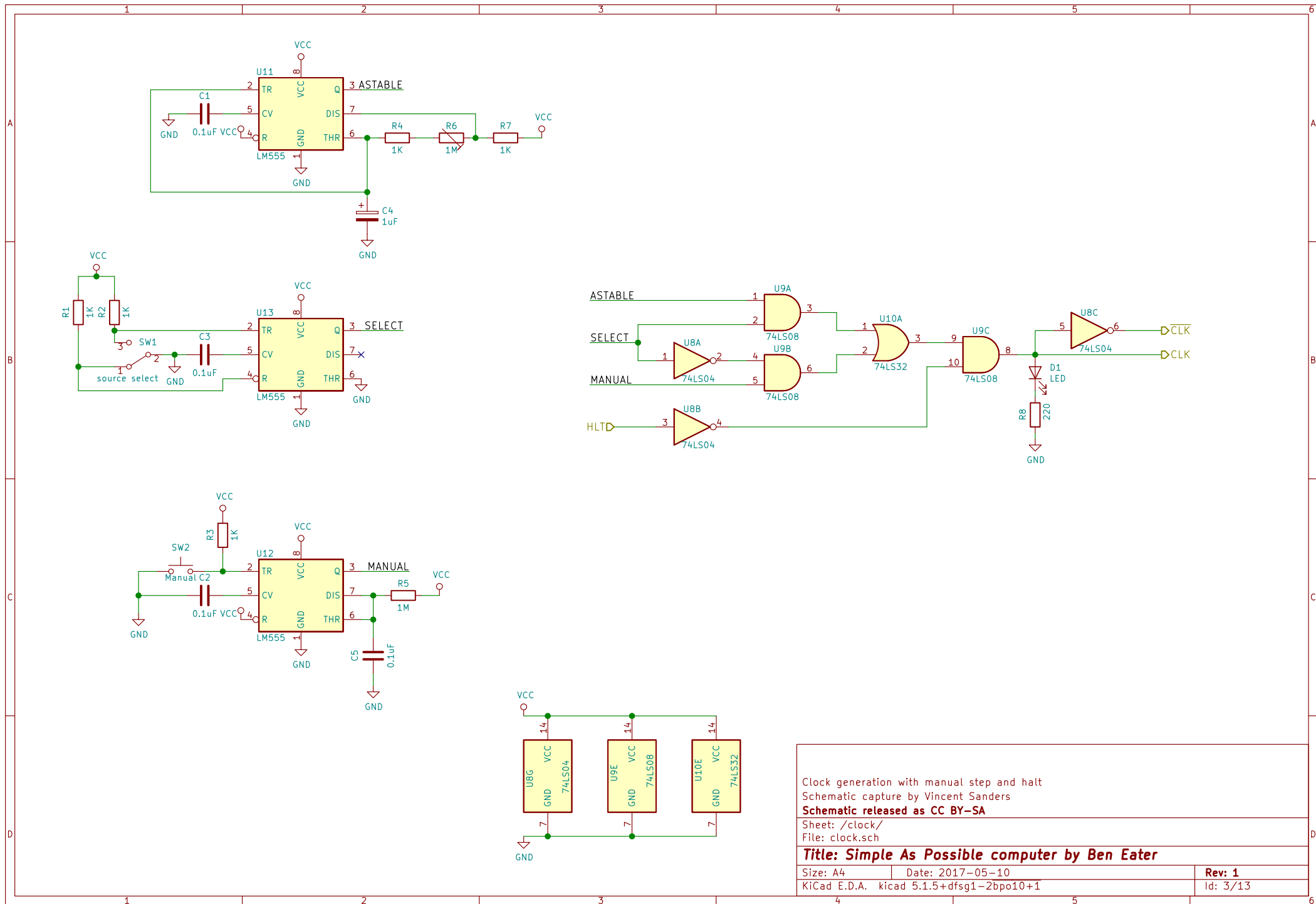


Arithmetic Logic Unit
 Schematic capture by Vincent Sanders
Schematic released as CC BY-SA

Sheet: /ALU/
 File: ALU.sch

Title: Simple As Possible computer by Ben Eater

Size: A4	Date: 2017-05-10	Rev: 1
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Clock generation with manual step and halt
Schematic capture by Vincent Sanders
Schematic released as CC BY-SA

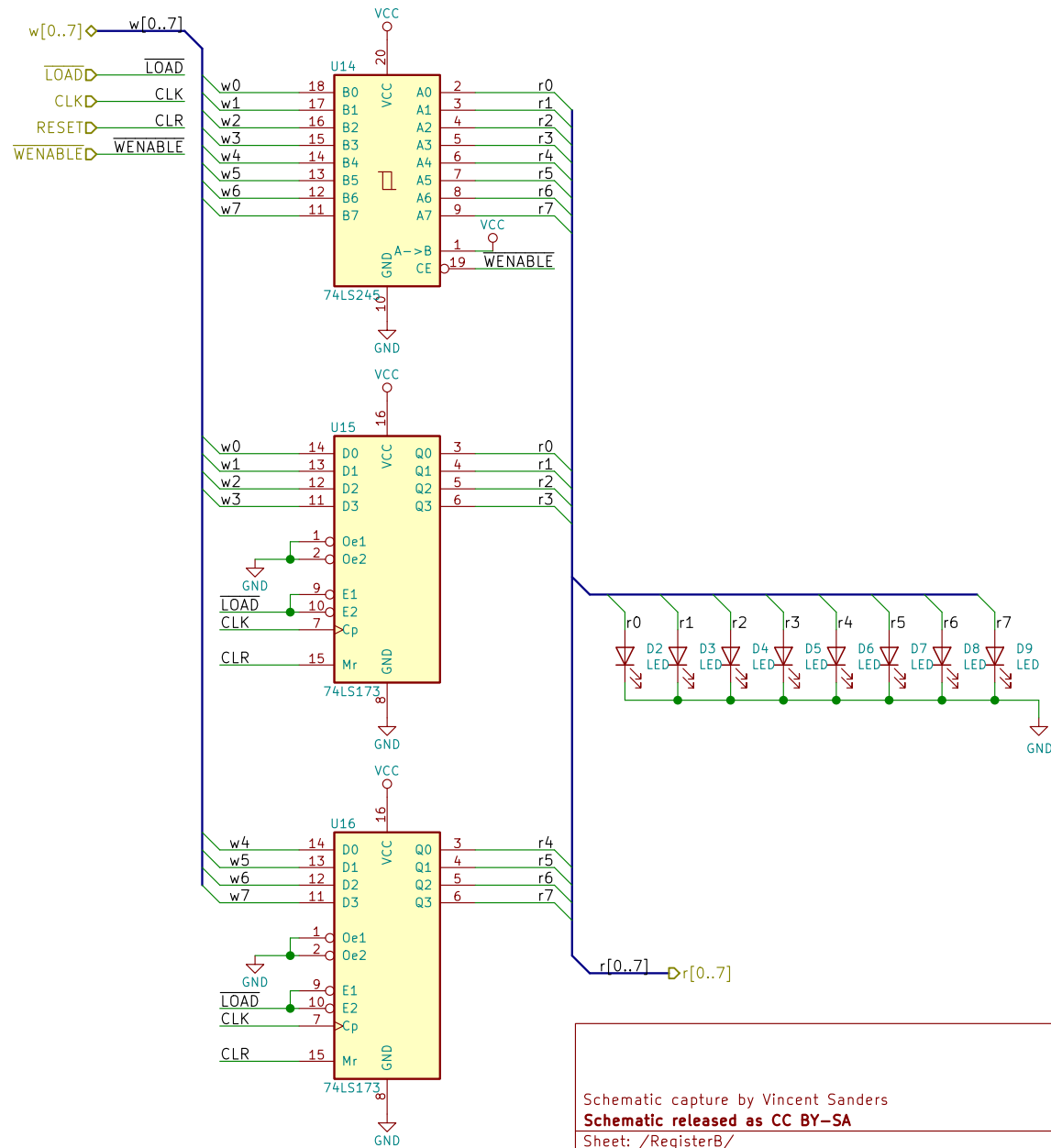
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File: clock.sch

Title: Simple As Possible computer by Ben Eater

Size: A4
KiCad E.D.A. kicad 5.1.5+dfsg1-2bpo10+1

Date: 2017-05-10

Rev: 1
Id: 3/13



Schematic capture by Vincent Sanders

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Sheet: /RegisterB/

File: register.sch

Title: Simple As Possible computer by Ben Eater

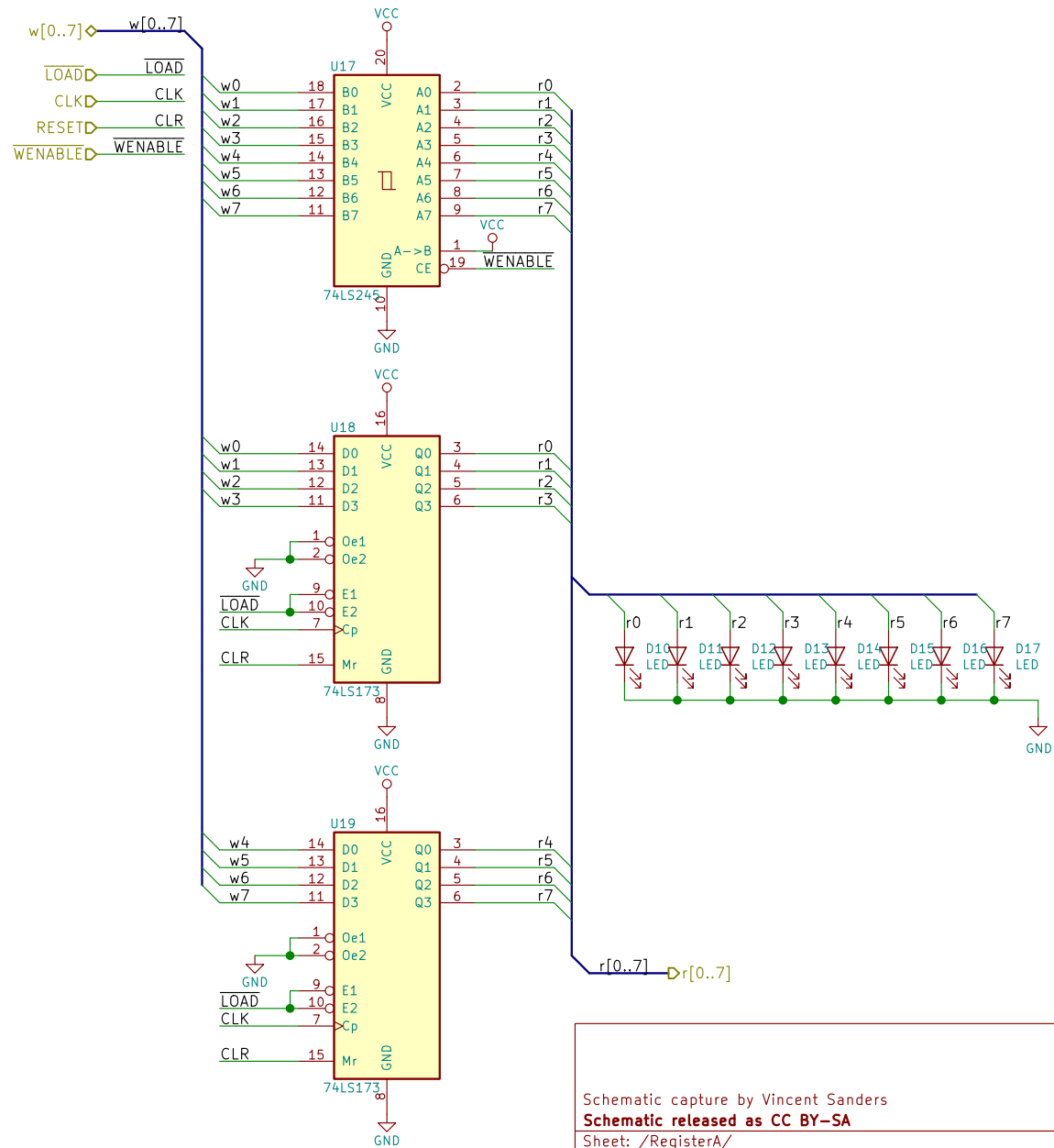
Size: A4

Date: 2017-05-10

Rev: 1

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Id: 4/13



Schematic capture by Vincent Sanders

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Sheet: /RegisterA/

File: register.sch

Title: Simple As Possible computer by Ben Eater

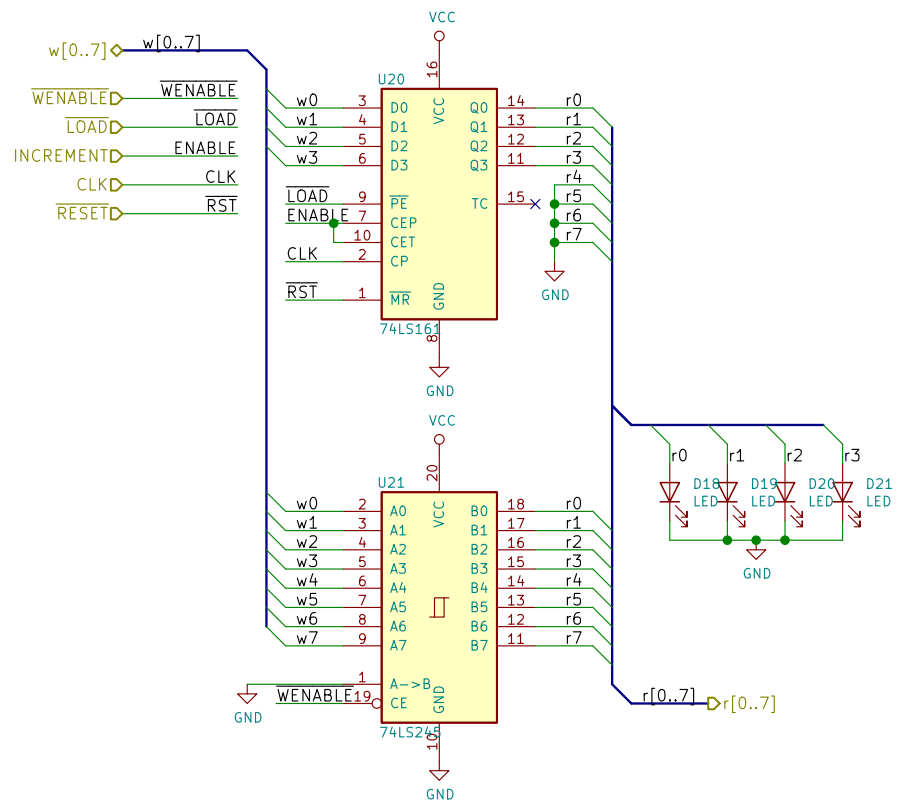
Size: A4

Date: 2017-05-10

Rev: 1

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Id: 5/13



Program counter
Schematic capture by Vincent Sanders
Schematic released as CC BY-SA

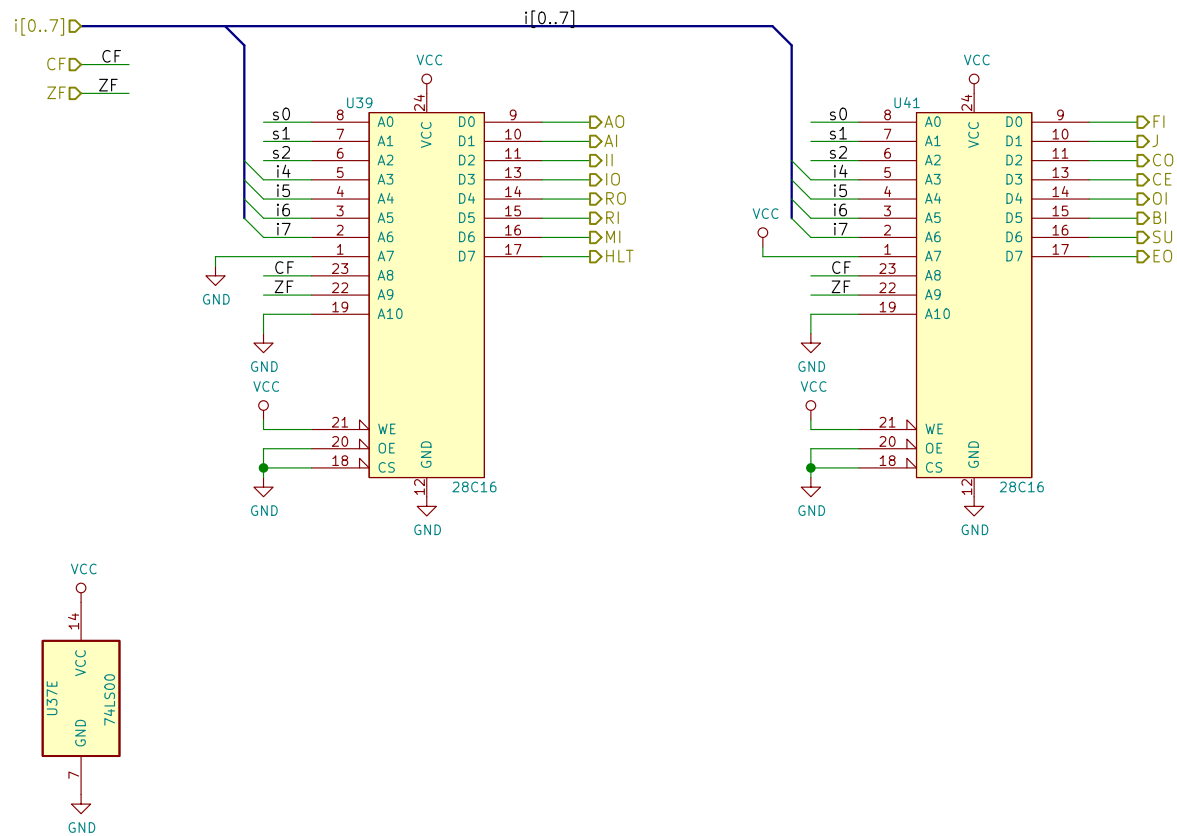
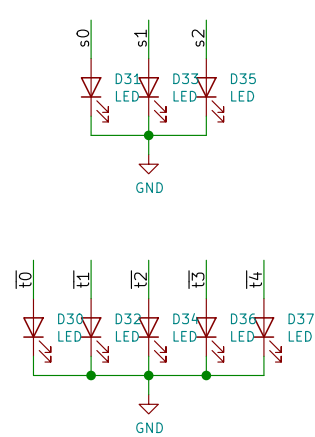
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File: programcounter.sch

Title: Simple As Possible computer by Ben Eater

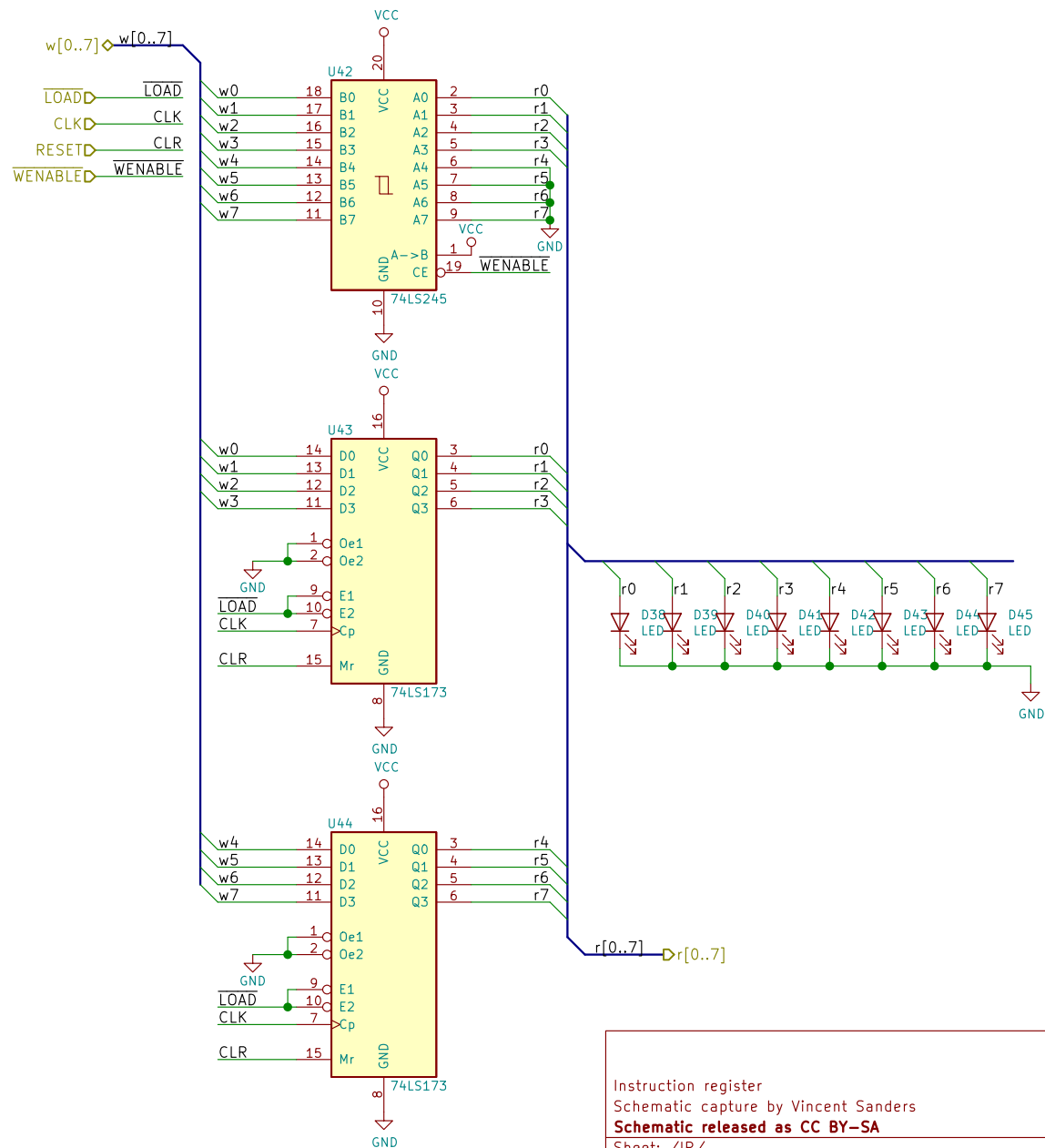
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KiCad E.D.A. kicad 5.1.5+dfsg1-2bpo10+1

Date: 2017-05-10

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Instruction register
Schematic capture by Vincent Sanders
Schematic released as CC BY-SA

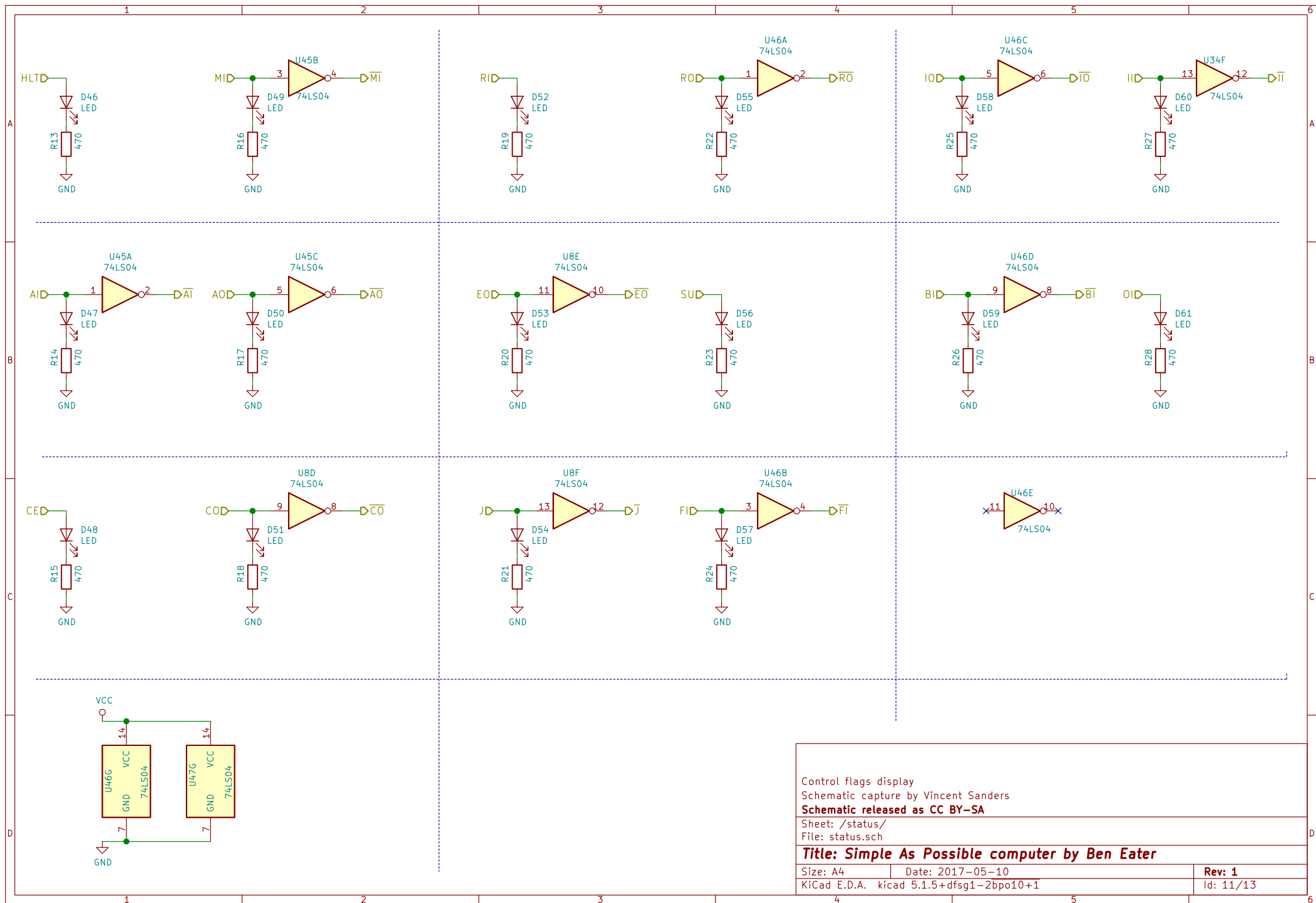
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File: instructionregister.sch

Title: Simple As Possible computer by Ben Eater

Size: A4
KiCad E.D.A. kicad 5.1.5+dfsg1-2bpo10+1

Date: 2017-05-10

Rev: 1
Id: 10/13



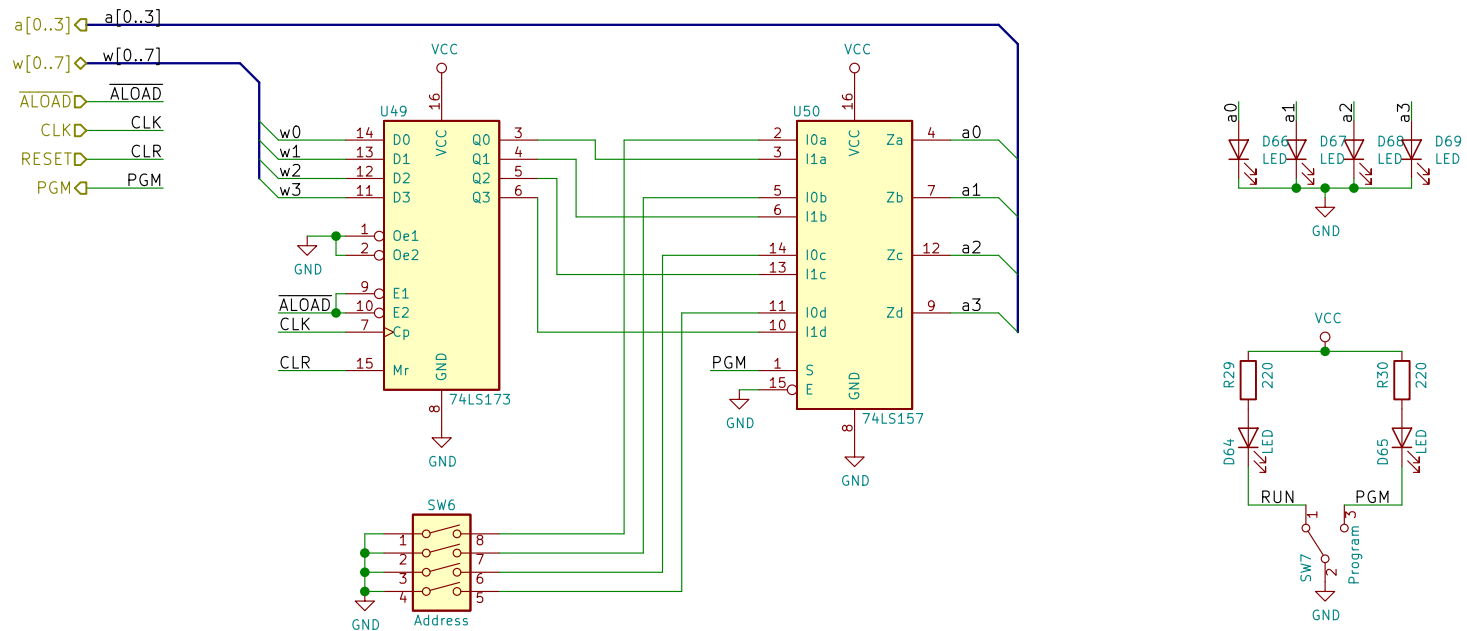
Control flags display
Schematic capture by Vincent Sanders
Schematic released as CC BY-SA

Sheet: /status/
File: status.sch

Title: Simple As Possible computer by Ben Eater

Size: A4
Date: 2017-05-10
KiCad E.D.A. kicad 5.1.5+dfsg1-2bpo10+1

Rev: 1
Id: 11/13



Address latch with manual programming
 Schematic capture by Vincent Sanders
 Schematic released as CC BY-SA

Sheet: /address/
 File: address.sch

Title: Simple As Possible computer by Ben Eater

Size: A4
 KiCad E.D.A. kicad 5.1.5+dfsg1-2bpo10+1

Date: 2017-05-10

Rev: 1

Id: 13/13