PROCESSOR HW/SW INTER

EECS 113

Assignment 2

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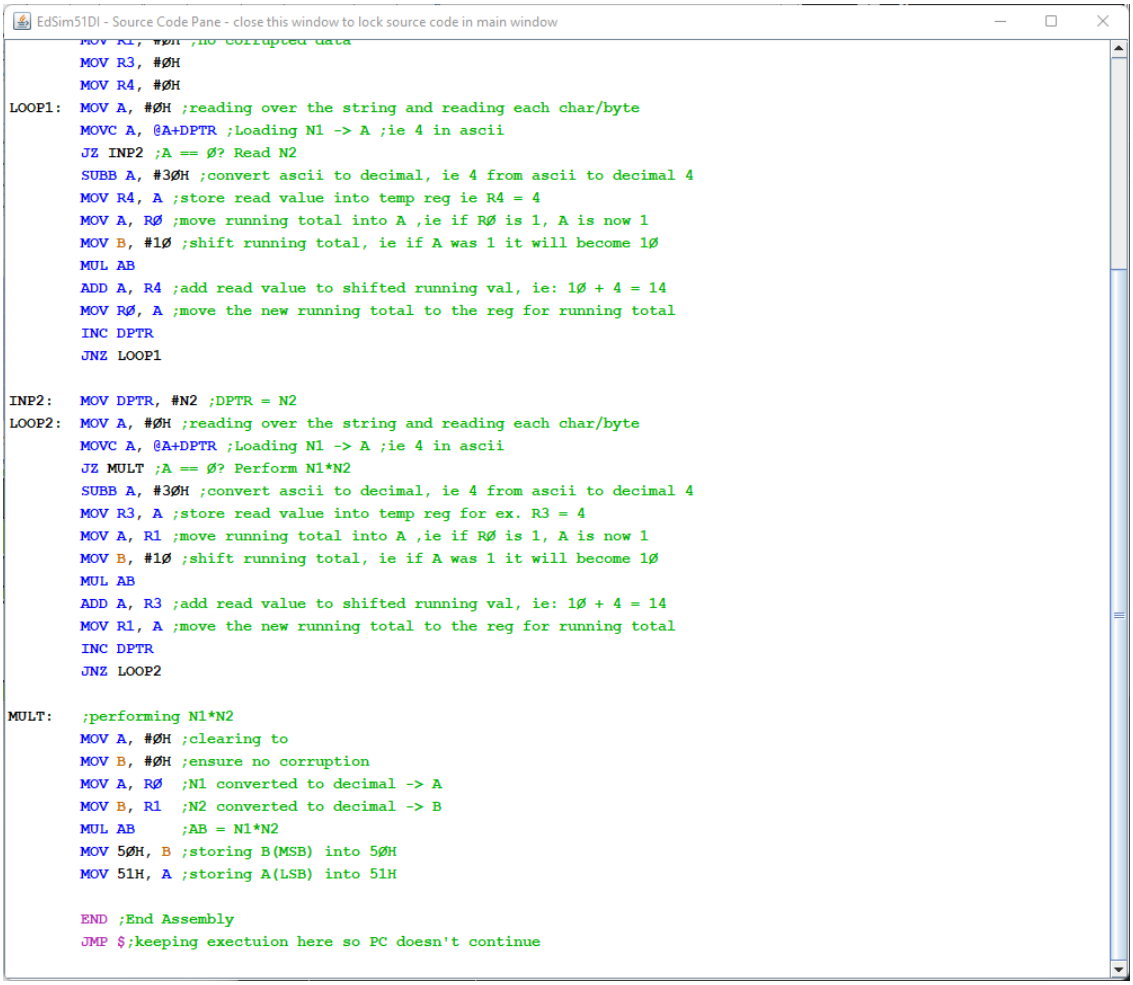
For this assignment we are to convert two input strings from ASCII digits to their proper decimal representation. We then take the converted strings and multiply the values and store them in data memory locations 50H (MSB) and 51H (LSB).

EdSim51DI - Source Code Pane - close this window to lock source code in main window

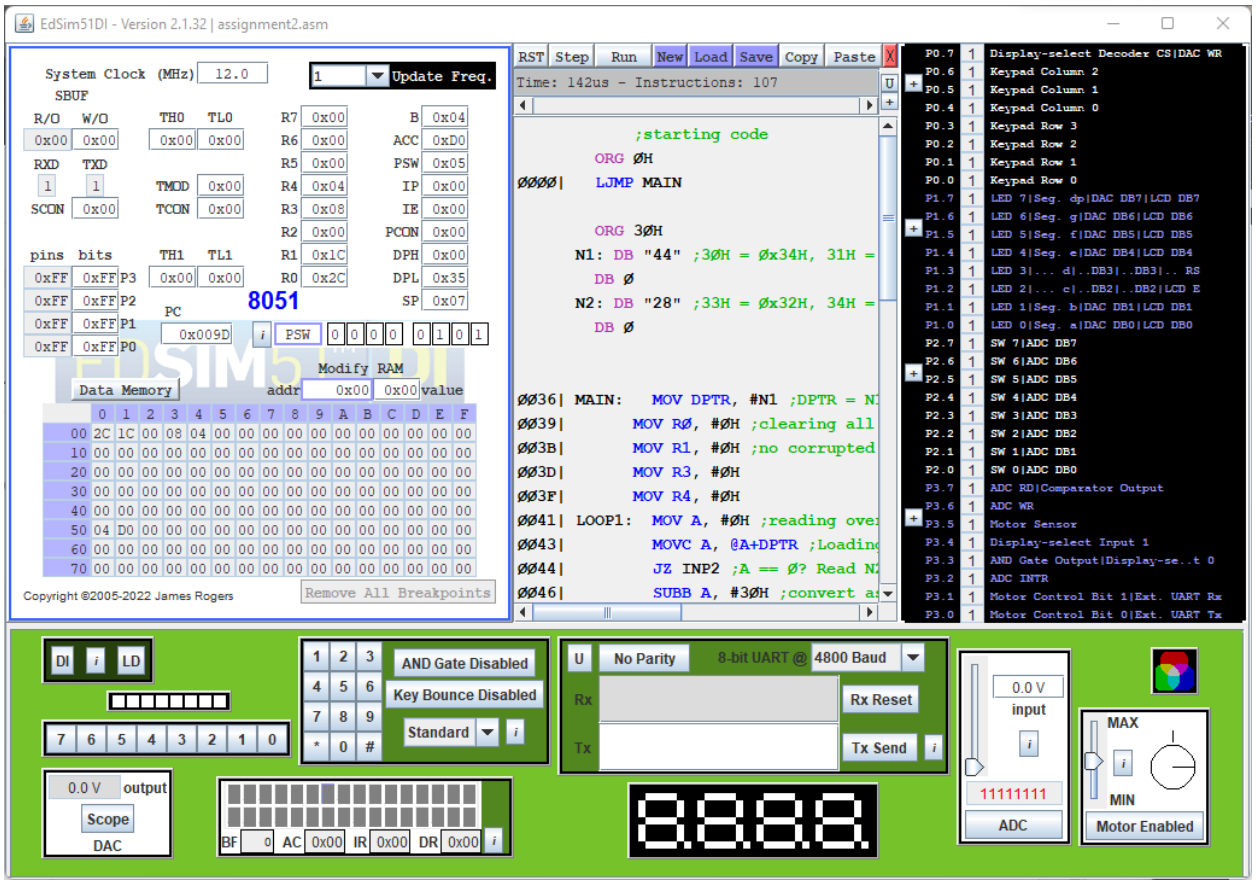
```
    ;starting code
    LJMP MAIN

    ORG 3ØH
N1: DB "44" ;3ØH = Øx34H, 31H = Øx34H
    DB Ø
N2: DB "28" ;33H = Øx32H, 34H = Øx38H
    DB Ø


MAIN:   MOV DPTR, #N1 ;DPTR = N1
        MOV RØ, #ØH ;clearing all registers to ensure
        MOV R1, #ØH ;no corrupted data
        MOV R3, #ØH
        MOV R4, #ØH
LOOP1:  MOV A, #ØH ;reading over the string and reading each char/byte
        MOVC A, @A+DPTR ;Loading N1 -> A ;ie 4 in ascii
        JZ INP2 ;A == Ø? Read N2
        SUBB A, #3ØH ;convert ascii to decimal, ie 4 from ascii to decimal 4
        MOV R4, A ;store read value into temp reg ie R4 = 4
        MOV A, RØ ;move running total into A ,ie if RØ is 1, A is now 1
        MOV B, #1Ø ;shift running total, ie if A was 1 it will become 1Ø
        MUL AB
        ADD A, R4 ;add read value to shifted running val, ie: 1Ø + 4 = 14
        MOV RØ, A ;move the new running total to the reg for running total
        INC DPTR
        JNZ LOOP1

INP2:   MOV DPTR, #N2 ;DPTR = N2
LOOP2:  MOV A, #ØH ;reading over the string and reading each char/byte
        MOVC A, @A+DPTR ;Loading N1 -> A ;ie 4 in ascii
        JZ MULT ;A == Ø? Perform N1*N2
        SUBB A, #3ØH ;convert ascii to decimal, ie 4 from ascii to decimal 4
        MOV R3, A ;store read value into temp reg for ex. R3 = 4
        MOV A, R1 ;move running total into A ,ie if RØ is 1, A is now 1
        MOV B, #1Ø ;shift running total, ie if A was 1 it will become 1Ø
        MUL AB
        ADD A, R3 ;add read value to shifted running val, ie: 1Ø + 4 = 14
        MOV R1, A ;move the new running total to the reg for running total
        INC DPTR
        JNZ LOOP2
```



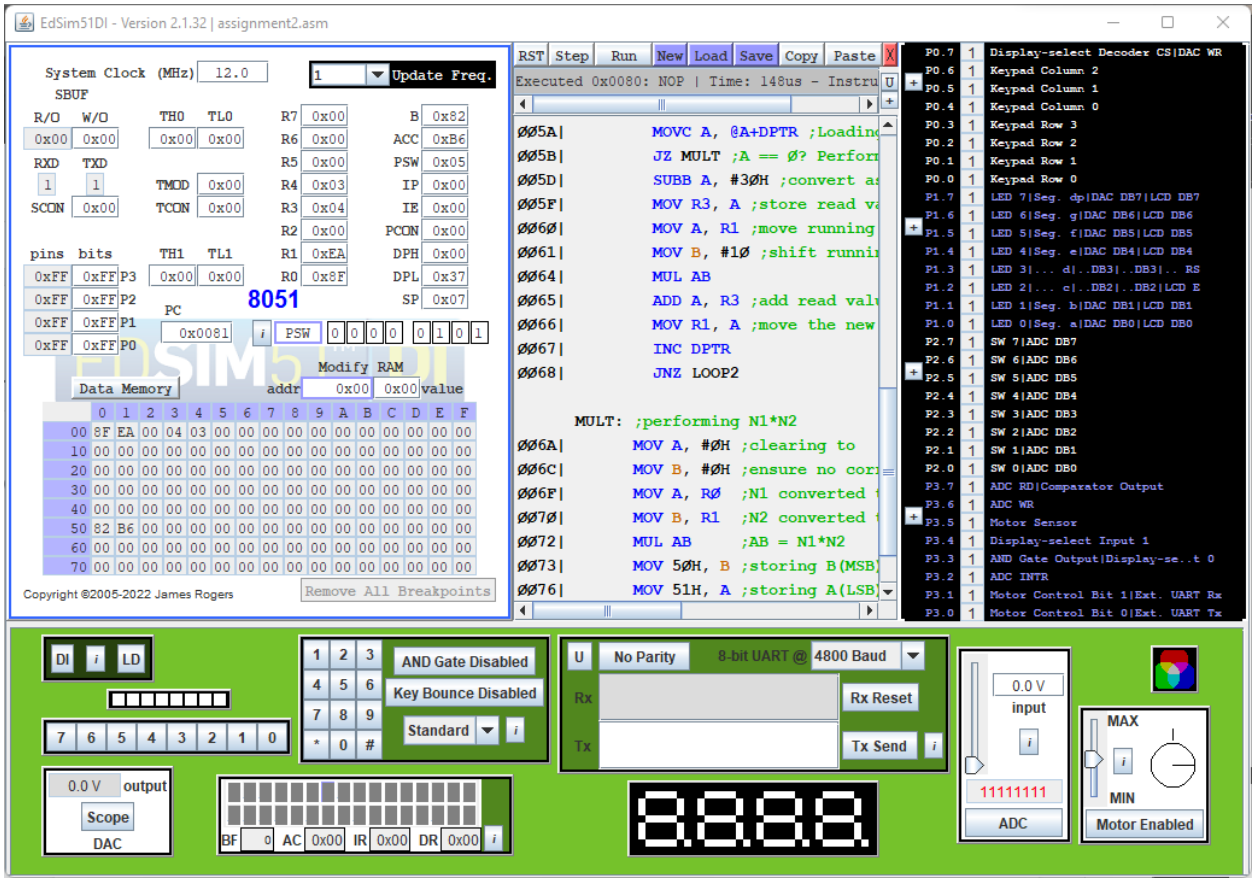
The code itself is commented and self documenting but will also be explained here. We have four main sections in which the code is broken up into; The input section, a section for retrieving N1, another section for retrieving N2, and the last section for performing the multiplication of the our retrieved values then storing it in the data memory. The first section starting from the ORG 30H instruction sets up our test cases and loads our strings into N1 and N2 at their respective labels.The next section is the setup of N1 and reading each char/byte and converting the value from ASCII to decimal in a loop, the result gets stored in R0. The section for N2 is a similar procedure, the only difference is that we store the result in different registers. The last section just loads the results onto the A and B accumulators and stores the results in our desired memory locations.

Final Memory and Register Values

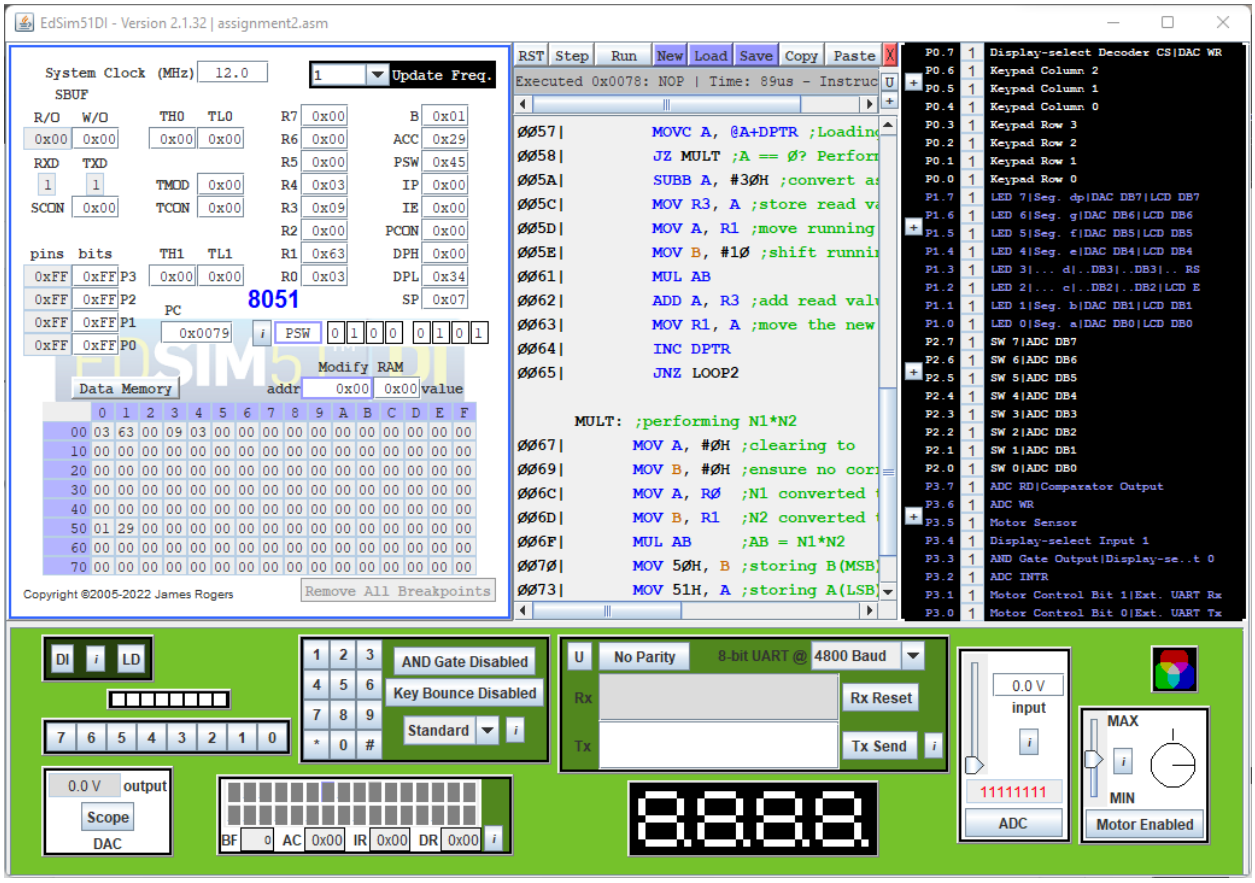
Test Case 1: Our inputs N1 and N2 are 44 and 28 respectively. We can see N1 and N2 being stored in R0 and R1 as their hexadecimal representations. (2CH and 1CH). The expected output was 04D0H which is shown in memory locations 50H and 51H.



Test Case 2: Our inputs N1 and N2 are 143 and 234 respectively. We can see N1 and N2 being stored in R0 and R1 as their hexadecimal representations. (8FH and EAH). The expected output was 82B6H which is shown in memory locations 50H and 51H.



Test Case 3: Our inputs N1 and N2 are 3 and 99 respectively. We can see N1 and N2 being stored in R0 and R1 as their hexadecimal representations. (03H and 63H). The expected output was 0129H which is shown in memory locations 50H and 51H.


EdSim51DI - Version 2.1.32 | assignment2.asm
System Clock (MHz) 12.0
Update Freq.
SBUF
R/O W/O TH0 TL0 R7 B
RXD TXD TMOD TCON R6 ACC
SCON R5 PSW
R4 IP
R3 IE
R2 PCON
pins bits TH1 TL1 R1 DPH
R0 DPL
SP
8051
PC
PSW
Modify RAM
Data Memory
addr value
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Remove All Breakpoints
RST Step Run New Load Save Copy Paste
Executed 0x0078: NOP | Time: 89us - Instruc
ØØ57| MOVC A, @A+DPTR ;Loadin
ØØ58| JZ MULT ;A == Ø? Perfor
ØØ5A| SUBB A, #3ØH ;convert a
ØØ5C| MOV R3, A ;store read v
ØØ5D| MOV A, R1 ;move running
ØØ5E| MOV B, #1Ø ;shift runni
ØØ61| MUL AB
ØØ62| ADD A, R3 ;add read val
ØØ63| MOV R1, A ;move the new
ØØ64| INC DPTR
ØØ65| JNZ LOOP2
MULT: ;performing N1*N2
ØØ67| MOV A, #ØH ;clearing to
ØØ69| MOV B, #ØH ;ensure no cor
ØØ6C| MOV A, RØ ;N1 converted
ØØ6D| MOV B, R1 ;N2 converted
ØØ6F| MUL AB ;AB = N1*N2
ØØ7Ø| MOV 5ØH, B ;storing B(MSB)
ØØ73| MOV 51H, A ;storing A(LSB)
P0.7 Display-select Decoder CS|DAC WR
P0.6 Keypad Column 2
P0.5 Keypad Column 1
P0.4 Keypad Column 0
P0.3 Keypad Row 3
P0.2 Keypad Row 2
P0.1 Keypad Row 1
P0.0 Keypad Row 0
P1.7 LED 7|Seg. dp|DAC DB7|LCD DB7
P1.6 LED 6|Seg. g|DAC DB6|LCD DB6
P1.5 LED 5|Seg. f|DAC DB5|LCD DB5
P1.4 LED 4|Seg. e|DAC DB4|LCD DB4
P1.3 LED 3|... d|..DB3|..DB3|.. RS
P1.2 LED 2|... c|..DB2|..DB2|LCD E
P1.1 LED 1|Seg. b|DAC DB1|LCD DB1
P1.0 LED 0|Seg. a|DAC DB0|LCD DB0
P2.7 SW 7|ADC DB7
P2.6 SW 6|ADC DB6
P2.5 SW 5|ADC DB5
P2.4 SW 4|ADC DB4
P2.3 SW 3|ADC DB3
P2.2 SW 2|ADC DB2
P2.1 SW 1|ADC DB1
P2.0 SW 0|ADC DB0
P3.7 ADC RD|Comparator Output
P3.6 ADC WR
P3.5 Motor Sensor
P3.4 Display-select Input 1
P3.3 AND Gate Output|Display-se..t 0
P3.2 ADC INTR
P3.1 Motor Control Bit 1|Ext. UART Rx
P3.0 Motor Control Bit 0|Ext. UART Tx
DI LD
7 6 5 4 3 2 1 0
AND Gate Disabled
Key Bounce Disabled
Standard
No Parity
8-bit UART @ 4800 Baud
Rx
Tx
Rx Reset
Tx Send
0.0 V output
Scope
DAC
BF AC 0x00 IR 0x00 DR 0x00
0.0 V input
11111111
ADC
MAX
MIN
Motor Enabled