TECHNICAL UNIVERSITY OF CRETE

DIPLOMA THESIS

Thesis Title

Author:

Thesis Committee:

Kyriakos Chalvatzis

Prof. Vasilis SAMOLADAS

Prof. Name SURNAME

Prof. Name SURNAME



A thesis submitted in fulfillment of the requirements for the diploma of Electrical and Computer Engineer

in the

School of Electrical and Computer Engineering Microprocessor and Hardware Laboratory

TECHNICAL UNIVERSITY OF CRETE

Abstract

School of Electrical and Computer Engineering

Electrical and Computer Engineer

Thesis Title

by Kyriakos Chalvatzis

The Thesis Abstract is written here (and usually kept to just this page). The page is kept centered vertically so can expand into the blank space above the title too...

TECHNICAL UNIVERSITY OF CRETE

Abstract

School of Electrical and Computer Engineering

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by Kyriakos Chalvatzis

Η περίληψη της διπλωματικής γράφεται εδώ (και συνήθως αποτελεί αυτή την μία μόνο σελίδα). Η σελίδα αυτή κρατάται στοιχισμένη στην μέση οριζόντια και κάθετα, ώστε να μπορεί να επεκτίνεται στον κενό χώρο και πάνω από τον τίτλο...

Acknowledgements

The acknowledgments and the people to thank go here, don't forget to include your project advisor...

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List of Abbreviations

ALU Arithmetic Logic Unit

ASIC Application Specific Integrated Circuit

BRAM Block Random Access Memory

CPU Central Processor Unit

CS Computer Science

DDR4 Double Data Rate type texbf4 memory

DRAM Dynamic Random Access Memory

DSP Digital Signal Processor

FF Flip Flops

FPGA Field Programmable Gate Array

GDDR6 Graphics Double Data Rate type 6 memory

GPU Graphic Processor Unit

HBM High Bandwidth Memory

HDL Hardware Description Language

HLS High Level Synthesis

HPC Hight Performance Computing

LUT Look Up Table

MPSoC Multi Processor System on Chip

PL Programmable Logic
PS Processing System

RAM Random Access MemorySDK Software Development Kit

SIMD Single Instruction Multiple Data

SSE Streaming SIMD Extensions

SSD Solid State Drive

TDP Thermal Design Power

URAM Ultra Random Access Memory

USD United States Dollar

Dedicated to my family and friends...