

arm_dct4_init_f32

```
graph LR; A[arm_dct4_init_f32] --> B[arm_rfft_init_f32]; B --> C[arm_cfft_radix4_init_f32];
```

The diagram illustrates a sequence of three ARM DSP initialization functions. The first function, 'arm_dct4_init_f32', is highlighted with a gray background. A blue arrow points from it to the second function, 'arm_rfft_init_f32', which has a white background. Another blue arrow points from the second function to the third function, 'arm_cfft_radix4_init_f32', also with a white background. All three functions are contained within rectangular boxes with black borders.

arm_rfft_init_f32

arm_cfft_radix4_init_f32