DMA_InitTypeDef

+ Channel
+ Direction
+ PeriphInc
+ MemInc
+ PeriphDataAlignment
+ MemDataAlignment
+ Mode
+ Priority
+ FIFOMode
+ FIFOThreshold
+ MemBurst
+ PeriphBurst

DMA_Stream_TypeDef

+   CR
+   NDTR
+   PAR
+   M0AR
+   M1AR
+   FCR

+Instance   +Init

__DMA_HandleTypeDef

+ Lock
+ State
+ Parent
+ XferCpltCallback
+ XferHalfCpltCallback
+ XferM1CpltCallback
+ XferM1HalfCpltCallback
+ XferErrorCallback
+ XferAbortCallback
+ ErrorCode
+ StreamBaseAddress
+ StreamIndex

USART_TypeDef

+   SR
+   DR
+   BRR
+   CR1
+   CR2
+   CR3
+   GTPR

UART_InitTypeDef

+ BaudRate
+ WordLength
+ StopBits
+ Parity
+ Mode
+ HwFlowCtl
+ OverSampling

+Instance   +Init

+hdmarx

+hdmatx

__UART_HandleTypeDef

+ pTxBuffPtr
+ TxXferSize
+ TxXferCount
+ pRxBuffPtr
+ RxXferSize
+ RxXferCount
+ ReceptionType
+ RxEventType
+ Lock
+ gState
+ RxState
+ ErrorCode