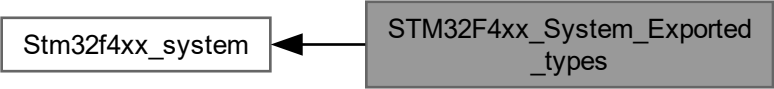


Stm32f4xx\_system

STM32F4xx\_System\_Exported  
\_types



```
graph LR; A[STM32F4xx_System_Exported_types] --> B[Stm32f4xx_system]
```

The diagram consists of two rectangular boxes. The box on the left is white with a black border and contains the text 'Stm32f4xx\_system'. The box on the right is gray with a black border and contains the text 'STM32F4xx\_System\_Exported' on the top line and '\_types' on the bottom line. A black arrow points from the right side of the gray box to the left side of the white box.