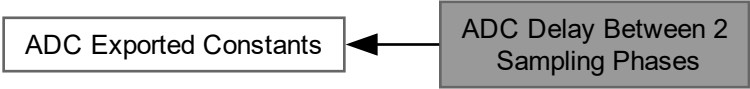


ADC Exported Constants

ADC Delay Between 2
Sampling Phases



```
graph LR; A[ADC Delay Between 2 Sampling Phases] --> B[ADC Exported Constants]
```

The diagram consists of two rectangular boxes. The box on the left is white with a thin black border and contains the text "ADC Exported Constants". The box on the right is gray with a thin black border and contains the text "ADC Delay Between 2 Sampling Phases". A black arrow points from the right side of the gray box to the left side of the white box, indicating a relationship or flow from the delay to the constants.