

arm_atan2_f32



```
graph LR; A[arm_atan2_f32] --> B[arm_atan_f32]; B --> C[arm_atan_limited_f32];
```

The diagram illustrates a three-step process flow. It begins with a box labeled 'arm_atan2_f32', followed by an arrow pointing to a box labeled 'arm_atan_f32', which then points to a final box labeled 'arm_atan_limited_f32'. The first two boxes are white with black borders, while the final box is gray with a black border.

arm_atan_f32

arm_atan_limited_f32