

arm_dct4_f32

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graph LR; A[arm_dct4_f32] --> B[arm_rfft_f32]; B --> C[arm_split_rfft_f32];
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The diagram illustrates a sequence of three ARM DSP functions. The first function, 'arm_dct4_f32', is shown in a white box. A blue arrow points from it to the second function, 'arm_rfft_f32', which is also in a white box. Another blue arrow points from the second function to the third function, 'arm_split_rfft_f32', which is shown in a gray box. This visual distinction likely indicates that the third function is the final or primary output of the sequence.

arm_rfft_f32

arm_split_rfft_f32