

arm_dct4_init_q31

arm_mfcc_init_q31

arm_rfft_init_q31

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graph LR; A[arm_dct4_init_q31] --> C[arm_rfft_init_q31]; B[arm_mfcc_init_q31] --> C;
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The diagram illustrates a dependency or data flow. On the left, there are two white rectangular boxes with black borders. The top box contains the text 'arm_dct4_init_q31' and the bottom box contains 'arm_mfcc_init_q31'. On the right, there is a single gray rectangular box with a black border containing the text 'arm_rfft_init_q31'. Two blue arrows originate from the right side of the left boxes and point towards the left side of the gray box, indicating that both 'arm_dct4_init_q31' and 'arm_mfcc_init_q31' are inputs or dependencies for 'arm_rfft_init_q31'.