

arm_dct4_init_q15

```
graph LR; A[arm_dct4_init_q15] --> B[arm_rfft_init_q15]; B --> C[arm_cfft_init_q15];
```

The diagram illustrates a sequence of three initialization functions for ARM DSP. The first function, 'arm_dct4_init_q15', is highlighted with a dark gray background. It is followed by 'arm_rfft_init_q15' and 'arm_cfft_init_q15', which have white backgrounds. Blue arrows indicate the flow from left to right between the functions.

arm_rfft_init_q15

arm_cfft_init_q15