

I2C_TypeDef
+ CR1
+ CR2
+ OAR1
+ OAR2
+ DR
+ SR1
+ SR2
+ CCR
+ TRISE
+ FLTR

I2C_InitTypeDef
+ ClockSpeed
+ DutyCycle
+ OwnAddress1
+ AddressingMode
+ DualAddressMode
+ OwnAddress2
+ GeneralCallMode
+ NoStretchMode

DMA_Stream_TypeDef
+ CR
+ NDTR
+ PAR
+ M0AR
+ M1AR
+ FCR

DMA_InitTypeDef
+ Channel
+ Direction
+ PeriphInc
+ MemInc
+ PeriphDataAlignment
+ MemDataAlignment
+ Mode
+ Priority
+ FIFOMode
+ FIFOThreshold
+ MemBurst
+ PeriphBurst

__DMA_HandleTypeDef
+ Lock
+ State
+ Parent
+ XferCpltCallback
+ XferHalfCpltCallback
+ XferM1CpltCallback
+ XferM1HalfCpltCallback
+ XferErrorCallback
+ XferAbortCallback
+ ErrorCode
+ StreamBaseAddress
+ StreamIndex

I2C_HandleTypeDef
+ pBuffPtr
+ XferSize
+ XferCount
+ XferOptions
+ PreviousState
+ Lock
+ State
+ Mode
+ ErrorCode
+ Devaddress
+ Memaddress
+ MemaddSize
+ EventCount

+Instance

+Init

+Instance

+Init

+hdmarx

+hdmatx