TDT4255 Points Overview 2023

Theory track

Name	Due date	Max points	Activity type
T1: Trends and performance	01.09.2023	10	Lecture
T2: Instruction sets	07.09.2023	10	Lecture
T3: Single-cycle datapath and pipelining	15.09.2023	10	Lecture
T4: Out-of-order execution	28.09.2023	10	Lecture
T5: Branch prediction	12.10.2023	10	Lecture
T6: The memory system	19.10.2023	10	Lecture
T7: Bringing it all together -- the interval model	26.10.2023	10	Lecture
T8: Microarchitectural design options	02.11.2023	10	Lecture
E1: Trends and performance exercise	07.09.2023	10	Theory exercise
E2: Instruction set exercise	15.09.2023	10	Theory exercise
E3: Single-cycle datapath and pipelining exercise	21.09.2023	10	Theory exercise
E4: Out-of-order execution exercise	05.10.2023	10	Theory exercise
E5: Branch prediction exercise	19.10.2023	10	Theory exercise
E6: Memory system exercise	26.10.2023	10	Theory exercise
E7: Interval model exercise	02.11.2023	10	Theory exercise
E8: Microarchitectural design options exercise	09.11.2023	10	Theory exercise
E1 evaluation	15.09.2023	5	Evaluation
E2 evaluation	22.09.2023	5	Evaluation
E3 evaluation	29.09.2023	5	Evaluation
E4 evaluation	13.10.2023	5	Evaluation
E5 evaluation	27.10.2023	5	Evaluation
E6 evaluation	03.11.2023	5	Evaluation
E7 evaluation	10.11.2023	5	Evaluation
E8 evaluation	17.11.2023	5	Evaluation

Practical track

Name	Due date	Max points	Activity type

Introductory lab lecture & Chisel lecture	31.08.2023	20	Lecture
Chisel warm-up exercises	20.09.2023	20	Demonstration
RISC-V CPU project introduction	14.09.2023	20	Lecture
CPU functionality milestone 1	11.10.2023	30	Demonstration
CPU functionality milestone 2	18.10.2023	30	Demonstration
CPU functionality milestone 3	01.11.2023	30	Demonstration
CPU project final delivery	15.11.2023	100	Practical exercise

Total

Name	Max points
Theory track points in total	200
Practical track points in total	250
Points in total	**450**
Points required to take the final exam	**350**