Tabela Disassembler

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| Endereço | Conteúdo | Rótulo | Mnemônico Instrução |
| 0x00000800 | 0xb500 | delay: | push {lr} |
| 0x00000802 | 0x2800 | again: | cmp r0,#0 |
| 0x00000804 | 0xd001 |  | beq return |
| 0x00000806 | 0x3801 |  | sub r0,#1 |
| 0x00000808 | 0xe7fd |  | b again |
| 0x0000080a | 0xbd00 | return: | pop {pc} |
| 0x0000080c | 0x4b10 | main: | ldr r3,SIM\_SCGC5 |
| 0x0000080e | 0x681a |  | ldr r2,[r3] |
| 0x00000810 | 0x2180 |  | movs r1,#128 |
| 0x00000812 | 0x00c9 |  | lsl r1,r1,#3 |
| 0x00000814 | 0x430a |  | orr r2,r1 |
| 0x00000816 | 0x601a |  | str r2,[r3] |
| 0x00000818 | 0x4b0e |  | ldr r3,PORTB\_PCR18 |
| 0x0000081a | 0x681a |  | ldr r2,[r3] |
| 0x0000081c | 0x4910 |  | ldr r1,MUXMASK |
| 0x0000081e | 0x400a |  | and r2,r1 |
| 0x00000820 | 0x601a |  | str r2,[r3] |
| 0x00000822 | 0x4b0c |  | ldr r3,PORTB\_PCR18 |
| 0x00000824 | 0x681a |  | ldr r2,[r3] |
| 0x00000826 | 0x2180 |  | movs r1,#128 |
| 0x00000828 | 0x0049 |  | lsl r1,r1,#1 |
| 0x0000082a | 0x430a |  | orr r2,r1 |
| 0x0000082c | 0x601a |  | str r2,[r3] |
| 0x0000082e | 0x4b0a |  | ldr r3,GPIOB\_PDDR |
| 0x00000830 | 0x681a |  | ldr r2,[r3] |
| 0x00000832 | 0x2180 |  | movs r1,#128 |
| 0x00000834 | 0x02c9 |  | lsl r1,r1,#11 |
| 0x00000836 | 0x430a |  | orr r2,r1 |
| 0x00000838 | 0x601a |  | str r2,[r3] |
| 0x0000083a | 0x4b08 | toggle: | ldr r3,GPIOB\_PTOR |
| 0x0000083c | 0x681a |  | ldr r2,[r3] |
| 0x0000083e | 0x2180 |  | movs r1,#128 |
| 0x00000840 | 0x02c9 |  | lsl r1,r1,#11 |
| 0x00000842 | 0x430a |  | orr r2,r1 |
| 0x00000844 | 0x601a |  | str r2,[r3] |
| 0x00000846 | 0x4807 |  | ldr r0,WAIT |
| 0x00000848 | 0xf7ff |  | bl delay |
| 0x0000084c | 0xe7f5 |  | b toggle |
| 0x00000850 | 0x40048038 | SIM\_SCGC5: |  |
| 0x00000854 | 0x4004A048 | PORTB\_PCR18: |  |
| 0x00000858 | 0x400ff054 | GPIOB\_PDDR: |  |
| 0x0000085c | 0x400ff04c | GPIOB\_PTOR: |  |
| 0x00000860 | 0xfffff8ff | MUXMASK: |  |
| 0x00000864 | 0x004c4b40 | WAIT: |  |

Tabela de Símbolos

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| Símbolo | Valor |
| delay | 0x00000800 |
| again | 0x00000802 |
| return | 0x0000080a |
| main | 0x0000080c |
| toggle | 0x0000083a |
| SIM\_SCGC5 | 0x00000850 |
| PORTB\_PCR18 | 0x00000854 |
| GPIOB\_PDDR | 0x00000858 |
| GPIOB\_PTOR | 0x0000085c |
| MUXMAK | 0x00000860 |
| WAIT | 0x00000864 |