Title Is Here

Author Name

A thesis submitted for the degree of  
YOUR DEGREE NAME

The Australian National University

May 2012

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Except where otherwise indicated, this thesis is my own original work.

Author Name

2 May 2012

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Acknowledgments

Who do you want to thank?

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Abstract

Put your abstract here.

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Introduction

1. Thesis Statement

I believe A is better than B.

1. Introduction

Put your introduction here. You could use \fix{ABCDEFG.} to leave your comments, see the box at the left side.

You have to rewrite your thesis!!!

1. Thesis Outline

How many chapters you have? You may have Chapter [2,](#bookmark62) Chapter [3,](#bookmark83) Chapter [4,](#bookmark95) Chapter [5,](#bookmark120) and Chapter [6.](#bookmark132)

Background and Related Work

At the begging of each chapter, please introduce the motivation and high-level pic­ture of the chapter. You also have to introduce sections in the chapter.

Section [2.1](#bookmark67) xxxx.

Section [2.2](#bookmark72) yyyy.

1. Motivation
2. Related work

You may reference other papers. For example: Generational garbage collection [[Lieber](#bookmark138)­[man and Hewitt, 1983;](#bookmark138) [Moon;](#bookmark139) [Ungar, 1984]](#bookmark140) is perhaps the single most important advance in garbage collection since the first collectors were developed in the early 1960s. (doi: "doi" should just be the doi part, not the full URL, and it will be made to link to dx.doi.org and resolve. shortname: gives an optional short name for a conference like PLDI '08.)

1. Summary

Summary what you discussed in this chapter, and mention the story in next chapter. Readers should roughly understand what your thesis takes about by only reading words at the beginning and the end (Summary) of each chapter.

Design and Implementation

Same as the last chapter, introduce the motivation and the high-level picture to read­ers, and introduce the sections in this chapter.

* 1. Smart Design
  2. Summary

Same as the last chapter, summary what you discussed in this chapter and be the bridge to next chapter.

Experimental Methodology

1. Software platform
2. Hardware platform

Table [4.1](#bookmark109) shows how to include tables and Figure [4.1](#bookmark110) shows how to include codes.

|  |  |  |  |
| --- | --- | --- | --- |
| Architecture | Pentium 4 | Atom D510 | i7-2600 |
| Model | P4D 820 | Atom D510 | Core i7-2600 |
| Technology | 90nm | 45nm | 32nm |
| Clock | 2.8GHz | 1.66GHz | 3.4GHz |
| Cores x SMT | 2x2 | 2x2 | 4x2 |
| L2 Cache | 1MB x 2 | 512KB x 2 | 256KB x 4 |
| L3 Cache | none | none | 8MB |
| Memory | 1GB DDR2-400 | 2GB DDR2-800 | 4GB DDR3-1066 |

Table 4.1: Processors used in our evaluation.

*Experimental Methodology*

1. **int** main(**void**)
2. {
3. printf("Hello\_1World\n");
4. **return** 0;
5. }

(a)

1 **void** main(String[] args)

2{

1. System. out. println( "HellO-.World");
2. }

(b)

Figure 4.1: Hello world in Java and C.

**Chapter 5**

Results

1. Direct Cost

Here is the example to show how to include a figure. Figure 5.1 includes two subfigures (Figure 5.1(a), and Figure [5.1(b))](#bookmark131);

1. Summary

Zeroed memory/burst trans Zeroing cycles

Core2 Quad i

Phenom II i

i7-2600

0.45

0.4

0.35

0.3

0.25

0.2

0.15

0.1

0.05

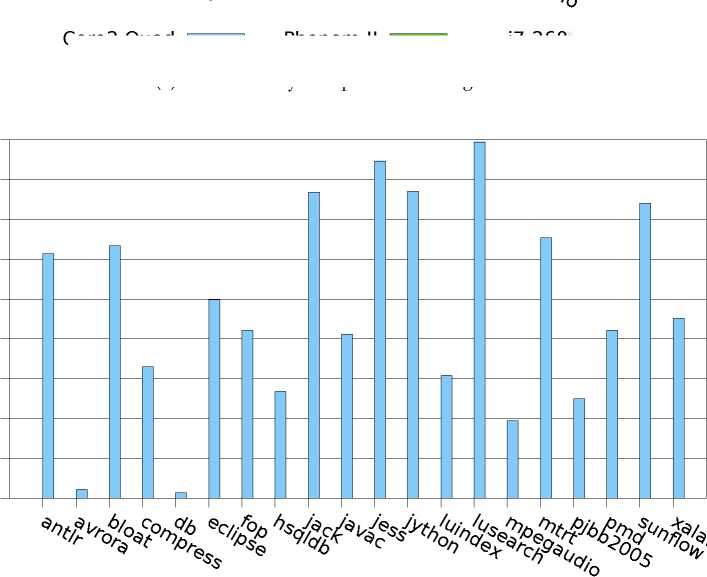
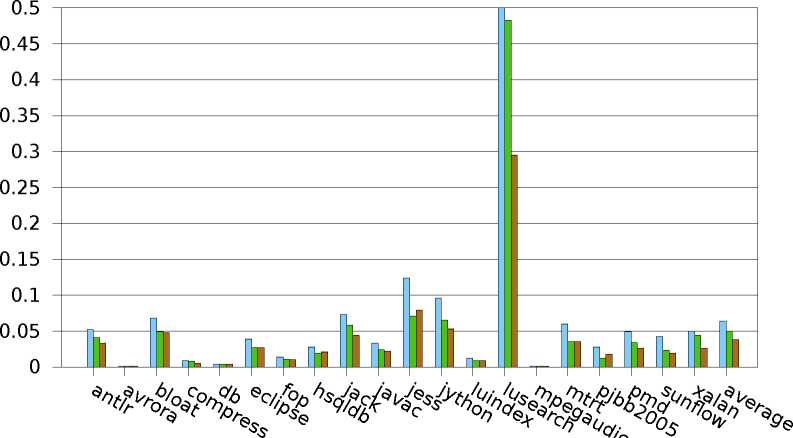
0

Core2 Quad i

(b) BytesZeroed / BytesBurstTransactionsTransferred

(a) Fraction of cycles spent on zeroing

Figure 5.1: The cost of zero initialization



**Chapter 6**

Conclusion

Summary your thesis and discuss what you are going to do in the future in Sec­tion [6.1.](#bookmark133)

6.1 Future Work

Good luck.

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