Virliana Annida Rekkayushar

L200180017

	Binary	Oct	Dec	Hex	Glyph	Binary	Oct	Dec	Hex	Glyph	Binary	Oct	Dec	Hex	Glyph
	010 0000	040	32	20	50	100 0000	100	64	40	@	110 0000	140	96	60	
	010 0001	041	33	21	- 1	100 0001	101	65	41	A	110 0001	141	97	61	а
	010 0010	042	34	22		100 0010	102	66	42	В	110 0010	142	98	62	b
Ì	010 0011	043	35	23	#	100 0011	103	67	43	С	110 0011	143	99	63	С
ı	010 0100	044	36	24	\$	100 0100	104	68	44	D	110 0100	144	100	64	d
1	010 0101	045	37	25	%	100 0101	105	69	45	Е	110 0101	145	101	65	е
	010 0110	046	38	26	8	100 0110	106	70	46	F	110 0110	146	102	66	f
1	010 0111	047	39	27	15	100 0111	107	71	47	G	110 0111	147	103	67	g
1	010 1000	050	40	28	(100 1000	110	72	48	н	110 1000	150	104	68	h
	010 1001	051	41	29)	100 1001	111	73	49	- 1	110 1001	151	105	69	i
	010 1010	052	42	2A	*	100 1010	112	74	4A	J	110 1010	152	106	6A	1
	010 1011	053	43	28	+	100 1011	113	75	48	К	110 1011	153	107	68	k
İ	010 1100	054	44	2C		100 1100	114	76	4C	L	110 1100	154	108	6C	1
Ì	010 1101	055	45	2D	-	100 1101	115	77	4D	M	110 1101	155	109	6D	m
	010 1110	056	46	2E		100 1110	116	78	4E	N	110 1110	156	110	6E	n
	010 1111	057	47	2F	1	100 1111	117	79	4F	0	110 1111	157	111	6F	0
l	011 0000	060	48	30	0	101 0000	120	80	50	Р	111 0000	160	112	70	р
0	11 0001	061	49	31	1	101 0001	121	81	51	Q	111 0001	161	113	71	q
l	011 0010	062	50	32	2	101 0010	122	82	52	R	111 0010	162	114	72	r
	011 0011	063	51	33	3	101 0011	123	83	53	S	111 0011	163	115	73	s
	011 0100	064	52	34	4	101 0100	124	84	54	T	111 0100	164	116	74	t
	011 0101	065	53	35	5	101 0101	125	85	55	U	111 0101	165	117	75	U
	011 0110	066	54	36	6	101 0110	126	86	56	V	111 0110	166	118	76	v
	011 0111	067	55	37	7	101 0111	127	87	57	W	111 0111	167	119	77	w
	011 1000	070	56	38	8	101 1000	130	88	58	х	111 1000	170	120	78	х
	011 1001	071	57	39	9	101 1001	131	89	59	Υ	111 1001	171	121	79	У
	011 1010	072	58	за		101 1010	132	90	5A	Z	111 1010	172	122	7A	z
	011 1011	073	59	38	7	101 1011	133	91	5B	1	111 1011	173	123	78	-{
	011 1100	074	60	30	<	101 1100	134	92	5C	1	111 1100	174	124	7C	1
	011 1101	075	61	3D	=	101 1101	135	93	5D	1	111 1101	175	125	7D)
	011 1110	076	62	3E	>	101 1110	136	94	5E		111 1110	176	126	7E	~

2.

Daftar Assembly Directive

Assembly Directive Keterangan

EQU Pendefinisian konstanta

DB Pendefinisian data dengan ukuran satuan 1 byte

DW Pendefinisian data dengan ukuran satuan 1 word

DBIT Pendefinisian data dengan ukuran satuan 1 bit

DS Pemesanan tempat penyimpanan data di RAM

ORG Inisialisasi alamat mulai program

END Penanda akhir program

CSEG Penanda penempatan di code segment

XSEG Penanda penempatan di external data segment

DSEG Penanda penempatan di internal direct data segment

ISEG Penanda penempatan di internal indirect data segment

BSEG Penanda penempatan di bit data segment

CODE Penanda mulai pendefinisian program

XDATA Pendefinisian external data

DATA Pendefinisian internal direct data

IDATA Pendefinisian internal indirect data

BIT Pendefinisian data bit

#INCLUDE Mengikutsertakan file program lain

Daftar Instruksi

Instruksi Keterangan Singkatan

ACALL Absolute Call

ADD Add

ADDC Add with Carry

AJMP Absolute Jump

ANL AND Logic

CJNE Compare and Jump if Not Equal

CLR Clear

CPL Complement

DA Decimal Adjust

DEC Decrement

DIV Divide

DJNZ Decrement and Jump if Not Zero

INC Increment

JB Jump if Bit Set

JBC Jump if Bit Set and Clear Bit

JC Jump if Carry Set

JMP Jump to Address

JNB Jump if Not Bit Set

JNC Jump if Carry Not Set

JNZ Jump if Accumulator Not Zero

JZ Jump if Accumulator Zero

LCALL Long Call

LJMP Long Jump

MOV Move from Memory

MOVC Move from Code Memory

MOVX Move from Extended Memory

MUL Multiply

NOP No Operation

ORL OR Logic

POP Pop Value From Stack

PUSH Push Value Onto Stack

RET Return From Subroutine

RETI Return From Interrupt

RL Rotate Left

RLC Rotate Left through Carry

RR Rotate Right

RRC Rotate Right through Carry

SETB Set Bit

SJMP Short Jump

SUBB Subtract With Borrow

SWAP Swap Nibbles

XCH Exchange Bytes

XCHD Exchange Digits

XRL Exclusive OR Logic