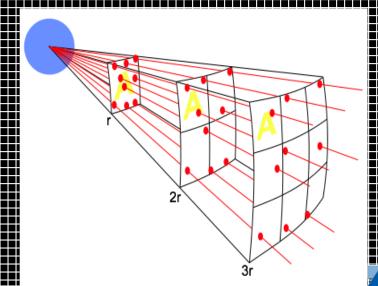
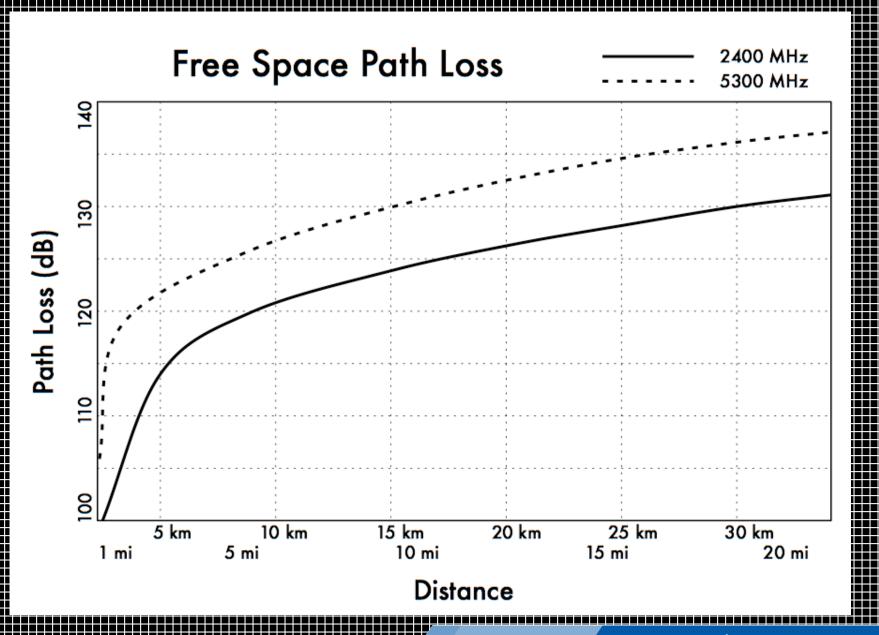
LINK BLECKLUZ ELIZER

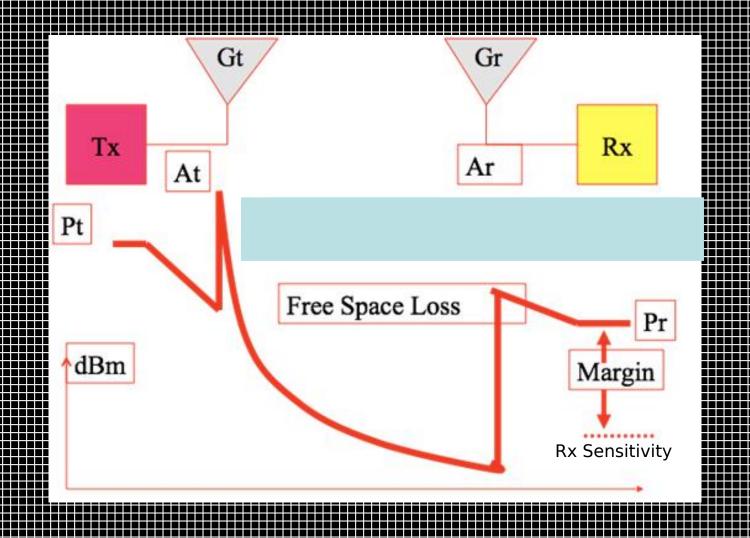


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Maximum Received Signal Strength

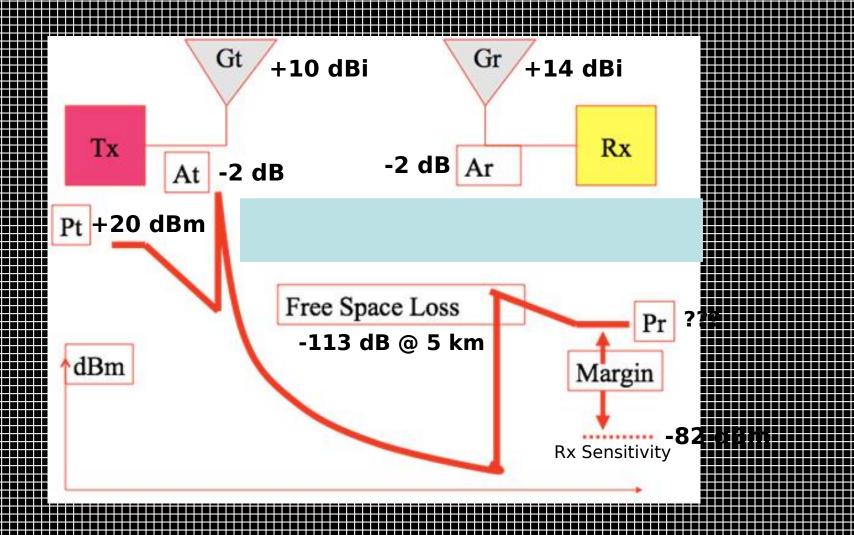
Fade Margin = Usable Range of Receiver

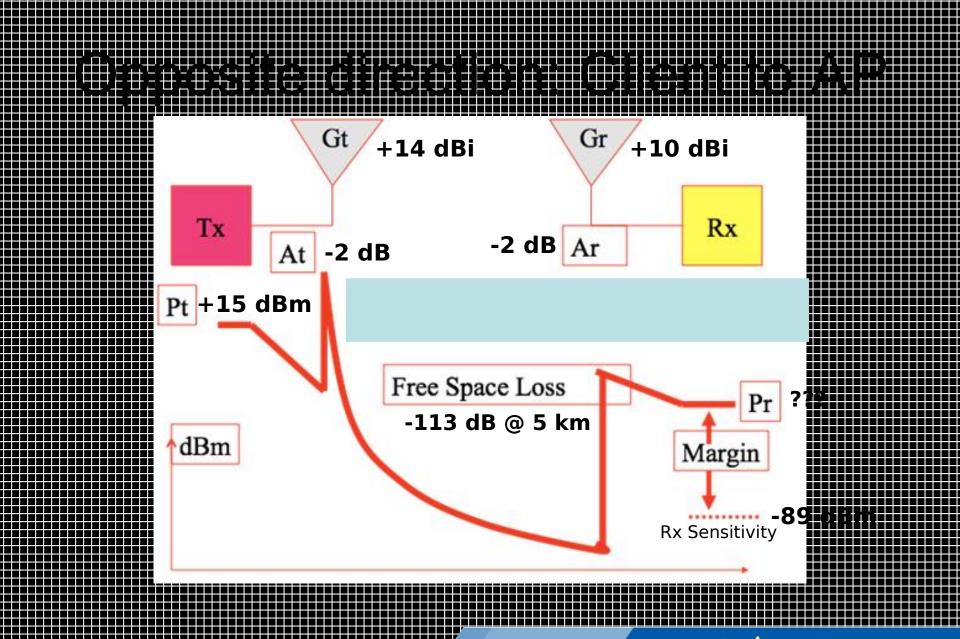
Minimum Receiver Sensitivity

The salues was a commental of the statement of the measure at a commental salues of the comment of the comment The case of the comment of the

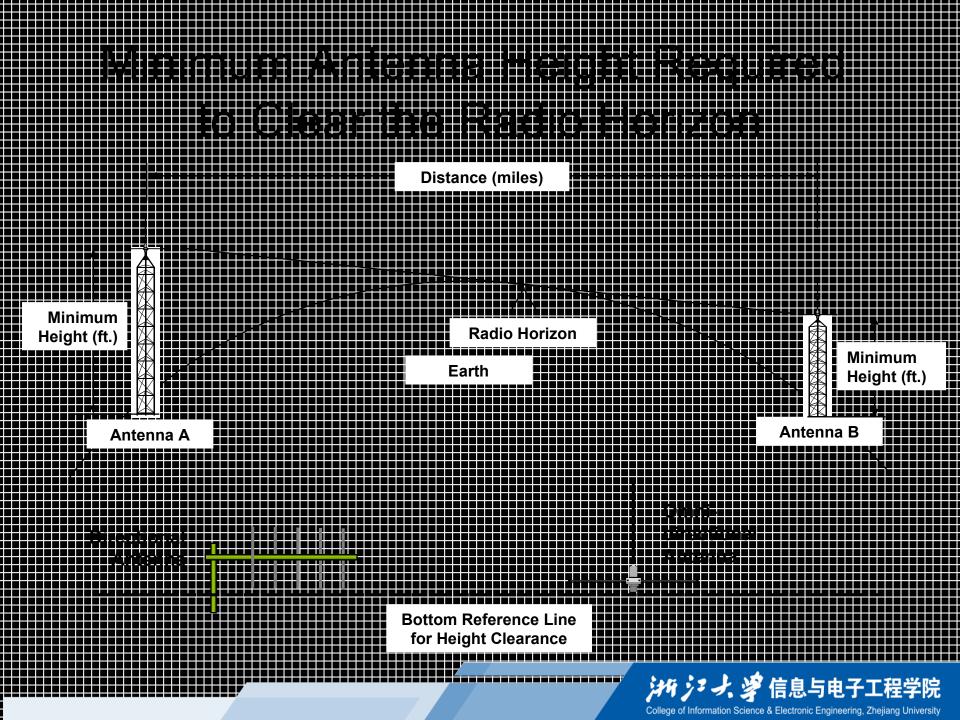
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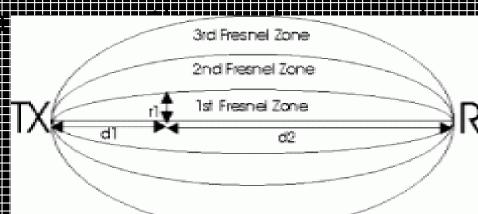




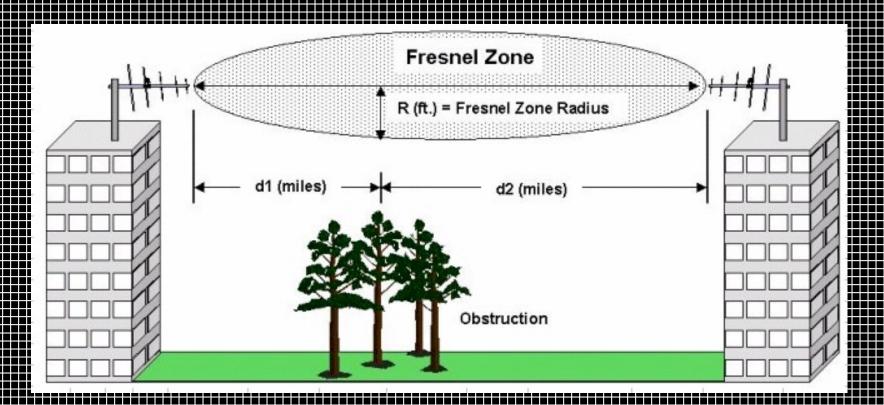
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The general equation for calculating the Fresnel zone radius at any point P in between the endpoints of the link is the following:

$$F_n = \sqrt{\frac{n\lambda d_1 d_2}{d_1 + d_2}}$$

where,

 F_n = The nth Fresnel Zone radius in metres

 d_1 = The distance of P from one end in metres

 d_2 = The distance of P from the other end in metres

 $^{\lambda}$ = The wavelength of the transmitted signal in metres

Now we have an easy way to calculate the radius of the first Fresnel zone (F_1 in the above equation), knowing the distance between the two antennas and the frequency of the transmitted signal.

In SI:
$$r = 8.657\sqrt{\frac{D}{f}}$$

where

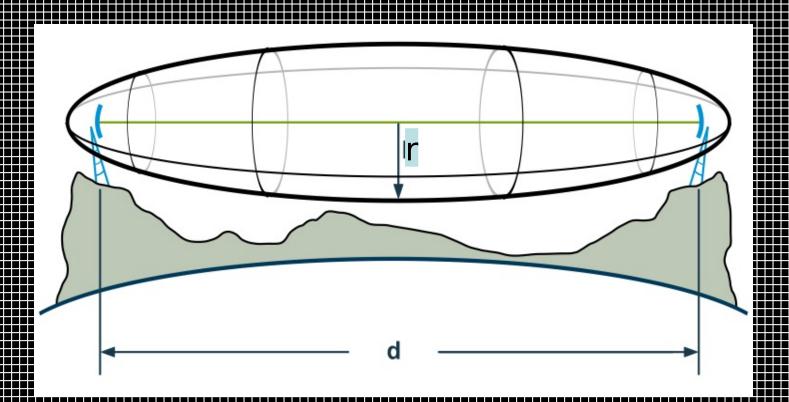
- r = radius in metres
- D = total distance in kilometres
- f = frequency transmitted in gigahertz.

Or in imperial units:

$$r = 36.03\sqrt{\frac{D}{f}}$$

where

- r = radius in feet
- D = total distance in miles
- *f* = frequency transmitted in Gigahertz.



Distance	1st zone	70%	Earth curvature	Required
(km)	(m)	(m)	(m)	Required height (m) 3.9 9.7
1	5.5	3.9	0.0	3.9
5	12.4	8.7	1.0	9.7
10	17.5	12.2	4.2	16.4
15	21.4	15.0	9.4	24.4
20	24.7	17.3	16.7	16.4 24.4 34.0 45.4
25	27.7	19.4	26.0	45.4
30	30.3	21.2	37.5	58.7

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r - Eyginedy - 10 % - 4 MG Breene - Kone highleyna - 1

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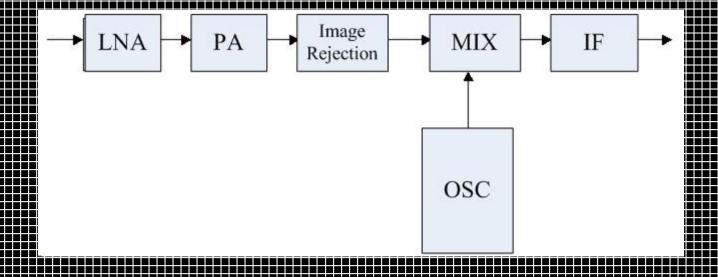
COSPELLE BROKE KETUNER



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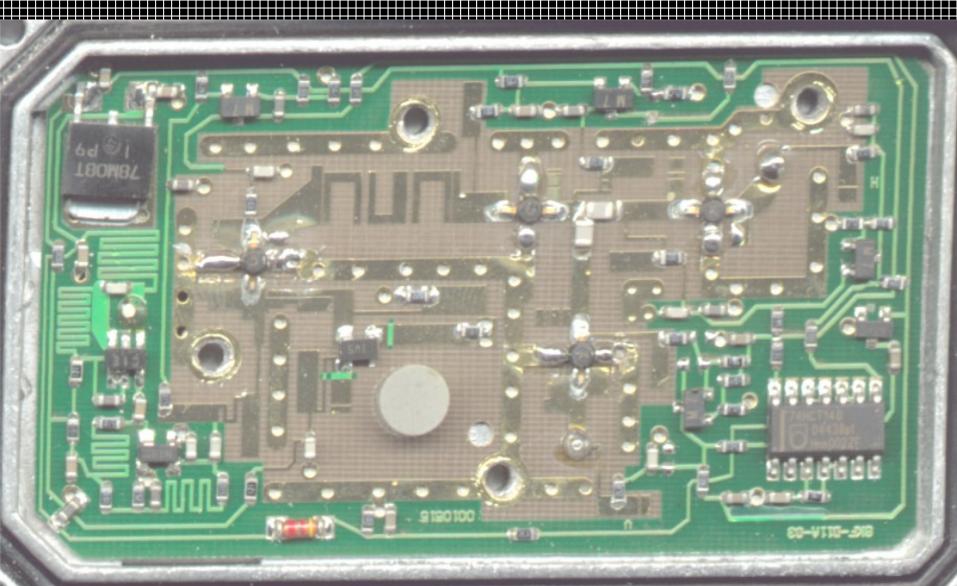


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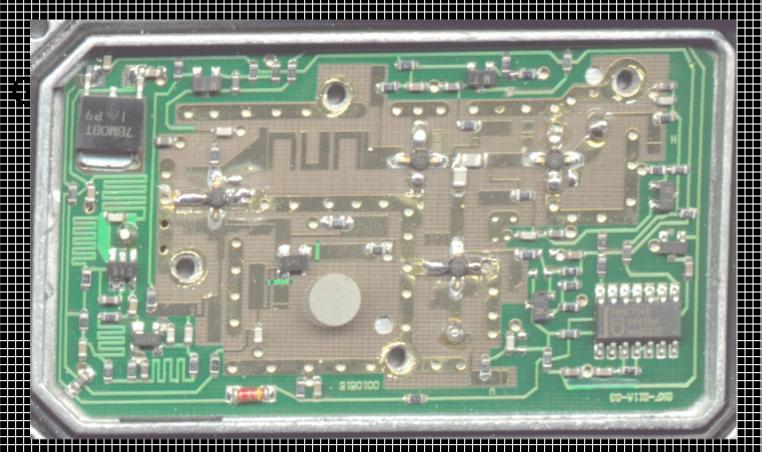
3 l ocal Oscillator -

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4. Mixer

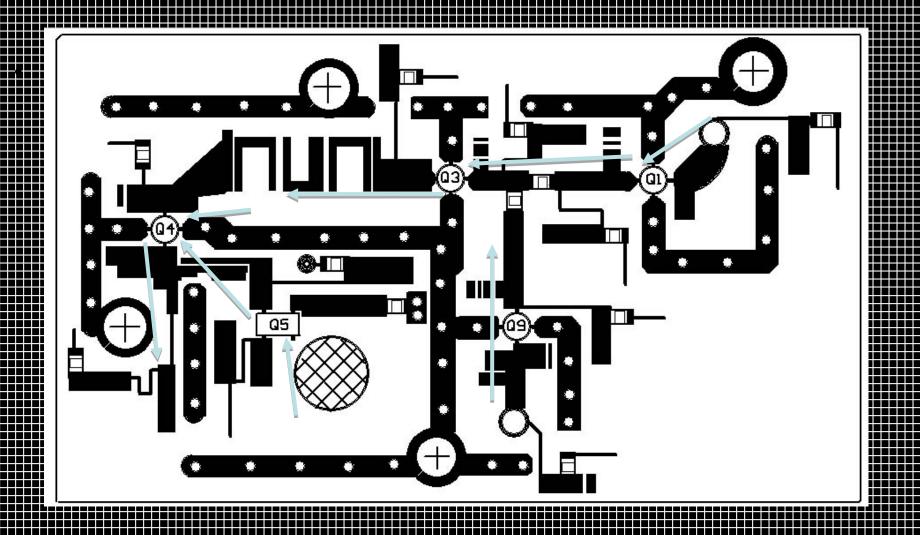


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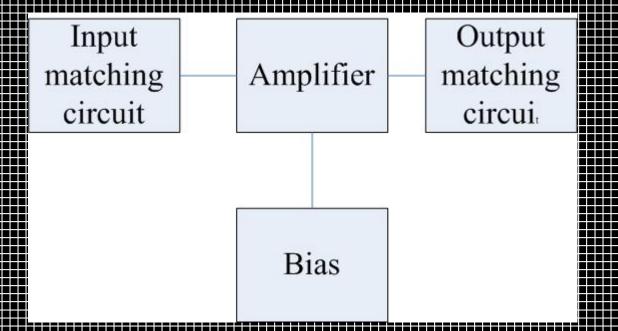
- e de l'impound de la complete de la La complete de la comp

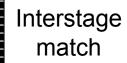
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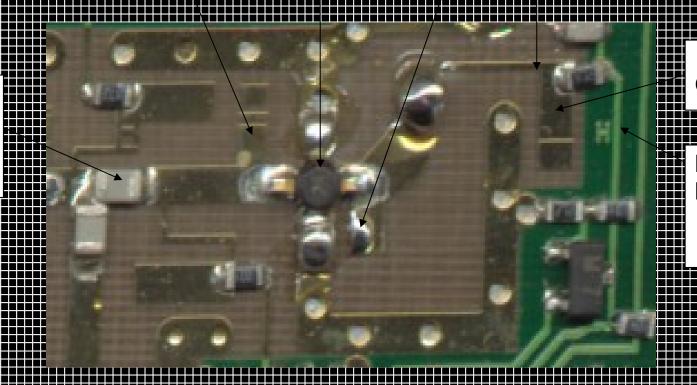


First stage Input LNA

Input match

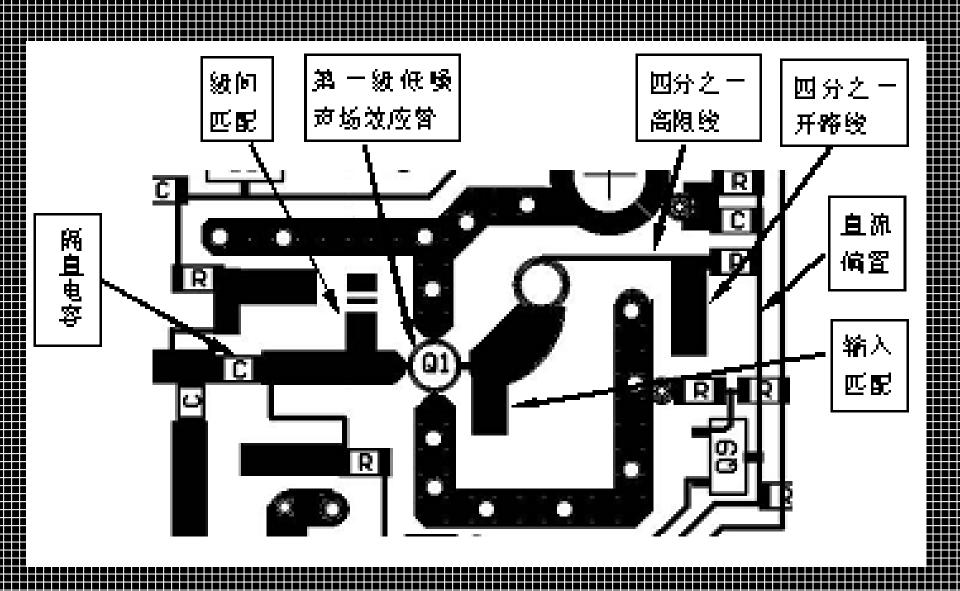
Quarter High impedance line

DC Blocking



Quarter Open-line

DC Bias



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DC bias

Quarter Open line

Quarter

High impedance line

x 1360061 111 H 10575111

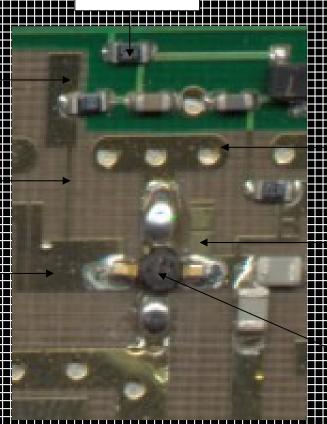
177371e3517721563277e7E

13 22 323 13 22 15c2 13 24 1 1 E 15c2

Output match

116367111171368313621111111

Terenear : :



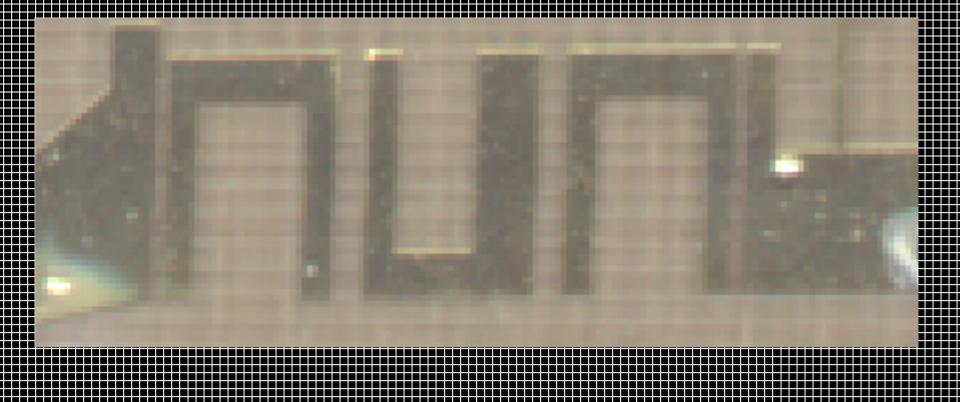
Source Ground

> Input match

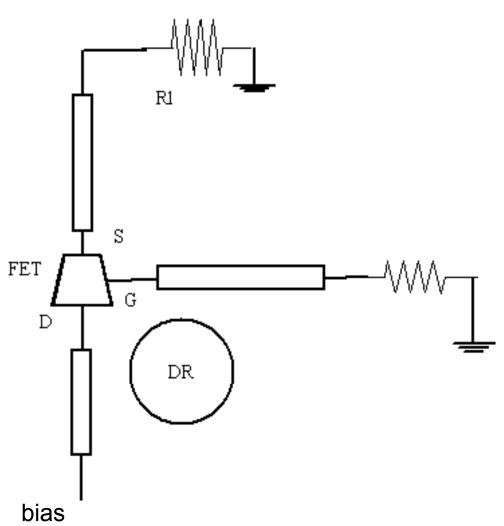
Second Transistor

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Source

Microstrip coupled line

Quarter Open line

Drain

DC supply

Quarter Open line

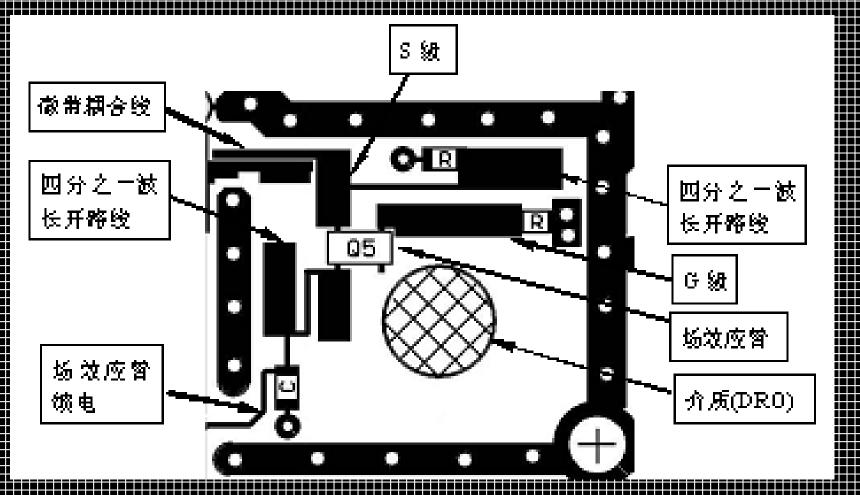
Gate

FET

DRO

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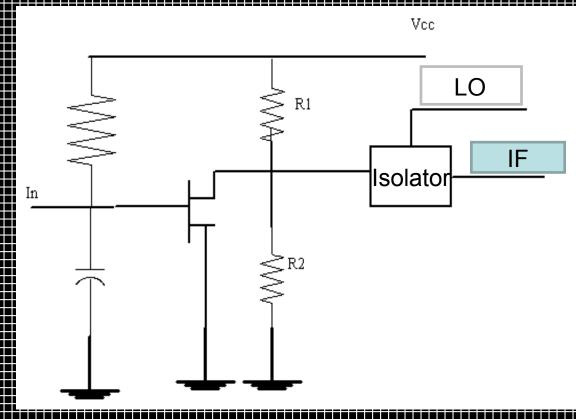
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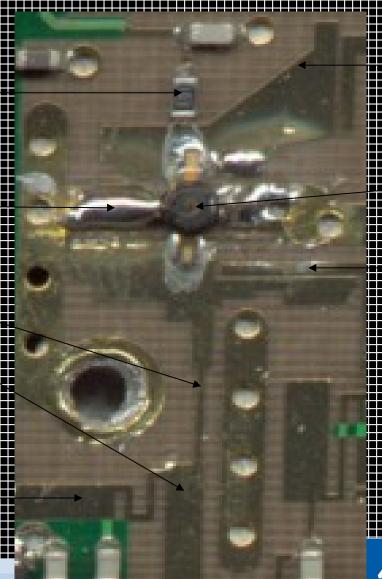
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Gate Bias

Source ground

Quarter microstrip for LO, Isolated LO leakage

IF output



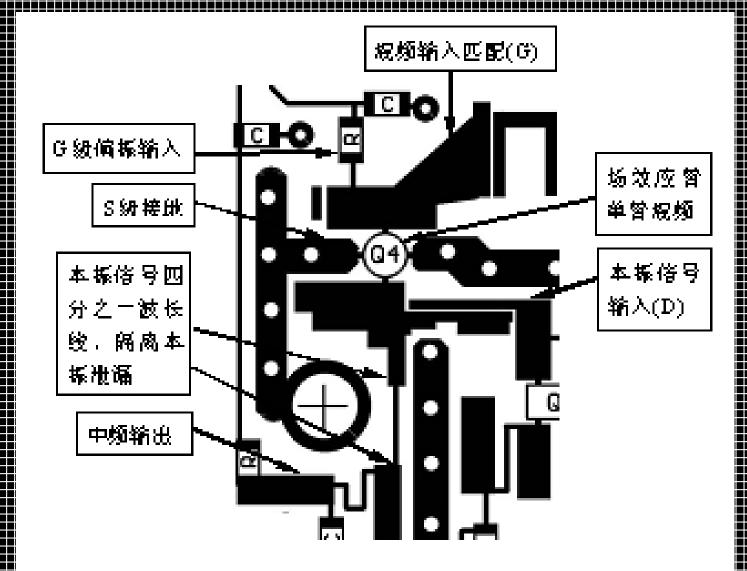
Input match (G)

FET Mixer

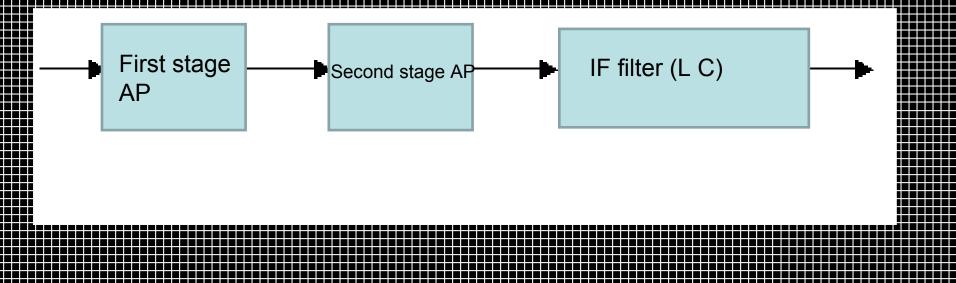
LO (D)

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printed inductance and distributed capacitor

Transistor

DC bias

IF

input

IF

output

Dividing Capacitor

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