



A novel design and optimization of Si based high performance double absorber heterojunction solar cell

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ABSTRACT

Researchers are currently focusing on Silicon (Si)-based solar cells due to their outstanding semiconductor properties. This study aims to enhance the performance of a new Si based solar cell structure of Cu/FTO/CdS/Si/FeSi₂/Au and investigate how the inclusion of FeSi₂ as a second absorber and CdS buffer layers affects key performance metrics such as V_{OC} , J_{SC} , FF, and PCE using SCAPS-1D simulation. Various factors including thickness, carrier concentration, defect density, temperature, and electrode design were analyzed in detail to improve performance. The PCE, V_{OC} , J_{SC} , and FF have shown 25.2 %, 0.718 V, 43.3 mA/cm², and 80.8 %, respectively with reference structure's (Cu/FTO/CdS/Si/Au). The PCE, V_{OC} , J_{SC} , and FF have improved to 27.73 %, 0.74 V, 45.55 mA/cm², and 79.94 %, respectively by adding FeSi₂ layer as a bottom absorber with proposed structures (Cu/FTO/CdS/Si/FeSi₂/Au). This research has the potential to offer insights and strategies for the development of cost-effective Si-based thin-film solar cells in a near future.

1. Introduction

The development of environmentally friendly and renewable energy sources is a primary goal for scientists and researchers worldwide in an effort to lessen the negative impacts of CO₂ emissions caused by the burning of fossil fuels. Photovoltaic (PV) cells are among the renewable energy sources that are essential for fulfilling the growing demand for energy and advancing green energy [12]. The unprecedented mass manufacture of silicon-based solar modules has propelled photovoltaic energy to the forefront as a low-cost, environmentally friendly alternative to traditional electricity generator. At present, silicon dominates the photovoltaic market with a substantial majority share of 90 %. This market is comprised of three unique kinds of silicon: monocrystalline silicon (mono-Si), polycrystalline silicon (poly-Si), and amorphous silicon (a-Si). First generation photovoltaic (PV) technology incorporates both monocrystalline and polycrystalline silicon in its development. Some research has been done in the last few years on monocrystalline silicon solar cells with a thickness of roughly 30 μ m using a variety of

fabrication techniques in an effort to boost the efficiency of the single-junction crystalline silicon solar cell with a single bandgap. The effect of varying the thickness of crystalline silicon (c-Si) solar cells from 10 to 80 μ m on achieving efficiencies of 15 % to 19 % was investigated by Bozzola et al. [3]. In order to increase the solar cell's optical absorption, M. Hilali et al. [4] used an exfoliation approach to create a c-Si solar cell with a thickness of 25 μ m. Theoretical optimization involving the use of a second back reflector layer for a 30 μ m thick c-Si solar cell can increase its efficiency by approximately 19.28 % [5].

Amorphous silicon (a-Si:H) thin-film solar cells in particular show promise for cost reduction because of their low material consumption, relatively low production heat requirements, and low temperature coefficient of solar cell performance [6]. However, several third-generation solar cells have been observed to be less efficient and less reliable than commercially available stabilised a-Si:H solar cells [7–11]. At present, the best that a-Si:H solar cells can do in terms of conversion efficiency is 10.2 % [12]. This is still well below the theoretical maximum.

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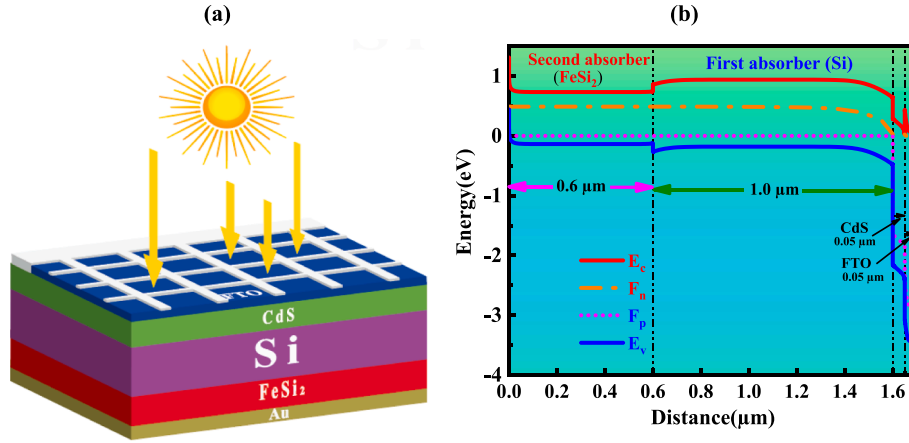


Fig. 1. The Si based proposed new double absorber solar cell (a) device Structure, and (b) energy band diagram.

Silicon clathrates are metastable phases of silicon characterised by their low density. These phases are composed of cage-like frameworks of silicon-atoms, referred to as hosts, which encapsulate atomic or molecule species known as guests [13]. Ongoing research is being conducted in both theoretical and experimental domains pertaining to this particular category of materials. A significant portion of the research efforts have been focused on examining the thermoelectric [14], semi-conducting [15,16] and superconducting properties [17–19]. Additionally, optical properties were studied, and they were shown to be promising as photonic materials [20–23]. Several groups [24–27] have recently brought up the possibility of using silicon-clathrates into photovoltaics. The current stage of the fabrication process is in its early phases and is currently being developed with very low efficiency.

To get beyond silicon's shortcomings (its weak absorption coefficient coupled to the indirect bandgap leading to conversion-efficiency constraint), researchers are looking into alternative semiconductor materials. Perovskites are currently the subject of extensive research because of their significant potential as photovoltaic materials, with remarkable conversion efficiency [28]. This is particularly evident in the development of perovskite/tandem solar cells. Despite exhibiting promising features, new semiconductors continue to require expensive, scarce, and occasionally poisonous materials such as cadmium and arsenide.

To improve the low performance, this research utilizes a dual absorber layer comprised of Si and FeSi₂ in the FTO/CdS/Si/FeSi₂ structure. Research finding [29] highlight that the utilization of two absorber layers in a solar cell results in improved efficiency due to its capacity to absorb solar photons over an extended spectral range. The absorber layer FeSi₂ exhibits a direct bandgap energy of 0.87 eV [30] and a significant α value surpassing 10^5 cm^{-1} when exposed to a photon energy of 1 eV [31–33]. Because iron (Fe) and silicon (Si) are abundant in the earth's crust, FeSi₂ is an affordable component for solar PV cells' active absorber materials. On the other hand, the FeSi₂-based solar cell's competitiveness would be demonstrated by comparing its efficiency to other new technologies, such as perovskite or multi-junction cells, or to conventional structures like single-junction silicon cells[34]. By removing photogenerated electrons from the absorber layer, electron-hole recombination at interfaces is inhibited and hole transmission to the electrode is stopped by ETL. The energy levels of the ETL material must be compatible with the absorber material to harvest electrons and prevent holes. Most commonly, n-type semiconductor materials including ZnS, ZnO, SnO₂, CdS, TiO₂, WO₃, WS₂, PCBM and TMD are considered ETL [35]. Of these, CdS shows great promise as an ETL layer material. Using a CdS electron transport layer, which has high electron mobility, wide bandgap, and low photocatalytic activity, improves SC stability [36].

This study focuses on the examination and improvement of various

performance parameters especially V_{OC} , J_{SC} , FF, and PCE with the aim of enhancing the efficiency of silicon based double absorber solar cells. To enhance the efficiency of the SC, several factors such as layer thickness, doping concentration, defect concentration, and interface defect density are examined and adjusted to analyse the output parameters. In addition, the suggested SC's output characteristics and overall adjusted PCE are studied across a range of operating temperatures, series and shunt resistances.

2. Modelling and simulation parameters

The present investigation encompassed the implementation of a computational model on a silicon-based photovoltaic device employing the Solar Capacitance Simulator (SCAPS-1D) software [37]. As a promising tool for building and analysing polycrystalline thin film solar cells (TFSC), the SCAPS program has garnered a lot of attention since its development by the Department of ELIS at the University of Ghent in Belgium. The purpose of the SCAPS-1D models is to simulate light absorption and the formation of electron-hole pairs in the solar cell's active layer. Characteristics such as carrier mobility, carrier lifetime, recombination rates, and bandgap are essential for correctly describing the behavior of the semiconductor material[38]. However, boundary conditions, which include things like temperature, specify the solar cell's operating environment. Changing the doping concentration of the semiconductor material can alter the device's conductivity and recombination rates [39–42]. Additionally, changes in the thickness of various layers-such as the contact, buffer, and absorber layers-have an impact on series resistance, charge carrier collection, and light absorption[43].The fundamental semiconductor equations (equations (1)–(3)) are employed in the SCAPS-1D software to describe the system. In SCAPS-1D, the solution of three coupled and nonlinear differential equations is obtained simultaneously [44–48].

$$\frac{\partial^2 \psi}{\partial x^2} = -\frac{q}{\epsilon} \left(p - n + N_D^+ - N_A^- + \frac{\rho_{def}}{q} \right) \quad (1)$$

$$\frac{\partial J_n}{\partial x} - U_n + G = \frac{\partial n}{\partial t} \quad (2)$$

$$\frac{\partial J_p}{\partial x} - U_p + G = \frac{\partial p}{\partial t} \quad (3)$$

The equation denoted as (1) is commonly referred to as the Poisson equation. It is utilized to depict electrostatic phenomena. In this context, ψ represents the electrostatic potential, while n and p correspond to the densities of free electrons and holes, respectively. Additionally, N_D^+ and N_A^- denote the concentrations of ionized donors and acceptors, respectively, while ρ_{def} corresponds to the density of deep defect centers

Table 1
Layer properties of the simulation. [55–61].

Parameters (unit)	FTO	CdS	Si	FeSi ₂
Thickness(nm)	50	50	1000	600
Bandgap(eV)	3.5	2.4	1.120	0.87
Electron affinity(eV)	4	4.4	4.05	4.16
Dielectric permittivity	9.00	9	11.9	22.6
CB effective DOS(cm ⁻³)	1×10^{19}	1.8×10^{19}	2.8×10^{19}	5.6×10^{19}
VB effective DOS(cm ⁻³)	1×10^{18}	2.4×10^{18}	1.04×10^{19}	2.04×10^{19}
Electron mobility (cm ² V ⁻¹ s ⁻¹)	2.0×10^1	100	1500	100
Hole mobility (cm ² V ⁻¹ s ⁻¹)	1.0×10^1	25	450	25
Donor density N _D (cm ⁻³)	1×10^{18}	1×10^{15}	0	0
Acceptor density N _A (cm ⁻³)	0	0	1×10^{16}	1×10^{17}
Defect type	Acceptor	Acceptor	Donor	Donor
Bulk defect density, N _t (cm ⁻³)	10^{14}	10^{14}	10^{12}	10^{12}
Electron capture cross-section, σ_e (cm ²)	10^{-15}	10^{-15}	10^{-15}	10^{-15}
Hole capture cross-section, σ_p (cm ²)	10^{-15}	10^{-15}	10^{-17}	10^{-15}
Defect position above Ev(eV)	0.6	0.6	0.6	0.6

Table 2
Interface parameters used in the BSF/absorber and absorber/ETL interface.

Parameters (unit)	FeSi ₂ /Si	Si/CdS
Defect type	Neutral	Neutral
Electron capture cross-section, σ_e (cm ²)	10^{-19}	10^{-19}
Hole capture cross-section, σ_p (cm ²)	10^{-19}	10^{-19}
Defect position above the highest Ev(eV)	0.6	0.6
Interface Defect density(cm ⁻²)	10^{12}	10^{12}

[49–51]. Equations (2) and (3) represent the continuity equations for electrons and holes, respectively. These equations describe the state of dynamic equilibrium within a semiconductor. The variable G denotes the generation rate, while U_n and U_p represent the recombination rates

for electrons and holes, respectively. The J_n and J_p are used to denote the current densities associated with electrons and holes, respectively [52–54].

The schematic representation of the device, referred to as FTO/CdS/Si/FeSi₂ SC, is illustrated in Fig. 1(a). This design incorporates a p-type material, FeSi₂ as the second absorber and employs Si as the first absorber layer. Moreover, the n-type substance CdS serves as the electron transport layer (ETL). Furthermore, the contact material chosen for this study is fluorine doped tin oxide (FTO), whereas the anode material is considered gold (Au). Fig. 1(b) illustrates the energy level diagram of the materials employed in the device architecture. The parameters for the device and the material are broken down and summarized in Tables 1 and 2, respectively.

3. Results and discussion

3.1. Influence of absorber layer thickness and doping density on PV parameter

Fig. 2(a) depicts how the absorber layer's thickness affects the efficiency, fill factor, open circuit voltage and short circuit current density. As the absorber layer thickness increased, a corresponding rise in all solar cell output parameters was observed. At a thickness of 0.3 μ m, V_{oc} is found to be 0.701 V and 0.726 V single and double absorber, and it rises to 0.764 V and 0.784 V at 3.0 μ m. It was also shown that J_{sc} grew linearly with absorber thickness up to 1.5 μ m, after which it essentially plateaued [62].

The observed outcome can be attributed to the significant absorption of incoming photons within the intrinsic layer, leading to an increase in the quantity of photo-generated carriers [10]. Similarly, both FF and efficiency grew with increased thickness. The obtained FF for thicknesses of 0.3 μ m and 3.0 μ m are 78.36 % and 81.89 %, respectively [63].

The power conversion efficiency is determined at 25.79 % and 28.1 % at 0.3 μ m and 3.0 μ m thicknesses, respectively. Photovoltaic saturation is seen in Fig. 3 for thicknesses greater than 1.0 μ m. For further exploration, the optimal thickness of the Si absorber layer is 1.0 μ m.

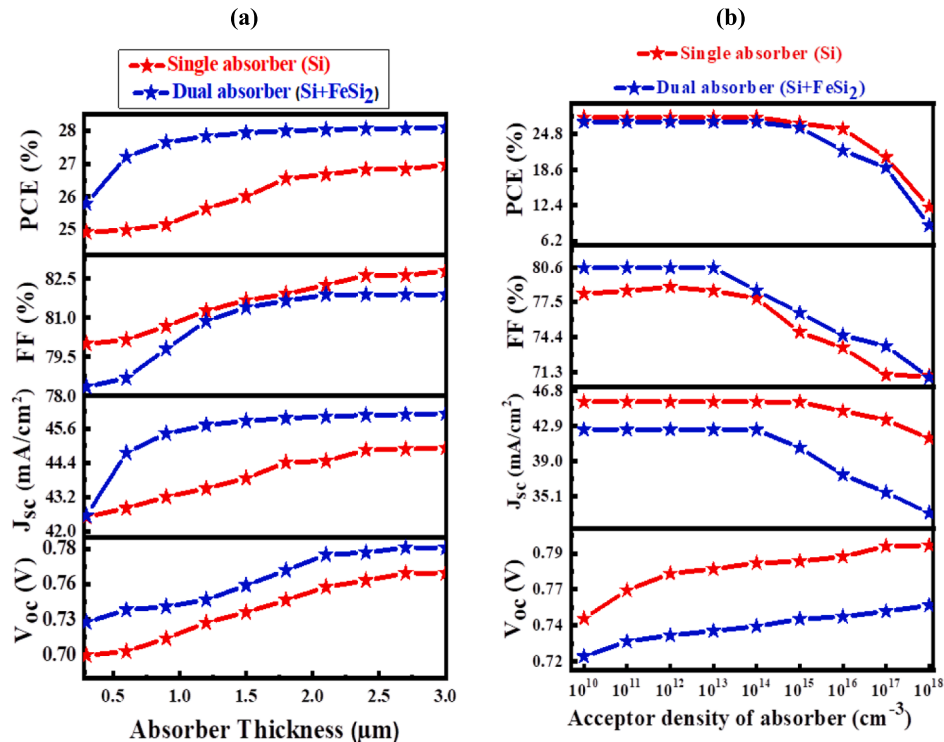


Fig. 2. Impact of variation in (a) absorber layer thickness, and (b) doping density of absorber on PV parameters of V_{oc} , J_{sc} , FF, and PCE.

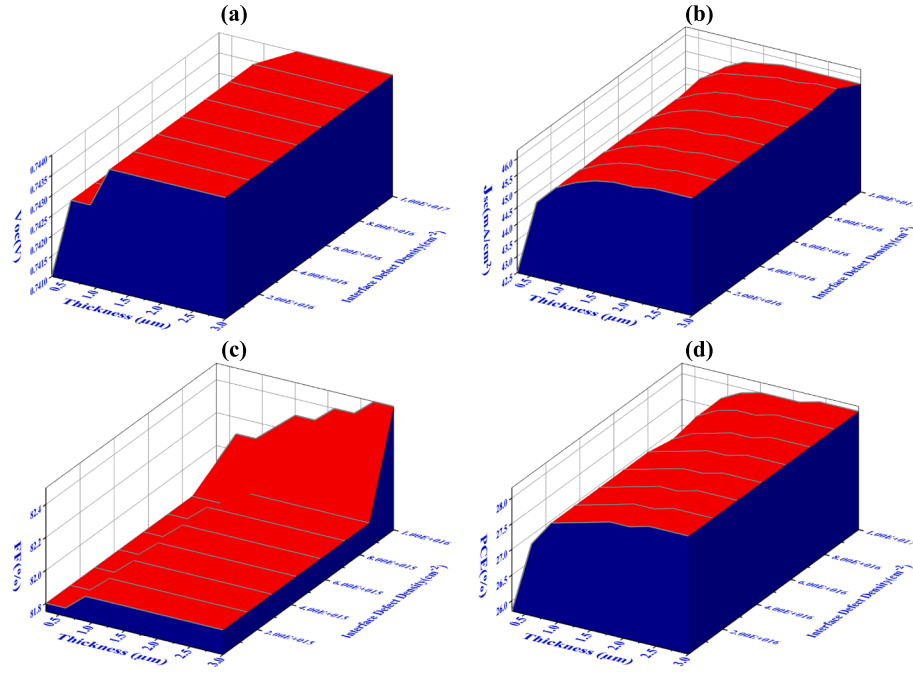


Fig. 3. Impact of thickness and absorber defect density on the photovoltaic performance (a) V_{OC} , (b) J_{SC} , (c) FF, (d) PCE-based on Si absorber solar cell.

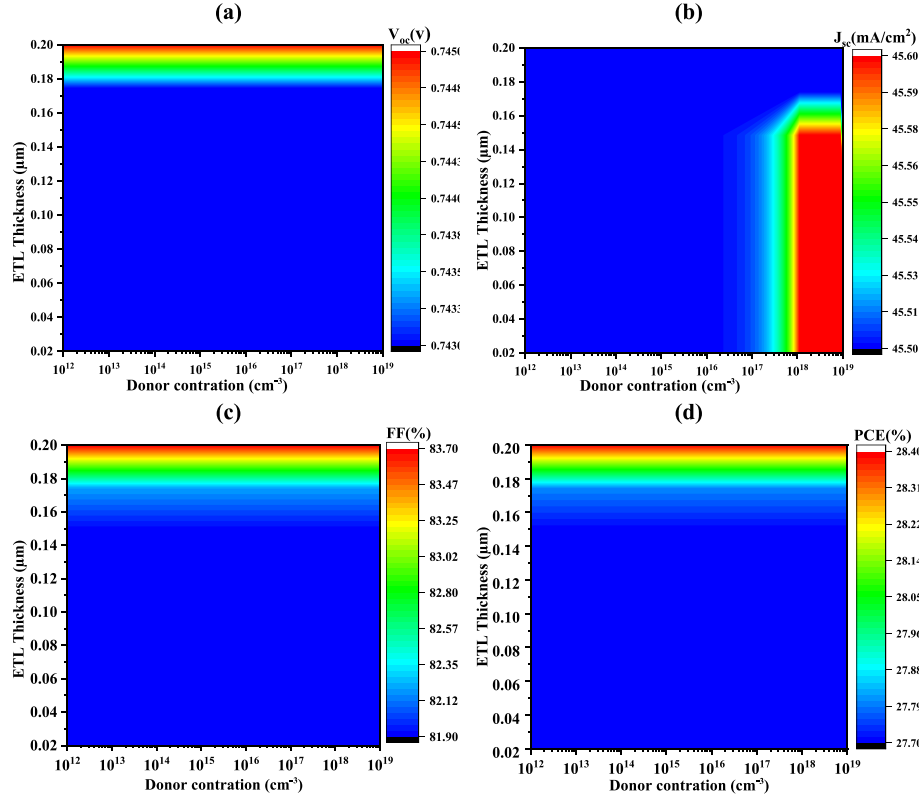


Fig. 4. Impact of thickness and doping concentration of CdS ETL on the photovoltaic performance (a) V_{OC} , (b) J_{SC} , (c) FF, and (d) PCE of Si based solar cell.

Fig. 2(b) depicts how the absorber layer's doping concentration affects the solar cell's parameter. Absorber doping density has a nearly insignificant impact on V_{OC} . Both the J_{SC} and FF remain nearly constant up to a doping density of 10^{14} cm^{-3} , beyond which they begin to drop [62,64]. For doping densities up to around 10^{15} cm^{-3} , efficiency is essentially constant; after that, it drops down.

Fig. 3 displays PV characteristics for $0.3 \mu\text{m}$ to $3 \mu\text{m}$ absorber

thickness and 10^{10} cm^{-3} to 10^{17} cm^{-3} absorber defect density to identify the best absorber thickness and defect density. Both V_{OC} and FF increase absorber thickness up to $1 \mu\text{m}$, where they saturate, but they are largely insensitive to changes in defect density [65]. The increase in absorber layer thickness from $0.3 \mu\text{m}$ to $3.0 \mu\text{m}$ is associated with a corresponding improvement in efficiency from 25.8 % to 28.1 %.

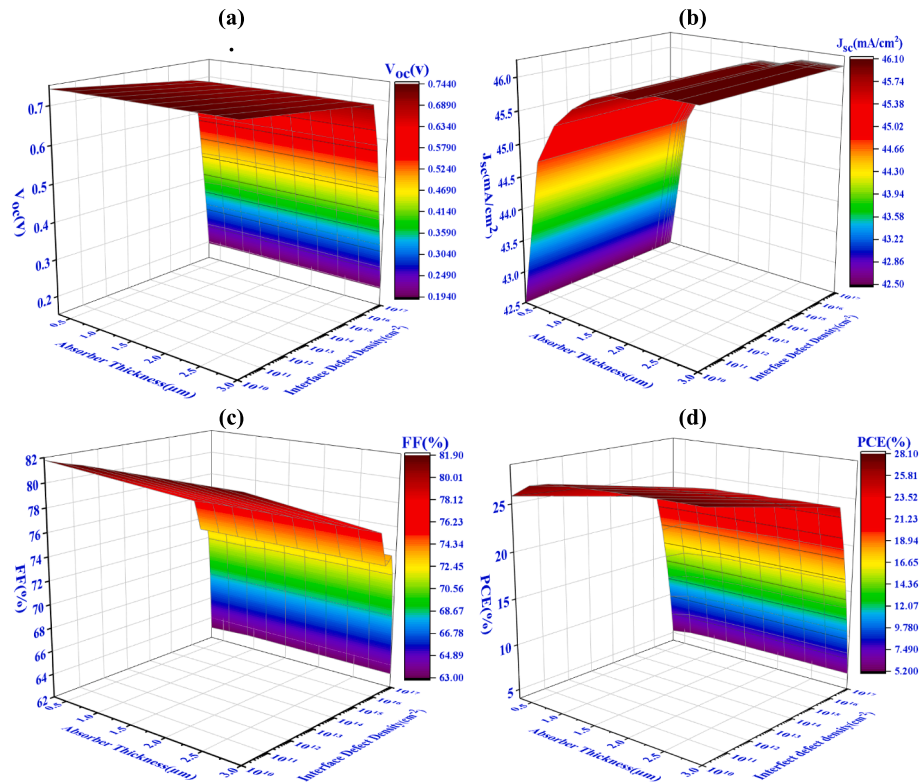


Fig. 5. Effects of absorber thickness and interface defect density (Si/CdS) variations on photovoltaic performance parameters (a) V_{oc} (b) J_{sc} (c) FF and (d) PCE.

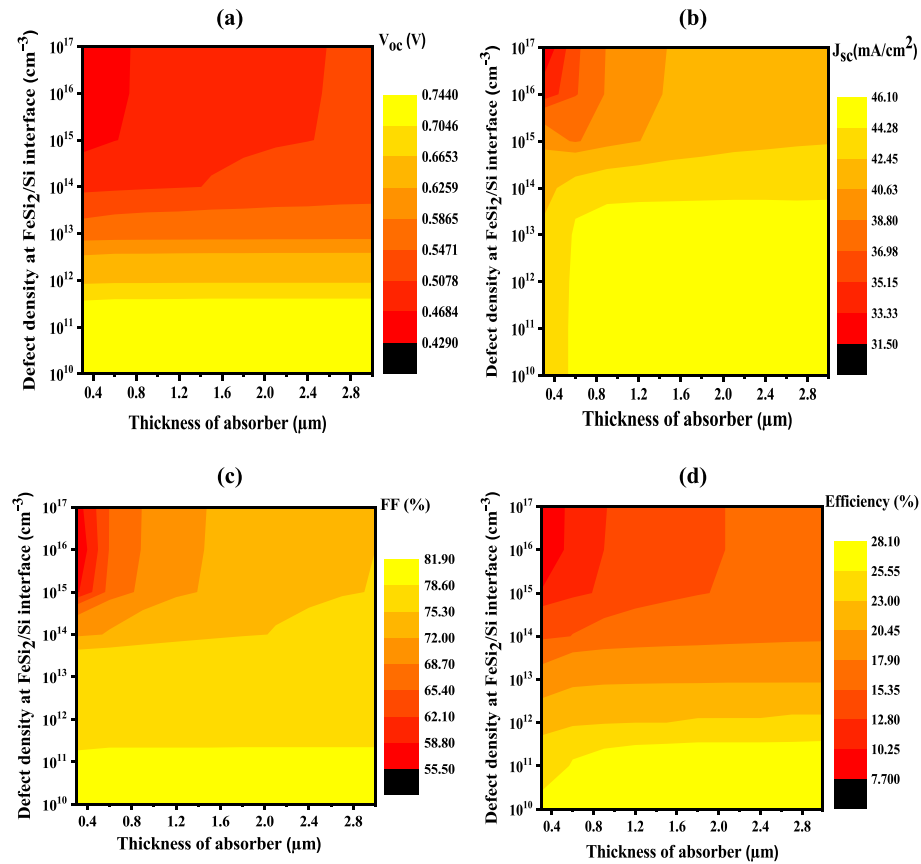


Fig. 6. Effects of absorber thickness and interface defect density (FeSi₂/Si) variations on photovoltaic performance parameters (a) V_{oc} (b) J_{sc} (c) FF and (d) PCE.

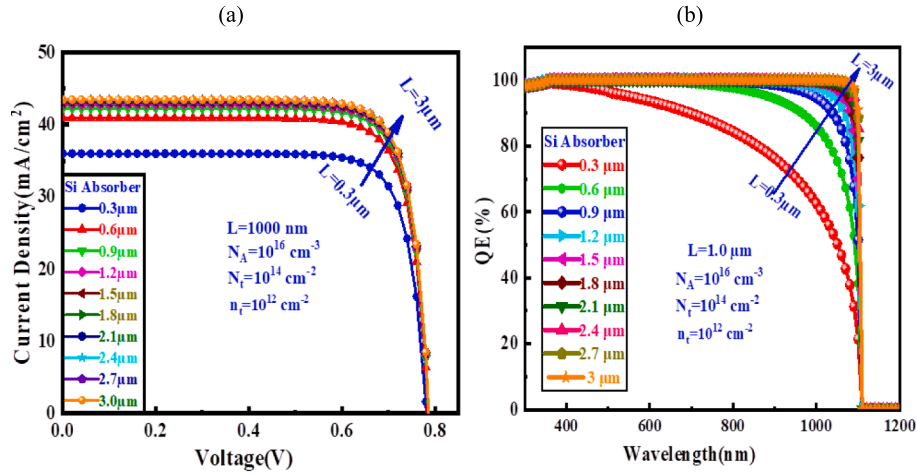


Fig. 7. Impact of the absorber layer thickness of Si solar cell (a) J-V, and (b) QE for various thicknesses.

3.2. Influence of ETL thickness and doping density on PV parameter

In Fig. 4, we observe the effects of changing the ETL thickness from 0.02 μm to 0.2 μm and the carrier concentration from 10^{12} cm⁻³ to 10^{19} cm⁻³ on the photovoltaic (PV) properties. The goal of this study is to establish the optimum thickness for the ETL thickness and carrier concentration. Up to 0.16 μm, with a given doping density, the V_{OC} , FF and PCE are nearly constant [59,66], before gradually increasing [67]. When the carrier density is greater than 10^{17} cm⁻³ and the thickness is less than 0.18 μm, the J_{SC} increases. However, the J_{SC} does not change outside of this range. Increased buffer layer doping concentration, generates a strong electric field that efficiently draws electrons and opposes minority carriers away from the ETL/absorber layer interface,

thereby decreasing interface recombination [68]. The thickness of 0.05 μm is chosen as optimal in light of all the simulation findings and the cost of production. In this study, a donor density of 1×10^{15} cm⁻³ for ETL was found to be optimal.

3.3. Influence of absorber thickness and defect density at Si/CdS interface on PV parameter

The performance of the suggested solar cell is shown to vary with absorber thickness and defect density at the Si/CdS interface, as shown in Fig. 5. As the interface defect density increases, the V , FF and efficiency all fall for a given absorber thickness [65,67]. The current density, J_{SC} is relatively insensitive to the interface defect density. Fig. 5

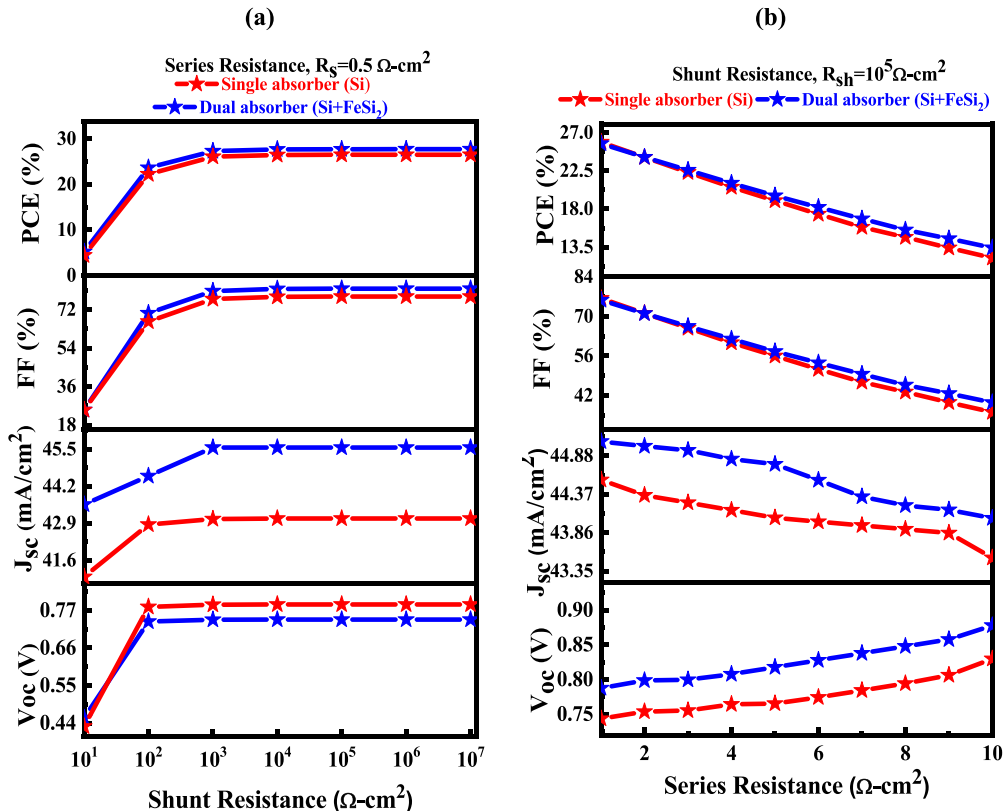


Fig. 8. The impact of varying (a) R_{sh} and (b) R_s on the characteristics of the proposed Si based solar cell.

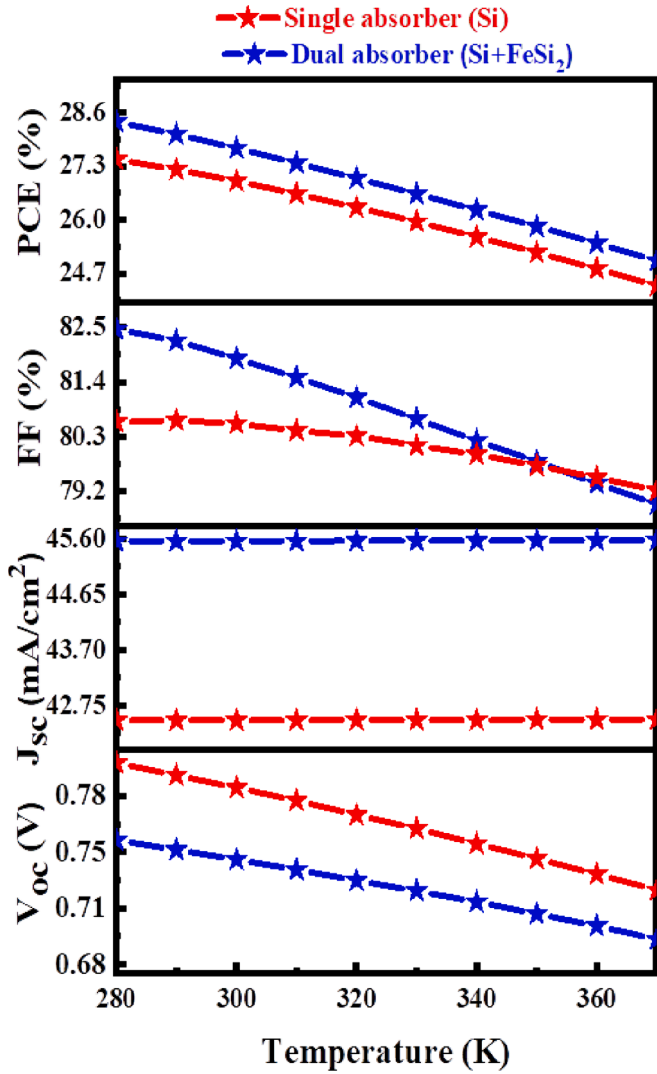


Fig. 9. The temperature effect on the proposed Si based parameters of the cell of the PV.

demonstrates that at a thickness of 1 μm , the V_{OC} , FF and PCE all significantly decrease from 0.744 V to 0.197 V, 81.9 % to 63.4 % and 27.8 % to 5.7 % respectively, as the defect density grows from $1 \times 10^{10} \text{ cm}^{-3}$ to $1 \times 10^{17} \text{ cm}^{-3}$. The decrease in efficiency can be attributed to the increase in recombination centers at the interface Si/Cds [69]. Additionally, the presence of electron traps contributes to this phenomenon. Furthermore, optical absorption also influences the observed decline in efficiency. At a certain defect density, the J_{SC} is found to grow proportionally with the thickness of the absorber material.

3.4. Influence of absorber thickness and defect density at FeSi_2/Si interface on PV parameter

Fig. 6 shows how the performance of the proposed solar cell changes depending on the absorber thickness and the defect density at the FeSi_2/Si interface. Photovoltaic characteristics decrease for a given absorber thickness as the interface defect density rises. At an absorber thickness of 1 μm , Fig. 6 shows that as the defect density grows from $1 \times 10^{10} \text{ cm}^{-3}$ to $1 \times 10^{17} \text{ cm}^{-3}$, the V_{OC} , J_{SC} , FF and PCE all substantially fall from 0.74 V to 0.43 V, 45.7 mA/cm^2 to 40 mA/cm^2 , 81.9 % to 72.1 % and 27.8 % to 13.7 %, respectively [70]. The presence of a high concentration of defects at the FeSi_2/Si interface results in the generation of a significant number of hole trap centers, leading to a decrease in the

efficiency of the cell [69,71–73]. Taking into account the practical fabrication conditions and stable defect density range at the interface, a defect density of $1 \times 10^{12} \text{ cm}^{-3}$ was selected for the suggested SC.

3.5. Influence of thickness and doping density of absorber on current density and quantum efficiency

Fig. 7(a) and (c) show the J-V characteristics of optimized solar cell structure with the variation of thickness and carrier concentration of absorber respectively. Both the absorber's thickness and the concentration of its carriers have minimal effects on the current density. Fig. 7 (b) and (d) show how the QE of the SC changes from 300 nm to 1200 nm, depending on the absorber layer thickness and carrier concentration. Over the shorter wavelength range, the QE approaches 100 % before gradually decreasing. Reduced QE is observed at 500 nm for a thickness of 0.3 μm , and at 1050 nm for a thickness of 3 μm as shown in Fig. 7(b) [61,67]. As may be seen in Fig. 7(d), the sandwiching of the BSF layer improves the absorption of incident solar spectra, most noticeably between 900 nm and 1100 nm. The enhancement of absorption and the suppression of surface recombination at the FeSi_2/Si interface are responsible for the enhancement of the SC's J_{SC} and PCE, which is why the QE is observed to extend at longer wavelengths.

3.6. Influence of series and shunt resistance on PV parameter

PV parameter fluctuation at different series and shunt resistances is shown in Fig. 8. The effect of R_{sh} ($10^1 \Omega\text{-cm}^2$ – $10^7 \Omega\text{-cm}^2$) on PV characteristics was examined at a fixed R_s of $0.5 \Omega\text{-cm}^2$ as shown in Fig. 8(a). Up to $10^3 \Omega\text{-cm}^2$, PV parameters vary with R_{sh} , but afterwards they reach a plateau. Previous reports [74] corroborate this effect of R_{sh} . Fig. 8(b) illustrates R_s increases from $1 \Omega\text{-cm}^2$ – $10 \Omega\text{-cm}^2$ at a fixed R_{sh} of $10^5 \Omega\text{-cm}^2$. When the series resistance increases from $1 \Omega\text{-cm}^2$ – $10 \Omega\text{-cm}^2$, we observed that the J_{SC} , FF and PCE drops from 44.56 mA/cm^2 to 43.53 mA/cm^2 , 76.38 % to 36.11 % and 25.88 % to 12.23 % respectively, while V_{OC} increases from 0.74 V to 0.83 V. Photo-generated carriers recombined severely after generation at high R_s , affecting solar cell performance [75,76].

3.7. Influence of temperature on PV parameter of proposed Si based double absorber based solar cell

Fig. 9 illustrates the fluctuations in photovoltaic (PV) characteristics throughout the temperature range of 280 K–370 K. The fill factor (FF) exhibited a decline from 82.46 % to 78.93 %, while the open circuit voltage (V_{OC}) decreased from 0.75 V to 0.69 V. As a result the PCE also decreased from 28.37 % to 25.01 % when the working temperature was increased from 280 K to 370 K [61,77]. The properties of solar cells are significantly influenced by temperature due to the temperature dependent nature of factors such as V_{OC} , J_{SC} , FF and PCE. The temperature-dependent drop in V_{OC} is proportional to the leakage current, J_0 which is a measure of the minority carriers across the p-n junction that are thermally generated while the solar cell is in its reverse bias state. An increase in temperature changes the diffusion length and raises stress and strain in the absorber layer, causing deformation and disorder in the layer [78]. These factors ultimately lower the device's overall performance by affecting how well solar cell use sunlight. However, the J_{SC} remains unaffected across the whole temperature range [70]. The proposed solar cell structure demonstrates a commendable thermal response, as evidence by a 3.36 % decrease in PCE within the temperature range of 280 K to 370 K. Temperature increases can cause a drop in photo-conversion efficiency [79] by lowering the band gap of Si and increasing the collision between vibration atoms and photo-generated carriers.

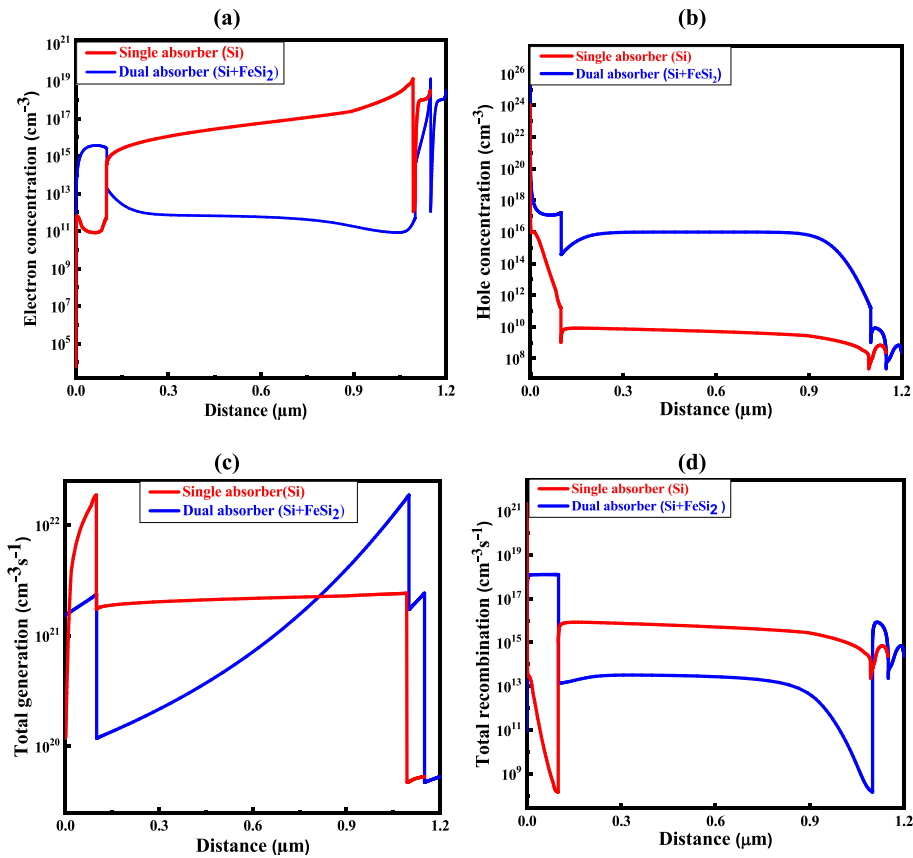


Fig. 10. Influence of (a) electron, (b) hole carrier concentration, (c) total generation, and (d) total recombination concerning the absorber layer thicknesses.

Table 3

A comparative study of the performance of Si-based solar cells.

Research type/ parameters	Device Structure	J_{sc} (mA/ cm ²)	V_{oc} (V)	FF (%)	PCE (%)	Ref.
Experimental	p-(a-SiO _x)/ buffer layer/i-(a-Si: H)/n-(a-Si:H)/ ITO/Al	17.2	0.87	70	10.58	[65]
	p-Si/n-Si/ n ⁺⁺ Si	37.7	0.62	78	18.9	[87]
Simulation	p-(a-SiO _x)/ buffer layer/i-(a-Si: H)/n-(a-Si:H)/ ITO/Al	18.95	0.973	68.86	12.71	[65]
	n-Si/p-Si/ p ⁺⁺ Si	19.34	0.699	83.48	11.93	[88]
	CZTS/Si based solar cell	19.38	1.4	83.5	22.9	[88]
	CIGS/Si based solar cell	15.8	1.25	81.5	19.8	[89]
	c-Si/SiGe based solar cell	30.25	1.018	80.35	24.74	[55]
	p-Si/n-Si/ n ⁺⁺ Si	16.09	0.66	81.3	15.56	[90]
	a-Si/FeSi ₂ /Si	13.31	1.982	0.762	19.80	[91]
	Perovskite/ Silicon	16.01	1.76	86.7	24.4	[90]
	Tandem cell Cu/FTO/CdS/ Si/FeSi ₂ /Au	45.55	0.74	79.94	27.73	This work

3.8. Effect of the carrier concentration of the absorber layer on the G-R profile

The overall generation-recombination (G-R) profiles and carrier (electron and hole) concentration as a function of absorber layer thickness with single and double absorber are shown in Fig. 10. When a double absorber is present, it is evident that the electron concentration of the absorber is reduced compared to the single absorber. The absorber hole concentration, on the other hand, is increased in the presence of a double absorber relative to single absorber. The carrier generation and recombination profiles produced by a systematic investigation showed the potential of the SC with single and dual absorber when compared to other studies at a given and regulated carrier concentration and defect density [80–84].

Photo-generation refers to the phenomenon in which electrons acquire energy and transition from the valence band to the conduction band. Recombination, on the other hand, can be defined as the process in which electrons in the conduction band lose energy and return to the energy state of a hole in the valence band [85]. The rate of photo-generation plays a crucial role in the functioning of solar cells. The number of electrons created at each location in the device owing to photon absorption is represented by the generation rate. Carrier concentrations in the vicinity of a surface are low due to the high recombination rate. The total performance of a solar cell can be improved if the rate of carrier creation is larger than the rate of carrier recombination [86]. According to the findings of the simulation, the rate of photo-generation is greater than the rate of recombination.

Table 3 presents a comparative analysis of the progress made in the investigation of solar cell structures based on Si by various research groups. The prior analysis shows efficiency rising from 10.58 % to 24.74 %. Our study found improved V_{oc} , J_{sc} , FF and PCE compared to earlier research. This study offers new design principles for optimizing Si-based

solar cells with a FeSi₂ second absorber layer for 27.73 % efficiency. By coupling a second absorber layer (FeSi₂), the voltage, current and PCE can be greatly increased while the absorbers thickness is greatly decreased. Table 3 demonstrates that the TFSC proposed here, even with a thin 1.6 μm (Si + FeSi₂) absorber layer, may offer greater economic flexibility than the other Si-based design solar cells displayed in Table 3. The configuration also provides an opportunity to reduce the cost of the absorber material used in (Si + FeSi₂) based solar cells. As a result, the proposed solar cell has greater efficiency than its Si and FeSi₂ based solar cells.

4. Conclusions

This paper presents an analysis of the photovoltaic (PV) performance of new Si-based solar cells, conducted through numerical investigation using the SCAPS-1D solar simulator with a FeSi₂ layer serving as the bottom absorber. The study explores how various device characteristics influence the outputs of solar cells. Optimal parameters, including a Si absorption layer thickness of 1 μm and doping density of ($1 \times 10^{16} \text{ cm}^{-3}$), as well as a CdS ETL thickness of 0.05 μm and doping density of ($1 \times 10^{15} \text{ cm}^{-3}$), were determined to achieve a high PCE for the proposed structure. It was discovered that a defect concentration of $1 \times 10^{12} \text{ cm}^{-3}$ for FeSi₂/Si and Si/CdS was optimal at the contact interface. The newly proposed Si hetero-structure incorporating FeSi₂ as a bottom absorber demonstrated superior PV characteristics compared to the reference solar cell, with a PCE of 27.73 %, V_{OC} of 0.74 V, J_{SC} of 45.55 mA/cm^2 , and FF of 79.94 %. The inclusion of FeSi₂ as a second absorber in the suggested solar cell aims to enhance thermal stability for PV performance parameters at high temperatures. The Cu/FTO/CdS/Si/FeSi₂/Au structure shows potential for integration into solar cells to improve efficiency.

CRedit authorship contribution statement

Basra Sultana: . **Md. Ferdous Rahman**: Writing – review & editing, Writing – original draft, Visualization, Validation, Supervision, Software, Resources, Methodology, Investigation, Formal analysis, Data curation, Conceptualization. **Amaresh Chandra Roy**: Writing – review & editing, Writing – original draft, Visualization. **Md Masum Mia**: Writing – review & editing, Writing – original draft, Visualization. **Md. Al Ijazul Islam**: Writing – review & editing, Writing – original draft. **Ahmad Irfan**: Writing – review & editing, Writing – original draft, Visualization. **Aijaz Rasool Chaudhry**: Writing – review & editing, Writing – original draft, Visualization. **Md. Dulal Haque**: Writing – review & editing, Writing – original draft, Visualization.

Declaration of competing interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

Data availability

Data will be made available on request.

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