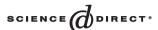
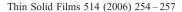


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# Effect of buffer layer on minority carrier lifetime and series resistance of bifacial heterojunction silicon solar cells analyzed by impedance spectroscopy

Germà Garcia-Belmonte <sup>a,\*</sup>, Jorge García-Cañadas <sup>a</sup>, Ivan Mora-Seró <sup>a</sup>, Juan Bisquert <sup>a</sup>, Cristóbal Voz <sup>b</sup>, Joaquim Puigdollers <sup>b</sup>, Ramon Alcubilla <sup>b</sup>

<sup>a</sup> Departament de Ciències Experimentals, Universitat Jaume I, E-12071 Castelló, Spain <sup>b</sup> Departament d'Enginyeria Electrònica, Universitat Politècnica de Catalunya, E-08034 Barcelona, Spain

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# Abstract

By combining information on solar cell layer structure and electrical response analyzed by impedance spectroscopy, relevant knowledge is obtained about photogenerated carriers recombination and extraction. The inclusion of a-Si:H buffer layers on the response of bifacial heterojunction silicon solar cells prepared by hot-wire chemical vapor deposition is studied. Impedance analysis indicates that the effect of the buffer layer is twofold: (a) effective minority carrier lifetime is improved by one order of magnitude, confirmed by alternative quasi-steady-state photoconductance, and (b) whole series resistance is increased. Both effects seem to compensate each other so as to get similar efficiency and fill factor.

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Keywords: Solar cell; Impedance spectroscopy; Buffer layer; Chemical vapor deposition; Carrier lifetime

# 1. Introduction

The static current–voltage characteristic of solar cells is routinely used to extract fundamental cell parameters and check device performance. It has been seldom recognized [1–4] that useful knowledge may be gathered by analyzing ac characteristics of solar cells in addition to dc curves. Impedance spectroscopy (IS) measurements taken over a broad frequency range (Hz–MHz) provide information on any system that is composed of a combination of interfacial and bulk processes, such as transport, recombination and interfacial states in solar cells [5]. The experimental method is straightforward but the interpretation of the results needs detailed models for assessing the mechanisms involved.

Simple ac equivalent circuits of solar cells should incorporate the capacitive effect corresponding to the excess minority

carriers ( $C_d$  usually denoted the diffusion capacitance) in parallel with the depletion layer capacitance  $C_i$  (see Fig. 1a) [6]. Resistive effects arise from the minority carrier recombination  $R_r$  [7] and shunt resistances  $R_t$ . An additional series resistance is needed to model the contact effect  $R_s$ . In the forward bias direction the diffusion capacitance increases due to the minority carrier accumulation in the absorber layer and excess the junction capacitance,  $\frac{C_d > C_i}{[8]}$ . The ac equivalent circuit in this situation is represented in Fig. 1b, assuming also  $R_t \gg R_r$ . This simple equivalent circuit behaves like a parallel RC subcircuit in series with  $R_s$ . It is expected that impedance contributions from layers in which transport is determined by majority carriers are assimilated into  $R_s$ . However, intrinsic buffer layers can contribute with additional subcircuits despite their thickness in the nanometer scale (Fig. 1c), in which  $C_b$  and  $R_{\rm b}$  account for the dielectric and resistive response of the buffer layer, respectively.

In this work we show that impedance spectroscopy provides detailed information on the factors determining the photovoltaic efficiency of different configurations of bifacial heterojunction

<sup>\*</sup> Corresponding author. Tel.: +34 964 728023; fax: +34 964 728066. E-mail address: garciag@uji.es (G. Garcia-Belmonte).

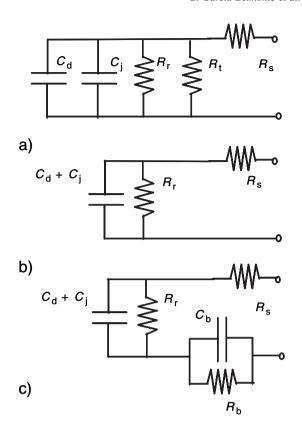


Fig. 1. (a) General solar cell ac equivalent circuit. (b) Simplification in case of  $R_1 \gg R_p$ . (c) Additional contributions from intrinsic buffer layers.

thin film silicon solar cells. These devices are an excellent alternative for the fabrication of high efficiency silicon solar cells in an entirely low temperature fabrication process [9–12]. However, knowledge about transport and recombination mechanisms taking part in this device is still incomplete. Then, IS measurements can offer a new and valuable insight into the interface between crystalline and amorphous silicon.

# 2. Sample preparation and experimental conditions

The heterojunction silicon solar cells studied in this work are obtained on p-type polished float zone silicon wafers (100) of thickness 400 µm following a method described elsewhere [13]. The inner structure of the studied solar cells is summarized in Table 1, with all the films deposited by hot-wire chemical vapor deposition technique [14]. In both cases, the solar cells are defined with an area of 1.4 cm² by sputtering 90-nm thick indium tin oxide (ITO) layers onto the emitter and back contact. The front grid and back contact are obtained by evaporating silver in high vacuum. Then, the main structural difference

Table 1 Heterostructures analyzed in this work

Cell name	Emitter		Back contact	
	n-doped	Intrinsic	Intrinsic	p-doped
A	μc-Si:H (50 nm)	_	_	μc-Si:H (50 nm)
В	a-Si:H (50 nm)	a-Si:H (5 nm)	a-Si:H (5 nm)	μc-Si:H (50 nm)

between Cells A and B is the inclusion of hydrogenated amorphous silicon (a-Si:H) buffer layers in the second case. Therefore, IS measurements are performed in order to study the effect of including a 5-nm thick buffer layer. As we will next show the buffer layer improves the effective minority carrier lifetime  $\tau_{\rm eff}$  by more than one order of magnitude. However, this intrinsic buffer layer increases the whole series resistance with an additional term  $R_{\rm b}$  (Fig. 1c). For the studied configurations, both effects seem to compensate each other so as to get similar cell parameters for efficiency and fill factor (FF).

The impedance measurements were carried out using an Autolab PGSTAT-30 equipped with a frequency analyzer module in the frequency range between I MHz and I Hz. Ac oscillating amplitude was as low as 10 mV (rms) in order to maintain the linearity of the response. Impedance spectra were recorded either in the dark under varying bias voltage or in open circuit conditions under varying illumination from 0.05 sun up to 1.5 sun. This last measuring procedure yields a more homogeneous distribution of excess minority carriers because dc current is not allowed.

### 3. Results and discussion

As observed in Fig. 2 the incorporation of buffer layers increases the value of the open-circuit potential ( $V_{\rm oc}$ ) from 546 mV (Cell A) up to 577 mV (Cell B). As known,  $V_{\rm oc}$  is related to the excess carrier density, what implies higher electron density in Cell B than in Cell A for 1 sun irradiance level. This is in fact an expected result because buffer layers inhibit to some extent the charge carrier recombination. On the other hand, the short-circuit current is slightly higher for Cell A (27.7 mA cm<sup>-2</sup>) than for Cell B (24.2 mA cm<sup>-2</sup>). However, this difference is probably due to a better quality of the ITO antireflecting coating in Cell A. In conclusion, both solar cells yield a conversion efficiency of around 9% with FF over 60%.

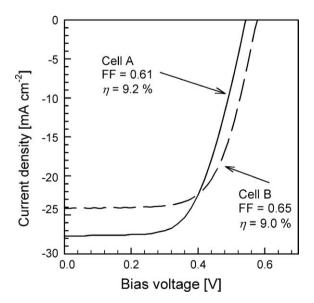


Fig. 2. Current–voltage characteristics of bifacial heterojunction solar cells measured under 1 sun-irradiance (1000 W m $^{-}$ 2) conditions. (solid line) Cell A and (dashed line) Cell B.

General capacitive response of both cells was firstly checked to verify that the diffusion capacitance is clearly distinguishable from the depletion capacitance in the forward bias direction. We measured the device capacitance at low frequencies (1 kHz) in dark conditions. By examining Fig. 3 one can readily observe that depletion-layer capacitance is visible at lower bias voltages. In this voltage region measured capacitance behaves as  $C^{-2} \propto V_{\rm bias}$ , as expected for a depletion layer capacitance (inset of Fig. 3). Flatband potential of each junction can be calculated from the linear fits and result for Cell A equal to 0.76 eV, and 0.96 eV for Cell B. This is in agreement with the higher open-circuit potential value reported for Cell B. The increase in the flatband potential observed for Cell B may be related to the potential drop associated to the insulating layer. For potentials more positive than 0.4 V, diffusion capacitance dominates exhibiting the standard exponential dependence with bias potential. At potentials ~0.6 V, the diffusion capacitance seems to saturate. This effect is commonly attributed to the series resistance potential drop, which has a dramatic influence as device dc current increases in the forward direction.

Impedance spectra measured under 1 sun irradiance are shown in Fig. 4. Spectrum corresponding to Cell A (no buffer layers) can be understood in terms of the equivalent circuit of Fig. 1b. In this case a contact series resistance of 1.58  $\Omega$  cm<sup>2</sup> is observed. The low-frequency part is dominated by the *RC* subcircuit that draws a semicircle in a Z'-Z'' complex plot. The recombination resistance results in  $R_r=0.24$   $\Omega$  cm<sup>2</sup>. The response time of such subcircuit  $\tau$  can be readily used to calculate the effective lifetime,  $\tau_{\rm eff}=R_rC_{\rm d}=\tau-R_rC_{\rm j}$ , provided that the depletion layer capacitance is known from measurements shown in Fig. 3. The frequency corresponding to the maximum reached by the imaginary part of the complex impedance signals the response time inverse, as indicated in Fig. 4. Alternatively,  $\tau$  can be also read from the peak of the Z''

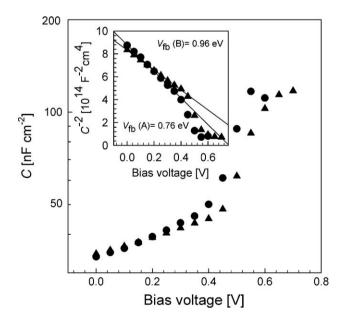


Fig. 3. Cell capacitance measured at 1 kHz as a function of the bias voltage in the dark. ( $\bullet$ ) Cell A and ( $\nabla$ ) Cell B. In the inset:  $1/C^2$  vs.  $V_{\rm bias}$  analysis showing the flatband potential calculated from the linear fit intercept.

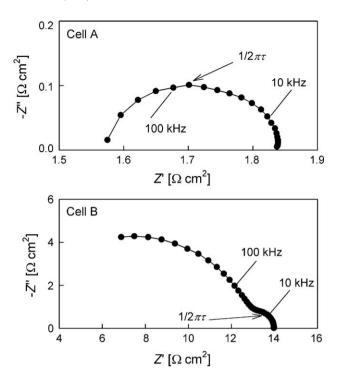


Fig. 4. Impedance spectra (Z'-Z'' complex plot) of bifacial heterojunction solar cells measured under 1 sun-irradiance (1000 W m<sup>-2</sup>) and open-circuit conditions. Characteristic frequency  $1/2\pi\tau$  is signaled with an arrow. Some other frequencies are also marked.

vs. f plot. By contrast, the impedance spectrum is rather different for cell B where, instead of a simple semicircle, two well-defined arcs are present. This suggests the equivalent circuit of Fig. 1c as more suitable to interpret the response. The elements corresponding to the arc at high frequencies,  $R_{\rm b}$  and  $C_{\rm b}$ , appear potential-independent. It is observed that  $R_{\rm b}$  decreases slightly with irradiance in the range of 10–13  $\Omega$  cm<sup>2</sup>. The origin of the small arc at low-frequencies can be attributed to excess charge carrier accumulation and recombination, as discussed for Cell A. Recombination resistance results much higher ( $R_{\rm r} \sim 2~\Omega~{\rm cm}^2$ ) compared to that measured for Cell A.

A simple method to compare effective lifetime values of both cells is therefore the determination of the response time, as indicated by an arrow in each spectra, under irradiation variation in open-circuit conditions. Results are presented in Fig. 5 for different illumination levels. As expected by effect of the buffer layer, Cell B exhibits longer  $\tau_{\rm eff}$  values in more than one order of magnitude. It should be stressed that these results are in good agreement with direct  $\tau_{\rm eff}$  measurements performed previously by the quasi-steady-state photoconductance (QSS-PC) technique [15], so as to confirm the validity of our approach.

As suggested previously, the additional arc appearing in Cell B impedance spectra at high frequencies must be related to the electrical response of the thinner buffer layer near the emitter. Other possibilities, like seeing the emitter layer itself as responsible for this additional subcircuit, can be safely discarded. Although emitter differs in structure between both cells, it is rather hard to expect a resistance contribution of about

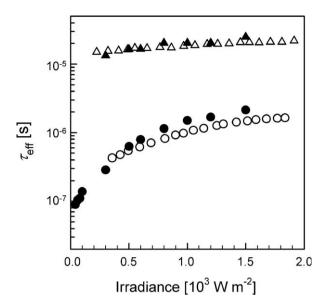


Fig. 5. Effective minority carrier lifetime calculated from impedance measurements (filled) compared with alternative QSS-PC results (empty) as a function of irradiance. (●) Cell A and (▼) Cell B.

 $12 \Omega \text{ cm}^2$  from doped layers only 50-nm thick. Furthermore, we can assure that the additional arc is due from the response of the intrinsic buffer layer by determining the relative permittivity resulting from the measured capacitance value  $C_{\rm h}$ =0.7  $\mu F$ cm<sup>-2</sup>, which is irradiance and voltage-independent. Relative permittivity gives  $\varepsilon \approx 4$  in good agreement with the value usually reported for a-Si:H [8]. The conductivity of the intrinsic buffer layer can be thereby calculated and results  $\sigma \approx 4.2 \times 10^{-8} \ \Omega^{-1} \ \text{cm}^{-1}$ . It is known that intrinsic amorphous silicon has a conductivity value of the order of  $10^{-9} \Omega^{-1} \text{ cm}^{-1}$ at room temperature in the dark [14]. Tacking into account that the buffer layer is only 5-nm thick and is placed between highly doped materials, it is expected a shift of the Fermi level. Moreover, impedance measurements were performed under illumination. The combination of these two effects suffices to explain the measured value for the intrinsic layer conductivity. A well-established order of magnitude for the amorphous silicon mobility is 1 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> [16]. This allows calculating an estimation of the doping level of the intrinsic layer, which results reasonably in the order of  $10^{11}$  cm<sup>-3</sup>.

Since the additional, high-frequency arc yields the main contribution to the cell B resistance at high illuminations, the overall series resistance is then increased as a consequence of the buffer layer inclusion. Then, effective lifetime and series resistance increments produced by the buffer layer contribute in opposite ways to the solar cell performance. In fact, both effects seem to compensate each other in the studied solar cells resulting in similar cell parameters for conversion efficiency and FF.

# 4. Conclusions

Impedance spectroscopy measurements taken over a broad frequency range are able to provide useful information on minority carrier recombination and the effect of buffer layers on carrier extraction in heterojunction silicon solar cells. The analysis is based on simple RC subcircuits resulting from different cell layers and electronic mechanisms. IS measurements confirm that, as expected, the buffer layer reduces minority carrier recombination. However, buffer layers have the drawback of increasing the series resistance. Finally,  $\tau_{\rm eff}$  values can be deduced from impedance spectra in complete devices, evidencing excellent agreement with QSS-PC measurements in device precursors.

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