

Macroscopic Study on Current Transport Path in Front-Side Contacts of Crystalline Silicon Solar Cells

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Double rectangular transmission line model and contact-end voltage measurement are used to study the variation in sheet resistance and current transfer length of the contact interface between the front-side electrode and the emitter of crystalline silicon solar cells during metallization. The current distribution and its relationship with the sheet resistances of the electrode, contact interface, and emitter are given at the macrolevel. The model shows that the current flows transversely for \approx 50-500 μm in the highly conductive interface with a sheet resistance in the range of 0.5–5 Ω \square ⁻¹ beneath the designed electrodes. The *I–V* curves beneath the front silver pad sintered at lower, optimum, and over-fired temperatures are always linear. However, they are nonlinear between the frontside and rear-side electrodes of the double phosphorus-doped N-type silicon substrates, which suggests that the contact type changes from Ohmic to Schottky. The experimental results imply that the photogenerated current on the emitter surface is mainly transferred transversely via the crystallites embedded in the shallow highly doped silicon layer rather than the deeper lightly doped contact area at the contact interface.

1. Introduction

Thus far, discussions on current transport between a silver electrode and silicon emitter have mostly been based on numerous scanning or transmission electron microscopy observations with detailed modeling and inference of the microscopic mechanism. [1,2] A large number of silver crystallites or colloids exist at the interfaces of samples with different morphologies, [3] sintering temperatures, [4] and doping concentrations. [1] A current

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The ORCID identification number(s) for the author(s) of this article can be found under https://doi.org/10.1002/pssa.201900480.

DOI: 10.1002/pssa.201900480

flows through the Ag–Si interface via these direct or indirect contact crystallites or colloids. [5–8] Few studies have used mathematical and physical models to analyze the conductive channels from the macroscopical perspective, [9] including the current distribution at the contact interface, *I–V* curve, current transfer length, and sheet resistance beneath the front electrode.

According to the traditional current transfer theory, in planar devices, current transport between a metal and a semiconductor is completed within a very short distance at the edge of the electrode. [10] The current transport path is clearly defined. A silicon solar cell is a large planar device. However, its contact interfaces differ considerably from that between metals and semiconductors. This is because, in the solar cell, the contact is mainly composed of sintered screen-printed silver paste. The phases and composition are complex

at the interface. The current transport path, transfer length ($L_{\rm t}$), and sheet resistance ($R_{\rm sk}$) beneath the electrode need to be clarified further.^[11]

In this study, the double rectangular transmission line method, contact-end voltage measurements, and I–V tests were applied to characterize $L_{\rm t}$, $R_{\rm sk}$, and current transport path including its distribution, through pertinent pattern and test fixtures. In addition, the rational and limitation of contact-end voltage measurement were discussed in detail to obtain reliable solutions from the model.

2. Experimental Section

The theoretical model is based on the double transmission line model and considers the influence of the internal resistance of the electrode and the sheet resistance below the electrode on current transmission. [12] As shown in **Figure 1**, the current flows from point 1 to the first electrode, from point 8 to the doped layer between the two electrodes, from point 7 to the second electrode, and from point 4 to the negative pole of the source meter. Four differential equations can be formulated to describe the distributions of current and voltage in the contact area by establishing a rectangular coordinate system with 0 as the origin.

$$\frac{dI_2(x)}{dx} = \frac{(V_1(x) - V_2(x))Z}{\rho_c}$$
 (1)



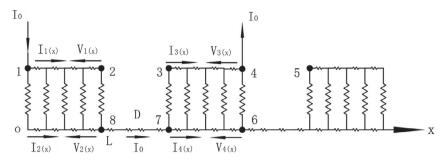


Figure 1. Double rectangular transmission line model schematic diagram.

$$\frac{\mathrm{d}V_2(x)}{\mathrm{d}x} = -I_2(x)\frac{R_{\mathrm{sk}}}{Z} \tag{2}$$

$$\frac{\mathrm{d}V_1(x)}{\mathrm{d}x} = -I_1(x)\frac{R_{\mathrm{sm}}}{Z} \tag{3}$$

$$\frac{\mathrm{d}I_1(x)}{\mathrm{d}x} + \frac{\mathrm{d}I_2(x)}{\mathrm{d}x} = 0\tag{4}$$

where Z is the length of the electrode. The following differential equation can be obtained using the above simultaneous equations

$$\frac{d^2 I_2(x)}{dx^2} - \alpha^2 I_2(x) + \frac{R_{\rm sm}}{\rho_c} I_0 = 0$$
 (5)

where

$$\alpha = \sqrt{(R_{\rm sk} + R_{\rm sm})/\rho_{\rm c}} = 1/L_{\rm t} \tag{6}$$

 $R_{\rm sk}$ is the sheet resistance under the electrode, $R_{\rm sm}$ is the square resistance of the electrode, $\rho_{\rm c}$ is the contact resistivity, and $L_{\rm t}$ is the transfer length. The solution of the second-order linear differential equation can be given as follows

$$I_2(x) = C_1 + C_2 e^{\alpha x} + C_3 e^{-\alpha x} \tag{7}$$

According to the boundary conditions

$$I_1(0) = I_0, I_2(0) = 0$$
 (8)

$$I_1(L) = 0, I_2(L) = I_0$$
 (9)

The values of the coefficients of C_1 , C_2 , and C_3 can be calculated, and then the current distributions $I_1(x)$ and $I_2(x)$ can be obtained. Using the current equations, the potential difference V_{37} between the electrode (point 3) and emitter (point 7) can be expressed as follows

$$V_{37} = V_3 - V_7 = \frac{\rho_c}{Z} \frac{\mathrm{d}I_4(x)}{\mathrm{d}x} \Big|_{x=P7} \tag{10}$$

which is theoretically identical to V_{28} (the potential difference between the points 2 and 8) in size and in the opposite direction. The model also introduced the contact-end resistance initially proposed by Shockley,^[13] which is defined as $R_{\rm end} = \frac{V_{\rm out}}{I_{\rm input}} = \frac{V_{45}}{I_7}$, where V_{45} is the potential difference between the electrode (point 4) and the emitter (point 5). Considering the fact that the value of

current is zero on the right side of point 6 and current flows through the first and second electrodes, potential of the third electrode (at point 5) can be considered to be equal to that at point 6.^[14] Then, V_{45} can be considered to be approximately equal to the potential difference between points 4 and 6 (V_{46}). Based on the definition, the following equation can be established

$$V_{45} = V_{43} + V_{37} = \frac{R_{\rm sm}}{Z} \int_0^L I_1[x] dx + \frac{-\rho_{\rm c}}{Z} \frac{dI_4(x)}{dx} |_{x=P7}$$
 (11)

where D is the distance between electrodes. The potential difference between the first electrode at point 2 and the second electrode at point 3 can be written as follows

$$V_{23} = \frac{\rho_{\rm c}}{Z} \frac{\mathrm{d}I_2(x)}{\mathrm{d}x}|_{x=P8} + I_0 R_{\rm sh} \frac{D}{Z} - \frac{\rho_{\rm c}}{Z} \frac{\mathrm{d}I_4(x)}{\mathrm{d}x}|_{x=P7} \tag{12}$$

The potential difference between points 1 and 2 of the first electrode is

$$V_{12} = \frac{R_{\rm sm}}{Z} \int_0^L I_1[x] dx \tag{13}$$

The values of V_{45} , V_{23} , and V_{12} can be measured directly using the voltage probes. Equation (11)–(13) have four unknown parameters: $R_{\rm sm}$, $R_{\rm sk}$, $\rho_{\rm c}$, and the sheet resistance of emitter $R_{\rm sh}$. $R_{\rm sm}$ can be directly measured using a four-probe square resistance tester and $R_{\rm sh}$ is semiquantified (the range of the values are known when the samples are removed from the diffusion furnace in the factory and measured by the four-probe method). Thus, there are three unknowns to be determined. Assuming that the value of $R_{\rm sk}$ between the adjacent electrodes is equal, $R_{\rm sh}$ and $\rho_{\rm c}$ are uniform around the two adjacent electrodes. $R_{\rm sk}$, $R_{\rm sh}$, and $\rho_{\rm c}$ can be obtained using Equation (10)–(12). $L_{\rm t}$ can be obtained using Equation (6). As shown in Figure 2,

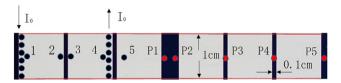


Figure 2. Schematic of sample and probe positions. Points 1–5 are potential probes, and points P1–P5 are probe positions for contact-end voltage measurement.



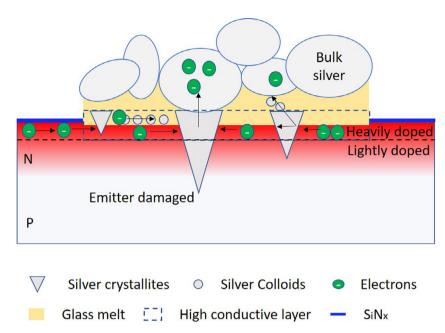


Figure 3. Schematic of the current transport path. Electrons flow from the side of the crystallites to the electrode by direct contact or quasidirect contact in the highly doped layer. Few electrons entered the silver crystallites from the lightly doped region and were then transferred to the electrode.

the probe positions corresponding to V_{12} , V_{23} , V_{34} , and V_{45} are marked.

Figure 3 shows a schematic of the phase distribution at the contact interface and the possible transport path of photogenerated electrons under illumination. The aforementioned model is used to solve the electrical parameters of the interface.

The test pattern was carefully designed. To increase the proportion of contact voltage in the measurement and deduce the current transport path and current distribution, the length of the silver electrodes was extended, and the gap between the electrodes was reduced. To ensure that the current is uniformly transmitted in the electrode in a transverse direction, parallel probes were used for the input and output current. The positions of the probes are shown in Figure 2. The current and voltage were measured directly using a digital source meter Keithley 2602B. The probes were gold plated.

The experimental substrates were P-type(N^+ emitter) monocrystal silicon solar cells, and these were divided into two groups. The substrates in one group were covered by a passivation layer of SiN_x and those in the other group had no passivation layer; however, they had a naturally formed thin SiO_x film. The sheet resistance was $90{-}100~\Omega$ \square^{-1} , and the surface concentration was $1{-}2\times10^{20}~\mathrm{cm}^{-3}$. Commercialized burnt-through silver paste and evaporated silver were used for metallization. The samples screen printed with silver paste were metallized in a box-type infrared rapid annealing furnace at six temperatures, 600, 660, 690, 720, 750, and 780 °C. After sintering or evaporation, the samples were cut into squares with a width of 1 cm for measurement, as shown in Figure 2.

In addition, to determine the variation of the I-V curves during the sintering process, N-type double-sided phosphorus-doped diffused samples were prepared, which had the same surface doping concentration as the N^+ emitter cells.

3. Results and Discussion

3.1. Selection of Test Current

The photogenerated current density of a monocrystalline silicon solar cell is \approx 40 mA cm⁻². If the photogenerated current is simulated completely, the output current of the source meter is \approx 4 mA, which is difficult for the reliable measurement of voltage. Thus, the current–voltage relationship between two adjacent electrodes was initially measured to determine whether there is a nonlinear I–V relationship. [15] **Figure 4** shows the

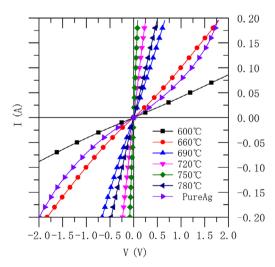


Figure 4. I-V curves of the adjacent two electrodes on SiN_x -deposited emitter surface. The curves of sintered samples below 690 °C show a weak nonlinear effect, whereas the evaporated sample shows a significant nonlinear relationship.

curves in the absence of emitter resistance, which accounted for a majority of the measured voltage. Then, the influence of contact voltage would be amplified. The emitter resistance was calculated by setting $R_{\rm sh}=95~\Omega$ \Box^{-1} . The current range was $\pm 0.2~\rm A$. It was clear that the I-V curves of the sintered electrodes below 690 °C exhibited a weak nonlinear effect, and the slope variation was small. Thus, these curves could be treated as approximately linear. The curves of evaporated pure silver electrodes showed a significant nonlinear relationship. For comprehensive consideration, the measurement current of I_0 was set to 0.1 A, which did not hinder judgment.

3.2. Measurement Results

Table 1 and **2** list the average measured values of V_{12} , V_{23} , V_{34} , V_{45} , and $R_{\rm sm}$ when the emitter surface was covered with SiN $_{x}$ and when it was not covered. Each average was obtained from 20 measurements using the pulse current method. It is clear that while $R_{\rm sm}$ decreased gradually with an increase in the temperature, V_{23} and V_{45} had significant inflection points at \approx 750 °C. The results indicated that the contact resistance between the electrode and the emitter surface decreased initially and then increased during sintering, which is consistent with the trend of I-V curves at different metallization temperatures shown in Figure 4.

In the early sintering stage, especially below 660 °C, the SiN_x layer present between the silver electrode and the emitter hindered the movement of electrons.^[11] The differences and trends of measurement values between Table 1 and 2, especially with respect to the contact-end voltage V_{45} , reflect the effect of SiN_x layer and indicate that it was difficult to form contact, even without the SiN_x layer.

Because the measurements of the contact-end voltage affected the final solutions, $^{[14,16]}$ we used a probe to directly measure the potential difference between point 4 and the emitter surface in the absence of the SiN_x layer. In addition, we changed the distance between them to further confirm the reliability and accuracy of V_{45} . The positions, marked as P1–P5, are shown in Figure 2. The measurement results are shown in **Figure 5**. It was found that the increasing trend of V_{45} was nonlinear (absolute value) with the increase in distance. Especially, in samples with better contact, V_{45} increased up to 2.5 times. The inflection point was located at P2.

Theoretically, the measured values of V_{45} should not be related to the location of the probe P1–P5. The nonlinear increase indicated that there was current inflow on the right side of the second electrode at point 6 (in Figure 1), and the current density on the right side was inversely proportional to the

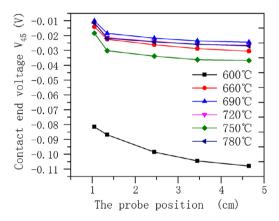


Figure 5. Relation of contact-end voltage V_{45} and probe-placed positions.

Table 1. Average measurements of V_{12} , V_{23} , V_{34} , V_{45} , and R_{sm} of the samples with SiN_x.

Temperatures [°C]	600	660	690	720	750	780
V ₁₂ [mV]	0.1731	0.162	0.16	0.137	0.14	0.138
V ₂₃ [V]	4.463244	2.129933	1.473477	1.176003	1.067686	1.140029
V ₃₄ [mV]	0.176	0.168	0.162	0.131	0.144	0.142
V ₄₅ [V]	-0.100442	-0.018548	-0.005852	-0.004481	-0.003664	-0.006242
R_{sml} [m Ω]	2.198	2.002	1.948	1.787	1.697	1.703
$R_{\rm sm2}$ [m Ω]	2.243	2.015	1.946	1.774	1.744	1.712

Table 2. Average measurements of V_{12} , V_{23} , V_{34} , V_{45} , and R_{sh} of the samples without SiN_x.

Temperatures [°C]	600	660	690	720	750	780
V ₁₂ [mV]	0.163	0.162	0.153	0.148	0.146	0.141
V ₂₃ [V]	3.083398	1.605412	1.275565	1.140534	1.308872	1.199277
V ₃₄ [mV]	0.168	0.164	0.155	0.153	0.151	0.146
V ₄₅ [V]	-0.049018	-0.011344	-0.006445	-0.008793	-0.009787	-0.008706
$R_{\rm sm1}$ [m Ω]	1.929	1.844	1.784	1.803	1.793	1.728
$R_{\rm sm2}$ [m Ω]	1.971	1.919	1.808	1.858	1.844	1.775

distance. With better contact and the potential ratio induced by the current flowing on the right, the measured values of V_{45} increased gradually. For the potential difference between 1.05 and 1.35 cm, the estimated current was ≈ 0.3 mA, accounting for 3‰ of the test current. The significance of these measurements will be discussed later. When the probe was fixed, the value of V_{45} at different temperatures followed the same trend as in Table 1 and 2, which varied with the metallization temperature and had sufficient selectivity. The systematic error of contact-end voltage measurement did not affect its discrimination for different samples.

3.3. Numerical Solutions of $R_{\rm sh}$, $L_{\rm t}$, $R_{\rm sk}$, and $\rho_{\rm c}$

The values of $R_{\rm sh}$ of the samples varied between 90 and $100\,\Omega$ \Box^{-1} , which can be used as a benchmark to determine the accuracy of the theoretical model compared with the extracted $R_{\rm sh}$. Figure 6 shows that the numerical solution of $R_{\rm sh}$ deviated far from the known scope of $R_{\rm sh}$, especially for samples sintered at low temperatures below 720 °C. After subtracting the emitter resistance between points 2 and 3, the measured value of V_{23} was much larger than the voltage obtained using Equation (10). The value of V_{45} did not fully represent the potential loss between points 7 and 4 (or points 1 and 7), the voltage loss was reflected in the numerical solution of $R_{\rm sh}$. According to the interface analysis of sintering, SiN_x existed mostly at temperatures lower than 660 °C.It can be approximated that the value of $R_{\rm sk}$ is still equal to $R_{\rm sh}$. Therefore, the model can be approximately modified to $^{[17]}$

$$V_{23} = I_0 \frac{R_{\rm sh}}{Z} (d + 2L_{\rm t}) \tag{14}$$

Thus, assuming that both $R_{\rm sh}$ and $R_{\rm sk}$ are 95 Ω \Box^{-1} , we can calculate the value of $L_{\rm t}$ at 600 °C, which is 0.112 cm, and that of $\rho_{\rm c}$ is 1.19 Ω cm², which are consistent with the existence of SiN_x. The current flowed at a length of approximately $L_{\rm t}$ beneath the electrode.

Around the optimal sintering temperature, the SiN_x and SiO_x layers were etched away and sufficient conductive channels were established. The solution of $R_{\rm sh}$ was close to the values provided by the cell manufacturer, and the contact resistivity was between 2×10^{-4} and $5\times 10^{-3}\,\Omega$ cm². The corresponding value of $L_{\rm t}$ varied between 50 and 500 μ m and that of $R_{\rm sk}$ was

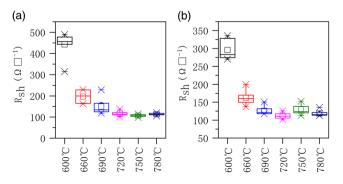
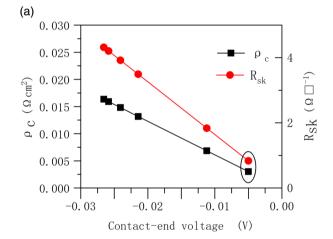


Figure 6. Numerical solutions of $R_{\rm sh}$. a) SiN_x-covered emitter. b) Emitter without SiN_x (covered by one thin SiO_x layer formed in air).

basically distributed in the range of $0.5-5 \,\Omega \,\Box^{-1}$. It is shown that the width of $L_{\rm t}$ is considerably greater than the current electrode fingers width (\approx 45 µm), which is why the traditional transmission line method can obtain reasonable results when calculating the contact resistivity (this method directly multiplies the measured contact resistance with the contact area per unit to obtain the contact resistivity^[18]). Despite some deviations, the model reflected the objective changes in metallization and was reliable.

In the aforementioned discussion, the measurements of V_{45} were related to the distance of two probes caused by the current flowing on the right side. Figure 7a,b shows the effect of V_{45} on ρ_c , $R_{\rm sk}$, $R_{\rm sh}$, and $L_{\rm t}$ considering that optimal fired samples were not covered with SiN $_x$ at 720 °C as an example (judged from the numerical extraction of $R_{\rm sh}$). The right-side data shown using circles were calculated using the hypothetical value of $V_{45}=0.005$ V. The value of ρ_c was sensitive to the variation in V_{45} , whereas $R_{\rm sh}$, $R_{\rm sk}$, and $L_{\rm t}$ were relatively insensitive. An appropriate fixed distance test helped in obtaining stable and differentiate contact resistivity, whereas the accuracy of the model still requires improvements.



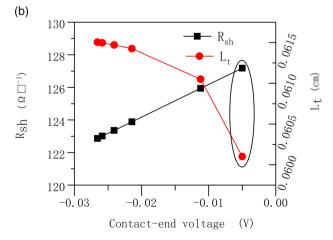


Figure 7. The effect of V_{45} measurements on $\rho_{\rm c}$ and $R_{\rm sk}$. a) $\rho_{\rm c}$ and $R_{\rm sk}$; b) $R_{\rm sh}$ and $L_{\rm t.}$

3.4. Current Distribution of Contact Interface

The current distribution in the electrode and the contact interfaces at different sintering temperatures can be determined from the average values of the above parameters. In **Figure 8**, the representative sintering temperatures of 600, 750, and 780 °C are taken as examples. Below 750 °C, the transfer length decreased gradually with an increase in temperature; however, it increased again above 750 °C. The shorter the transfer length, the lower the cell power loss.

The photogenerated current on the surface of the actual cells flows from both sides of the finger grid. The current distribution is also regulated by $R_{\rm sm}$, $R_{\rm sk}$, and $\rho_{\rm c}$.

3.5. Current Transport Path of Contact Interface

Figure 8 shows that the numerical solution of the theoretical model indicated that the current flowing from the electrode at point 1 passed through the electrode and the interface layer below the electrode, before it entered the emitter surface. The transmission distance could be approximately considered as the transfer length $L_{\rm t}$. Figure 9a,b shows their distributions obtained using numerical solutions. The model indicates that the current traveled the length of $L_{\rm t}$ in a highly conductive layer

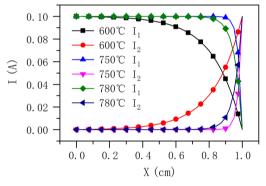


Figure 8. Current distributions in the electrode and highly conductive layer of the samples sintered at 600, 750, and 780 °C. Transfer length decreases gradually for firing temperatures between 600 and 750 °C but increases for firing temperatures above 750 °C.

with square resistance nearly 20 times smaller than $R_{\rm sh}$ under the electrode, as shown in Figure 9c,d.

The scanning electron microscopy photographs of the contact interfaces of samples with no SiN_x layer showed that the number of silver crystallites at the top or the edges of the pyramid increased gradually, as shown in **Figure 10**. It also corresponded to the decrease in contact resistivity below the over-firing temperature and was in accordance with the SiN_x -covered samples. The current was mostly transmitted to the silver electrode through direct or quasidirect contact of the crystallites, which forms the physical basis of the larger L_t .

The high conductivity layer under the electrode is a complex system formed by silver crystallites(including colloids), glass melt, and a heavy doping layer. The current was mainly conducted from the left or right of the crystallites to the electrodes. We designed an experiment to determine the principal path of electron transmission. Double-sided phosphorus-doped N-type silicon substrates were fabricated with the same front structure as P-type cells. Phosphorus was also doped on the rear side of the samples to facilitate Ohmic contact. The samples were printed with the same commercial paste and sintered at the same temperature. Then, the rear side of the sintered samples was scrubbed with HF 5% solution and then coated with GaSn liquid metal after drying. The *I–V* curves of the samples were measured between the front and rear electrodes. Under this condition, the current transferred from the bottom of the contact crystallites to silicon. It was found that the I-V curves changed from linear to nonlinear above 720 °C, as shown in Figure 11a, where a typical Schottky contact was formed. The reverse breakdown voltage was greater than 20 V, and the reverse saturation current increased significantly with an increase in the sintering temperature. A significant forward breakdown voltage of \approx 5 V was observed. Whether the applied bias was positive or negative, the electrons transport was hindered in over-fired samples.

Figure 12 shows the cross-sectional images of the contact interface sintered at 600, 720, and $780\,^{\circ}\text{C}$, respectively. At 600 $^{\circ}\text{C}$, most of the passivation layer of SiN_x with a thickness of $\approx 70\,\text{nm}$ existed at the interface; however, the glass melt in some areas started penetrating it. At $720\,^{\circ}\text{C}$, little amounts of SiN_x remained, where direct or quasidirect contact formed at many locations. At $780\,^{\circ}\text{C}$, only few locations had direct or

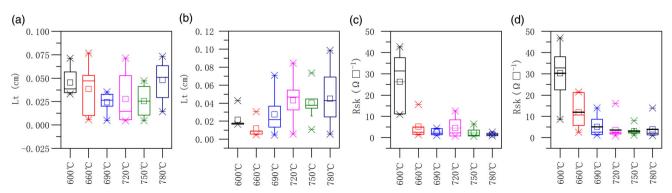


Figure 9. Relationship among L_t , R_{sk} , and sintered temperatures of two types of samples. a) L_t with SiN_x . b) L_t without SiN_x . c) R_{sk} with SiN_x . d) R_{sk} without SiN_x .



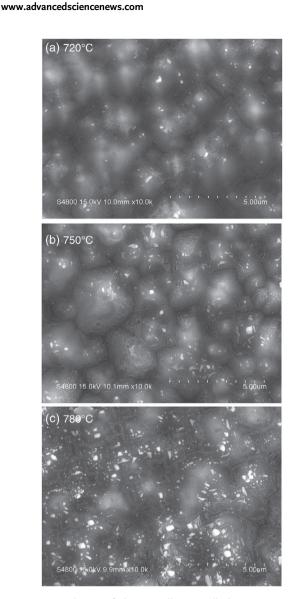
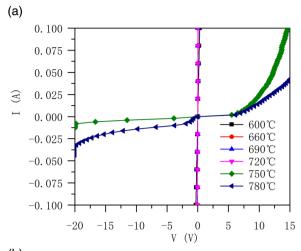


Figure 10. Distribution of silver crystallites (small white areas) on the top of the pyramids at the contact interface of the samples without SiN_x . a) Few crystallites at $720\,^{\circ}$ C. b) Few more crystallites at $750\,^{\circ}$ C. c) Excessive amount of crystallites at $780\,^{\circ}$ C.

quasidirect contact, and the thickness of the glass layer was mostly above 100 nm. It can be inferred that the nonlinear I–V curves originated because of the low doping concentration (when the highly doped layer was etched away) and the thick glass melt (doped with silver crystallites or colloids).

The current would mainly be transferred from the side of the contact spots in the highly doped area to the electrode for the optimally fired samples at $\approx\!750\,^{\circ}\text{C}$, and it was rarely transferred via the lightly doped contact area, as shown in Figure 3. The thickness of the highly doped layer was $\approx\!50\,\text{nm}$. This was problematic as more silver crystallites must be formed on the emitter surface for the transverse current transfer, whereas the penetration depth of silver crystallites should be controlled to reduce junction leakage or shunting. The entire emitter surface covered with small silver particles or tiny colloids was the only



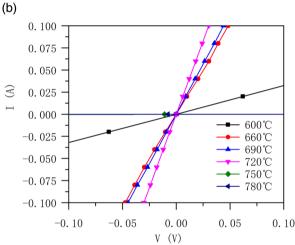


Figure 11. *I–V* curves measured between front and rear electrode of double-side phosphorus-doped N-type silicon. a) Complete curves. b) Magnification around the origin of the coordinate.

ideal contact. Surface morphology and defects, [19,20] including those arising from process conditions, would result in silver particles of different sizes, which will damage the emitter to various extents and block the transport of electrons.

4. Conclusions

The double transmission line model along with contact-end voltage measurement was used to objectively characterize the current transport path of front contact interfaces in silicon solar cells during metallization. It was shown that the current transversely flowed to $\approx\!50~\mu\text{m} \le L_{\rm t} \le 500~\mu\text{m}$ in a complex relatively highly conductive interface, comprising a heavily doped layer, silver crystallites(including silver colloidal particles), and glass melt with a sheet resistance of $\approx\!0.5~\Omega~\Box^{-1} \le R_{\rm sk} \le 5~\Omega~\Box^{-1}$ beneath the electrodes in the designed pattern. The comparisons of $I\!-\!V$ curves between the silver electrodes on the emitter surface and the front and rear electrodes in the double-sided phosphorus-doped N-type silicon substrates indicated that the electrons



(a) 600°C Local etching penetration SiNx 70 nm (b) 720°C Direct contact (c) 780°C Contact and penetration Glass melt 100 nm

Figure 12. Cross-sectional focused ion beam-scanning electron microscopy (FIB-SEM) images of Ag–Si contacts sintered at different temperatures. a) 600, b) 720, and c) 780 °C.

were mainly transferred from the shallow, highly doped area to the embedded crystallites and then to the electrode. A Schottky contact was formed between the silver crystallites and the deeper, lightly doped area, blocking the electron transport from the bottom. The model and experimental results show that the performance of silver paste can be further improved by reducing the paste corrosion to the emitter, increasing the precipitation of silver particles with shallow penetration depth, and reducing the sheet resistance of the electrode. In addition, the reliability and selectivity of the contact-end voltage measurements were

sufficient to distinguish between the samples contacted in various degrees, although the accuracy must be enhanced further

Acknowledgements

This study was supported by the National High Technology Research and Development Program of China (grant no. 2014AA052101).

Conflict of Interest

The authors declare no conflict of interest.

Keywords

contact-end voltages, contact resistivities, current transport paths, solar cell metallization, transfer lengths

Received: June 19, 2019 Revised: September 17, 2019 Published online:

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