

Impact of voltage and frequency on electrical characteristics of MIS capacitors with triphenylamine layer



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ABSTRACT

This study examines the effect of triphenylamine (TPA) interlayers on the electrical properties of Al/TPA/p-Si metal-interlayer/insulator material-semiconductor (MIS) capacitors. Using Gaussian software, TPA molecular structure was optimized, and HOMO-LUMO energy levels were simulated. Capacitance-conductance-voltage (*C-G-V*) measurements were performed at room temperature over -4 V to $+4\text{ V}$ and 50 kHz – 700 kHz . AFM and SEM analysis showed TPA films were smooth with a uniform thickness of about 178 nm . The *C-V* characteristics revealed a frequency-dependent decrease in capacitance, indicating a continuous distribution of interface states. Barrier height (Φ_B) increased from 0.305 eV at 50 kHz to 0.655 eV at 700 kHz , while the active interface trap density (D_{it}) decreased from $6.73 \times 10^{12}\text{ eV}^{-1}\text{ cm}^{-2}$ to $3.23 \times 10^{11}\text{ eV}^{-1}\text{ cm}^{-2}$. Additionally, the accumulating region exhibited low series resistance values (between 6.88 and 8.44Ω). These results suggest that TPA thin films are effective for MIS capacitors.

1. Introduction

Organic devices, including resistors, capacitors, Schottky diodes, photodiodes, and thin-film transistors, have been extensively studied, and significant advancements have been made in their manufacturing technologies [1–4]. Since 2000, researchers have increasingly utilized organic materials to develop high-efficiency devices with optimal electrical properties [5–10]. They have enhanced the metal/semiconductor (MS) contact by modifying the barrier height and improving the quality of MS structure through the replacement of SiO_2 , typically used in MS connections, with organic or inorganic interfacial layers. An alternative method to design rectifying devices involves creating metal-interlayer/insulator-semiconductor (MIS) structures. In MIS diodes, rectification is achieved by the pinning of the Fermi level by interface states. It is simple to modify the interface structure of MIS diodes by adding suitable inorganic metal oxides [11–14], polymers [15–17], or functional organic molecules [18–22]. The voltage range over which MIS diodes are utilized typically varies depending on the application type, the semiconductor technology employed, and the thickness of the oxide layer. Generally, MOS diodes are operational across various voltage ranges. Specifically, in the low voltage range (1.2 V–3.3 V), MOS diodes are commonly used in modern integrated circuits

(ICs), particularly within low-voltage digital CMOS circuits [23–25].

In a recent study, Sunkur et al. investigated the electrical and charge transport characteristics of the Al/1PA2N/p-Si MIS device using current-voltage techniques [26]. They determined the barrier height, series resistance, and interface state density of the MIS structure. In another work, these researchers fabricated an Al/ERY/p-Si MIS structure and calculated its electrical characteristics using capacitance-voltage and current-voltage measurements [27]. Pakma et al. also created an MIS-type structure with a Coronene interlayer and subsequently determined the electrical characteristics of this structure [19]. Additionally, Imer et al. fabricated a Sn/C₁₄H₁₅N₃/p-Si heterojunction using the spin coating technique and measured its electrical characteristics through current-voltage and capacitance-voltage measurements [28].

Triphenylamine (TPA) is an organic compound that is highly effective for surface coating in Schottky device designs [29]. TPA is also widely used as a donor group in the literature due to its excellent electron-donating properties [30–33]. It features continuous conjugation between the central nitrogen atom and three neighboring phenyl groups, making it an ideal organic structure for donor- π -acceptor (D- π -A) systems in recent years [34–37]. Structures containing triphenylamine molecules generally reduce the gap between the highest occupied molecular orbital (HOMO) and the lowest unoccupied

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molecular orbital (LUMO), resulting in absorption in the near-infrared region [38,39].

Cavdar et al. presented an Al/TPA/p-Si/Al diode structure and they examined the experimental *I-V* characteristics at various illumination intensities to explore its electrical and photo response properties [40]. In this study, we focus on the detailed frequency-dependent electrical characterization of TPA MIS capacitors, emphasizing the role of the insulator-semiconductor interface, particularly interface states and series resistance, on the device parameters. Consequently, the capacitance-voltage and conductance-voltage characteristics of the TPA organic layered Al/TPA/p-Si/Al structure were studied under dark conditions at room temperature across a frequency range of 50 kHz–700 kHz and a voltage range of ± 4 V to investigate the impacts of interface state density and series resistance on the electrical characteristics of the structure.

In this study, the active interface trap density (D_{it}) values of the TPA MIS capacitor were calculated using the Hill-Coleman method and found to decrease with increasing frequency, while the barrier height (ϕ_B) values increased. Voltage-dependent profiles of D_{it} and series resistance (R_s) were determined through capacitance measurements in the low-high frequency range ($C_{LF} - C_{HF}$) using the Nicollian-Brews method. Furthermore, based on the obtained results, we evaluated the potential applications of TPA as an interfacial layer in MIS-based technology. To our knowledge, no prior research has been conducted on the capacitors in TPA thin films. Additionally, this study examined frequency-dependent electrical parameters, including barrier height (ϕ_B), Fermi level (E_F), depletion region width (W_D), and acceptor atom concentration (N_A). These findings provide new insights into the electrical behavior of TPA MIS structures and their potential for future electronic applications.

2. Experimental section

In this study, we utilized a p-type Si semiconductor with (100) surface orientation, $1 \Omega\cdot\text{cm}$ resistivity, 525 μm thickness, and a polished surface. The Si semiconductor was chemically cleaned to eliminate organic and inorganic contaminants. It was ultrasonically cleaned with chloroform, methanol, and acetone for 5 min each. Subsequently, it was washed with HF for 1 min and rinsed thoroughly with ultrapure water to remove the oxide layer. Immediately after cleaning, a $\sim 1500 \text{ \AA}$ thick layer of high-purity (99.999 %) Al was thermally evaporated onto the back matte surface of the p-Si substrate at a pressure of 10^{-6} Torr using a physical evaporation system. The p-Si/Al structure was annealed at 575 °C for 5 min in a dry nitrogen atmosphere to achieve a low-resistance ohmic contact.

For the organic layer formation, 0.4 g of TPA ($(\text{C}_5\text{H}_4\text{NCH}_2)_3\text{N}$) was dissolved in 10 ml of chloroform and stirred for 10 min. A TPA organic thin film was then deposited on the polished surface of the p-Si substrate using the spin coating method, first at 500 rpm for 10 s and then at 3000 rpm for 20 s. Rectifying contacts were created on the organic layer using

a mask and the thermal evaporation method (radius of the contact was 1.10 mm). SEM images of the diode and the AFM measurements were taken using a HITACHI SU5000 Field Emission Scanning Electron Microscope (FE-SEM) and with a NanoMagnetics hpAFM device, respectively. Capacitance and conductance measurements were performed using a Keithley 4230 source meter. A diagram of the Al/p-Si MIS-type capacitor fabrication process with a TPA interlayer is shown in Fig. 1.

3. Results and discussion

We performed calculations using density functional theory (DFT) at the B3LYP/6-31G (d,p) level to determine the electronic properties of TPA (see Fig. 2). This analysis includes the determination of the HOMO and LUMO energy levels, as well as their distribution and location, which are crucial for understanding the molecular features. DFT-calculated energy levels of the LUMO and HOMO for the TPA compound are illustrated in Fig. 3. The values of TPA's HOMO and LUMO energies, along with other molecular characteristics computed from these values, are presented in Table 1 (see Fig. 4).

When two semiconductors with differing electron affinities, band gaps (E_g), and work functions are combined to form a heterojunction diode, energy band discontinuities occur due to the alignment of the Fermi level. When the free interfacial states align with the vacuum level,

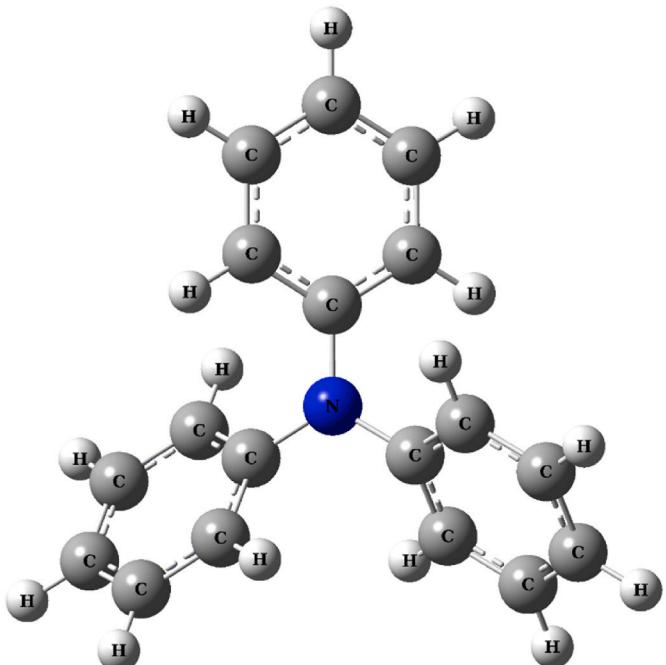


Fig. 2. The optimized molecular structure of TPA compound.

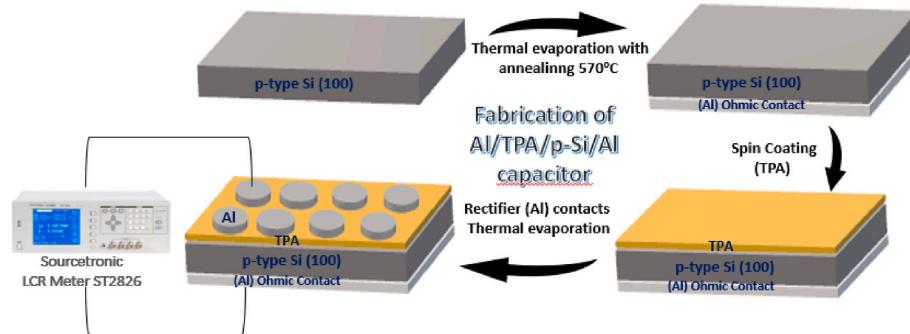


Fig. 1. Schematic diagram of Al/TPA/p-Si/Al MIS-type capacitor fabrication procedure.

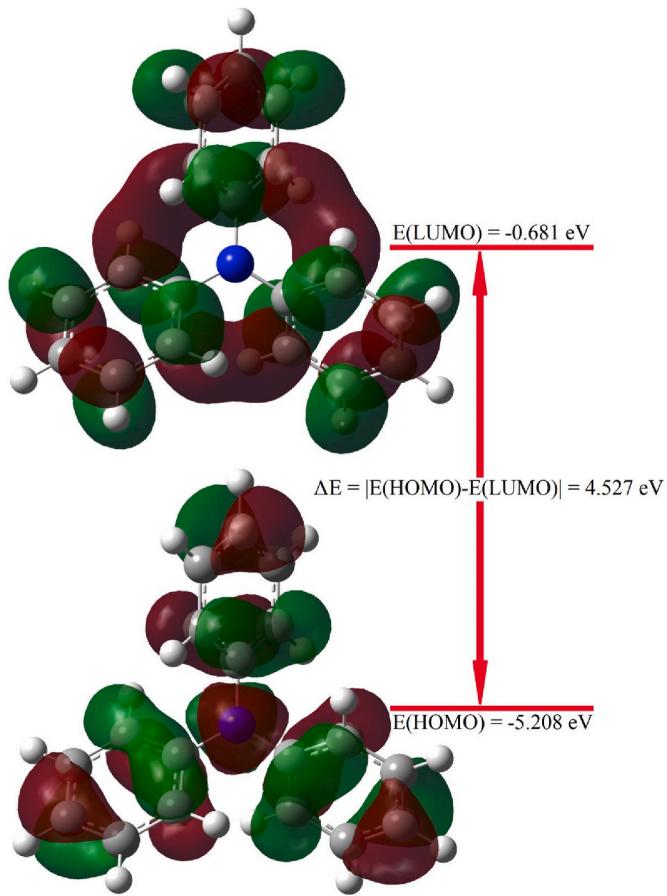


Fig. 3. DFT calculated energy levels of LUMO and HOMO of TPA compound.

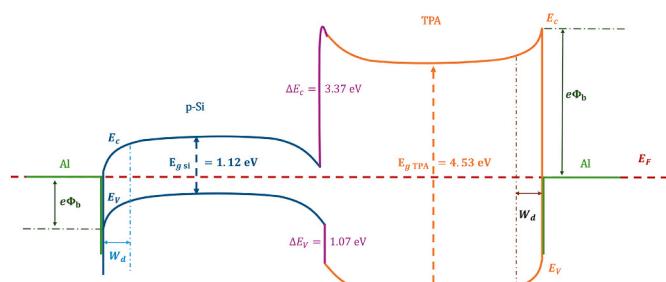


Fig. 4. The energy band diagram of Al/TPA/p-Si/Al MIS-type capacitor.

Table 1
Molecular descriptors derived from the HOMO and LUMO energy levels of TPA.

Parameters (eV)	Value (eV)
E_{LUMO} (eV)	-0.681
E_{HOMO} (eV)	-5208
Energy bandgap $ E_{\text{HOMO}} - E_{\text{LUMO}} $	4527
Ionization potential ($I = -E_{\text{HOMO}}$)	5208
Electron affinity ($A = -E_{\text{LUMO}}$)	0,681
Chemical hardness ($h = (I - A)/2$)	2263
Chemical softness ($z = 1/h$)	0,221
Electronegativity ($\chi = (I + A)/2$)	2945
Chemical potential ($\mu = -(I + A)/2$)	-2945
Electrophilicity index ($w = \mu^2/2h$)	1915
Maximum charge transfer index ($\Delta N_{\max} = -\mu/h$)	1301

the discontinuities in the conduction band are equal to the difference in the electron affinities of the semiconductors ($\Delta E_c = \chi_{\text{Si}} - \chi_{\text{TPA}}$), according to Anderson's affinity rule. Furthermore, the valence band discontinuities (ΔE_V) are calculated as $\Delta E_V = \Delta E_g - \Delta E_c$, where ΔE_g represents the difference in the band gaps between the two semiconductors.

Fig. 5a shows the SEM image and **Fig. 5b** shows the schematic diagram of the TPA diode. The SEM images in **Fig. 5a** shows that the TPA organic layer has an average thickness of 170 nm.

Fig. 6(a) and (b) and (c) show two- and three-dimensional (2D and 3D) AFM images of the TPA organic thin film $1 \times 1 \mu\text{m}^2$, $3 \times 3 \mu\text{m}^2$, and $5 \times 5 \mu\text{m}^2$ scales. The root mean square (rms) surface roughness of the TPA films were found as 14.46 nm, 17.81 nm, and 24.76 nm, respectively. The surface morphology of the TPA film were reasonably smooth, with uniform grain distribution. The values of the roughness parameters have confirmed the validity and significance of establishing a uniform interfacial contact between the TPA and the rectifier contact, which enhances the performance of electronic devices [41].

The electrical properties of the Al/TPA/p-Si/Al capacitor were investigated under forward and reverse bias voltages at various frequencies. **Fig. 7** presents the capacitance-voltage ($C_m - V$) characteristics of the MIS structure measured at ± 4 V across a frequency range of 50 kHz–700 kHz. The $C-V$ curve exhibited accumulation, depletion, and inversion regions, with capacitance variations observed as a function of voltage and frequency [42]. Notably, prominent peaks were observed in the -1 V – 0 V range. The magnitude of these peaks decreased and shifted towards lower voltages with increasing frequency. This behavior is attributed to the reconfiguration of interface states [43].

There was a rapid decrease in C_m value from -1 V to $+1 \text{ V}$ within the $\pm 1 \text{ V}$ voltage range. This region corresponded to the depletion region, where charge carriers are depleted [44]. At positive voltage values (greater than $+1 \text{ V}$), there was no significant change in capacitance regardless of frequency, as the defect charges do not have sufficient time to respond to the applied voltage. This region is known as the inversion region, characterized by a change in the polarity of the charge carriers [45]. When a certain positive voltage threshold is exceeded, minority carriers accumulate in the inversion layer compared to majority carriers. Although the voltage increases, the depletion zone reaches its maximum depth [46].

Fig. 8 illustrates the measured conductance-voltage ($G_m - V$) characteristics of the Al/TPA/p-Si/Al MIS-type capacitor at increasing frequencies. It was observed that conductance increases with frequency, which was an expected outcome since the frequency-dependent behavior of conductance is the inverse of that of capacitance [47,48]. This variation in conductance confirms the presence of interfacial states. Additionally, conductance decreased with increasing voltage at all frequencies, and in the positive voltage region, all conductance values converge.

Fig. 9 represents the capacitance-frequency ($C_m - f$) characteristics measured at negative bias voltage, highlighting the diode's accumulation and depletion regions. The measurements reveal that the capacitance value decreased with increasing frequency across all voltages. Specifically, at -1 V , the capacitance values were 18.1 nF at 50 kHz and 4.28 nF at 700 kHz. This reduction in capacitance at higher frequencies may be attributed to the inability of majority of carriers to follow the AC signal [49]. The capacitance at high frequencies is primarily due to space charge capacitance [50]. Furthermore, the variation in capacitance in such heterojunction structures can be ascribed to the relaxation time and the series resistance effects arising from the interface states [51].

Fig. 10 shows the measured conductance-frequency ($G_m - f$) characteristics of the Al/TPA/p-Si/Al MIS-type capacitor under negative voltage conditions. The data indicate that the conductance of the capacitor increased with rising frequency across all voltage levels. This observation was consistent with the anticipated behavior that the capacitance and conductance of a capacitor were inversely related to

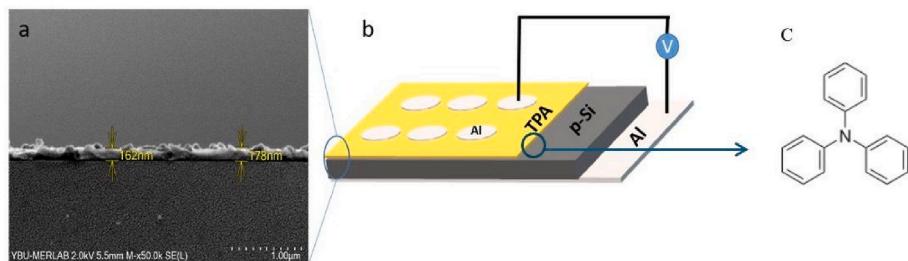


Fig. 5. (a) SEM image of TPA/p-Si structure, (b) Schematic diagram of Al/TPA/p-Si/Al capacitor.

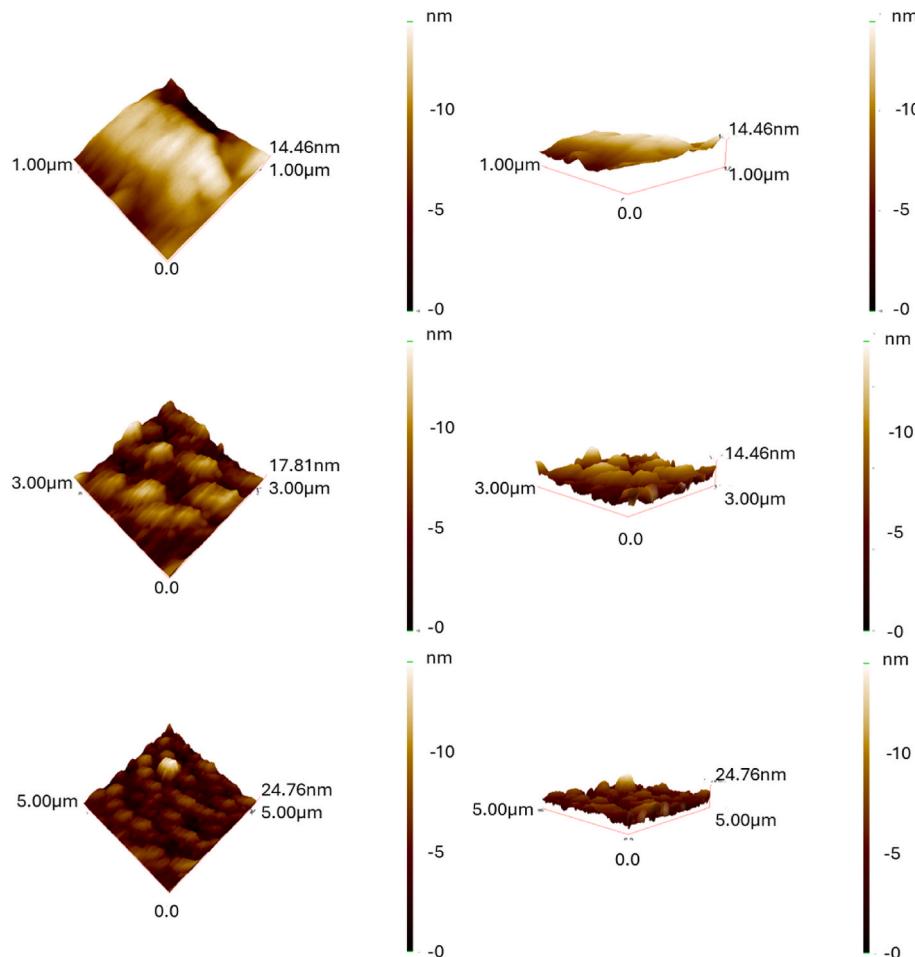


Fig. 6. (a) $1 \times 1 \mu\text{m}^2$, (b) $3 \times 3 \mu\text{m}^2$, (c) $5 \times 5 \mu\text{m}^2$ AFM images (2D and 3D) of TPA/p-Si structures.

increasing frequency [52]. The observed increased in conductance with frequency can be attributed to the interface states, the relaxation time of the charges, as well as the dynamics of carriers trapped between empty and occupied states at the Fermi level [53].

Series resistance (R_s) is a critical parameter that can induce non-ideal behavior in capacitors. It can be derived from both capacitance and conductance values using the expression proposed by Nicollian and Goetzberger [54,55]:

$$R_s = \frac{G_m}{G_m^2 + (\omega C_m)^2} \quad (1)$$

Here, C_m and G_m represent the measured capacitance and conductance, respectively, and ω denotes the angular frequency. Fig. 11 illustrates the $R_s - V$ curves at various frequencies, demonstrating that R_s decreased with increasing frequency. It was evidence that the series resistance

varies with both voltage and frequency under positive bias. Notably, the series resistance value decreased with increasing frequency under negative bias. This behavior of R_s can be attributed to the distribution of localized interface states [56].

The frequency-dependent R_s values of the TPA/p-Si structure is given for different voltages in Fig. 12. It is observed that the series resistance decreases with an increase in frequency, ranging from 50 kHz to 700 kHz, irrespective of the applied voltage. Although the series resistance values exhibited similar behavior across the spectrum, they were significantly higher under positive bias compared to negative bias. The series resistance values in the accumulation region (R_s) were determined to be 8.44Ω at 50 kHz and 6.88Ω at 700 kHz. The R_s values at various frequencies are presented in Table 2.

To eliminate the series resistance effect that adversely affects the capacitance and conductivity measurements of the diode, corrected

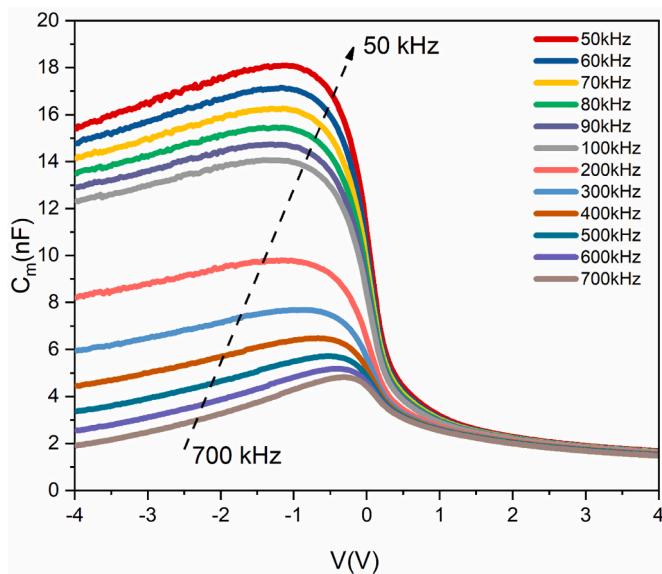


Fig. 7. Measured capacitance–voltage plots of the Al/TPA/p-Si Schottky diode.

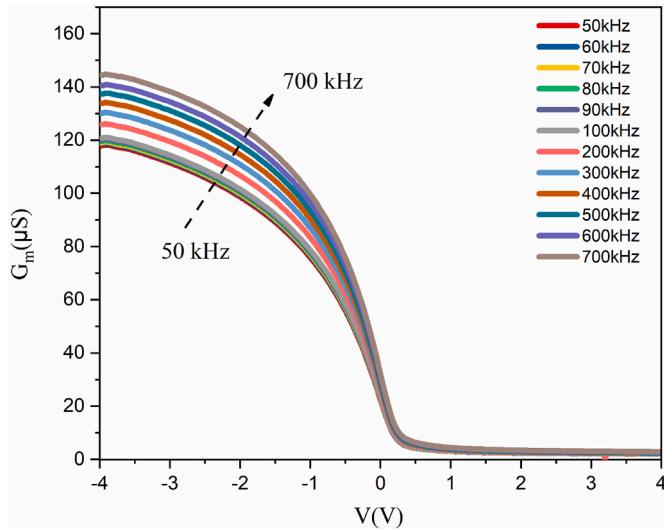


Fig. 8. Measured conductance-voltage plots of the Al/TPA/p-Si/Al MIS-type capacitor at different frequencies.

capacitance (C_c) and conductance (G_c) values can be obtained by using the following Eqs. (2) and (3) [55]:

$$C_c = \frac{(G_m^2 + \omega^2 C_m^2) C_m}{a^2 + \omega^2 C_m^2}, \quad G_c = \frac{(G_m^2 + \omega^2 C_m^2) a}{a^2 + \omega^2 C_m^2} \quad (2)$$

$$a = G_m - (G_m^2 + \omega^2 C_m^2) R_s \quad (3)$$

where C_m and G_m are the measured capacitance and conductance, respectively.

Figs. 13 and 14 show the voltage-dependent corrected capacitance (C_c) and corrected conductance (G_c/ω) graphs of the Al/TPA/p-Si/Al MIS-type capacitor at various frequencies, respectively, calculated using Eqs. (2) and (3). As depicted in Fig. 13, the capacitance values decreased with increasing frequency. Across all frequencies, the corrected capacitance reached its maximum value in the voltage range of $-3.5V$ to $-4V$.

Fig. 14 shows that the corrected conductance of the capacitor also decreased with increasing frequency. The conductance values attained

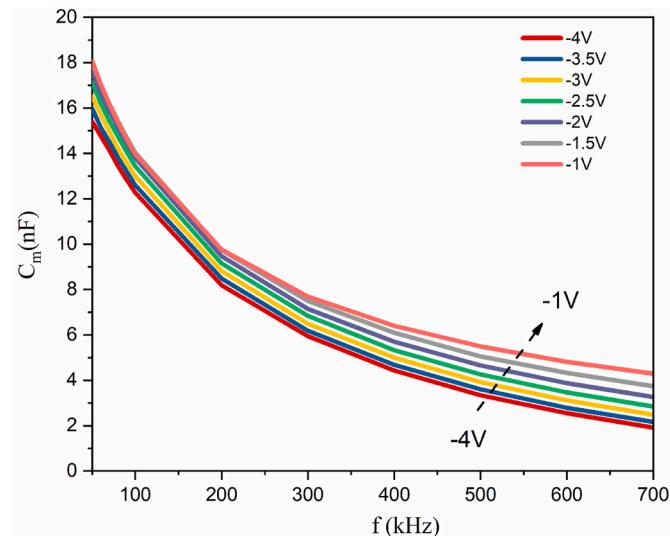


Fig. 9. Measured capacitance-frequency plots of the Al/TPA/p-Si/Al MIS-type capacitor at different voltages.

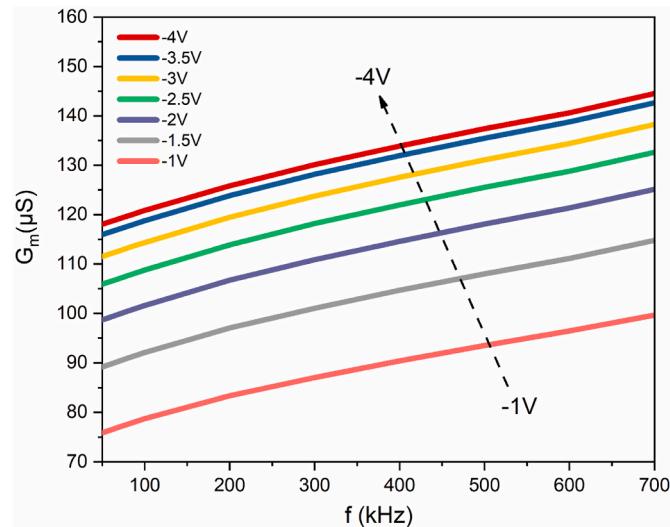


Fig. 10. Conductance-frequency plots of the Al/TPA/p-Si/Al MIS-type capacitor at different voltages.

their maximum between $-2.5V$ and $-3.5V$ at all frequencies, and as the frequency increased, these peaks shifted towards lower negative voltages. This behavior clearly confirms the existence of an interface states [54,57]. The voltage range where the maximum peak values in the G_c/ω graph (Fig. 14) were observed corresponds to the depletion region of the C_c-V graph shown in Fig. 13. This phenomenon is illustrated in Fig. 15.

The $C_c - V$ and G_c/ω plots of the fabricated capacitor at 700 kHz are shown in Fig. 15. The region where the rectified conductance of the diode peaks corresponded to the depletion region of the rectified capacitance, and this peak indicated the active interface trap density (D_{it}) [58]. The most common method used to calculate active interface trap density (D_{it}) is the equation derived by Hill-Coleman [59]. Using the depletion region in the corrected capacitance graph for each frequency and the data at the maximum points of the corrected conductance curve corresponding to this region, the values of the interfacial state density of the produced capacitor can be calculated with the following Eq. (4) [60].

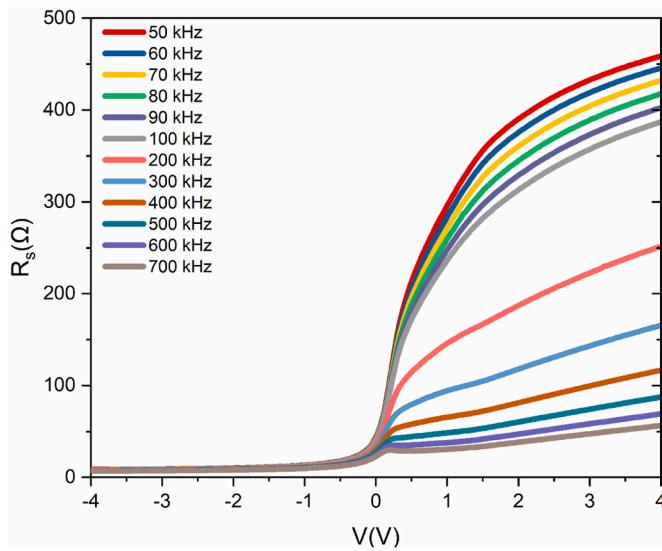


Fig. 11. Series resistance-voltage plots of the Al/TPA/p-Si/Al MIS-type capacitor at different frequency.

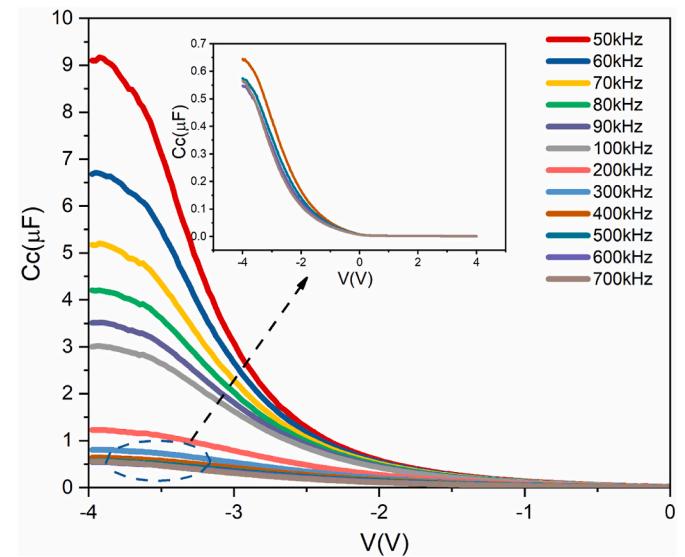


Fig. 13. Corrected capacitance-voltage plots of the Al/TPA/p-Si/Al MIS-type capacitor.

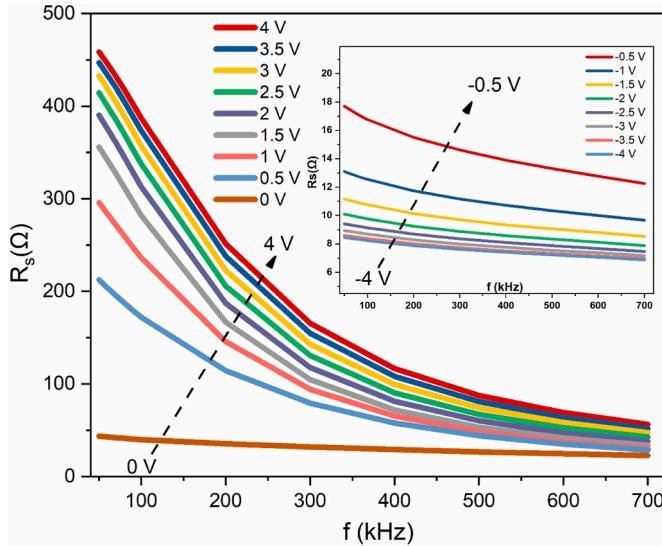


Fig. 12. Series resistance-frequency plots of the Al/TPA/p-Si Schottky diode at different voltages.

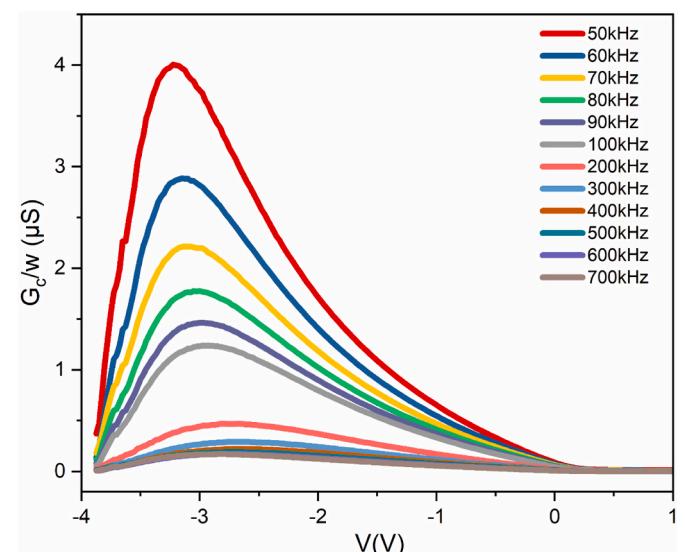


Fig. 14. Corrected conductance-voltage plots of the Al/TPA/p-Si/Al MIS-type capacitor.

Table 2

Electrical parameters of Al/TPA/p-Si/Al MIS-type capacitor determined from capacitive and conductive properties in the frequency range of 50 kHz–700 kHz.

f (kHz)	V_{peak} (V)	C_c (μF)	G_c/ω (μS)	R_s (Ω)	$D_{it} \times 10^{11}$ ($\text{eV}^{-1}\text{cm}^{-2}$)
50	-3.23	4.580	4.006	8.44	67.36
60	-3.15	3.389	2.887	8.40	49.81
70	-3.13	2.733	2.216	8.35	40.43
80	-3	2.032	1.777	8.31	30.43
90	-2.98	1.753	1.466	8.27	25.53
100	-2.98	1.579	1.240	8.23	23.20
200	-2.78	0.642	0.469	7.87	9.30
300	-2.63	0.389	0.292	7.61	5.43
400	-2.68	0.323	0.221	7.40	4.47
500	-2.7	0.285	0.189	7.21	3.87
600	-2.78	0.270	0.172	7.06	3.59
700	-2.8	0.256	0.170	6.88	3.23

$$D_{it} = \frac{2 \left(\frac{G_{c,max}}{\omega} \right)}{qA} \left[\left(\frac{G_{c,max}}{\omega C_i} \right)^2 + \left(1 - \frac{C_c}{C_i} \right)^2 \right]^{-1} \quad (4)$$

Here, C_i is the capacitance of the interface layer in the accumulation region and is calculated from the maximum value of the C_c curve. A is the area of the capacitor, $G_{c,max}$ is the maximum of the rectified $G_c/\omega - V$ curve, and C_c is the capacitance of the diodes corresponding to $G_{c,max}$. Using the voltage-dependent parameters given in Figs. 13 and 14 in Eq. (4), the active interface trap density of the capacitor at different frequencies was calculated and presented in Table 2 and shown in Fig. 16. Analyses of the results revealed that the obtained value was consistent with the findings in the literature [54,61].

As seen in Fig. 16 and in Table 2, D_{it} of the Al/TPA/p-Si/Al MIS-type capacitor decreased with increasing frequency. D_{it} decreased slowed down in high frequency regions. The obtained D_{it} values are 6.73×10^{12} and $3.23 \times 10^{11} \text{ eV}^{-1} \text{cm}^{-2}$ for 50 kHz and 700 kHz, respectively. The values obtained for D_{it} are similar to the values reported for the

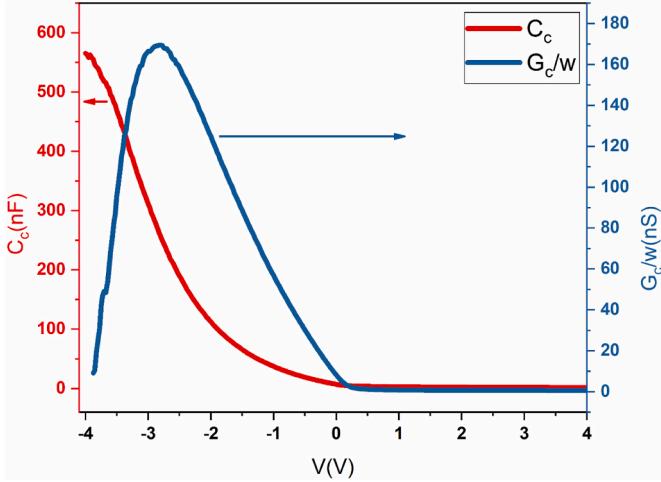


Fig. 15. Corrected capacitance and conductance-voltage curves of the capacitor at 700 kHz.

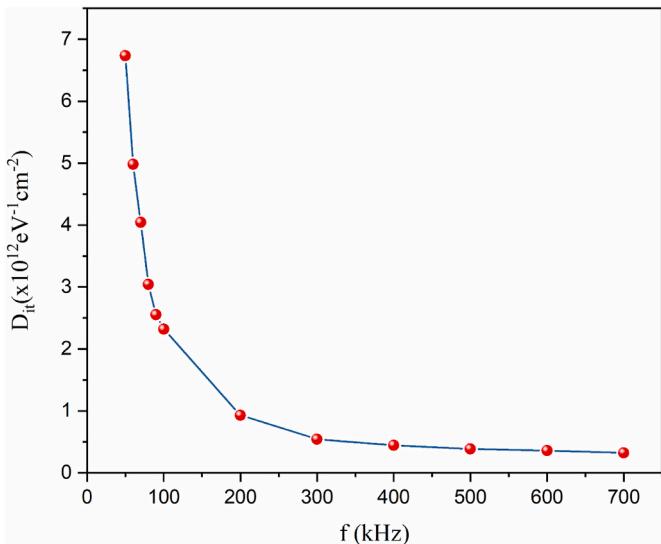


Fig. 16. The variation plot of D_{it} with frequency in Al/TPA/p-Si/Al MIS-type capacitor.

fabricated MIS-type structures in the literature [44,54,62–64]. Furthermore, the series resistance of a device is affected by the localized interface states in a frequency-dependent manner. At low frequencies, these states can increase the resistance by trapping charge carriers. As the frequency increases, the resistance generally decreases as the trapped states become less influential due to the rapid switching of the signal. A negative bias typically reduces the impact of these interface states on series resistance by modifying their energy levels or reducing their interaction with the carriers. This interplay is crucial in designing high-frequency electronic devices and optimizing their performance by managing interface states and understanding their impact on resistance.

Depending on the voltage, the low-high frequency capacitance approach can also be used to calculate the active interface trap density (D_{it}). The fabricated capacitor was subjected to this approach, and the active interface trap densities were computed using the following equation [65–67]:

$$D_{it} = \frac{1}{qA} \left[\left(\frac{1}{C_{LF}} - \frac{1}{C_i} \right)^{-1} - \left(\frac{1}{C_{HF}} - \frac{1}{C_i} \right)^{-1} \right] \quad (5)$$

Fig. 17 shows the variation of $D_{it} - V$, which was obtained from the

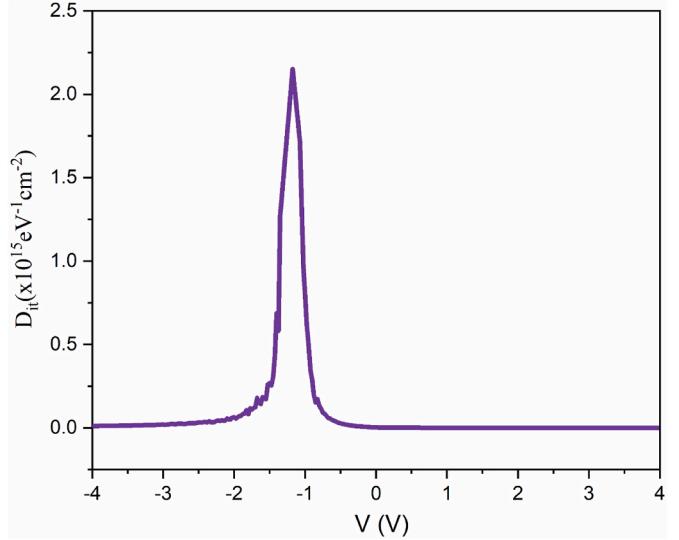


Fig. 17. The variation plot of D_{it} with voltage in Al/TPA/p-Si/Al MIS-type capacitor.

$C_{LF} - C_{HF}$ approach using low frequency (50 kHz) and high frequency (700 kHz). **Fig. 17** illustrates the continuous distribution of D_{it} , which remains constant between -4 V and -2 V, exhibits a peak at -1.18 V, subsequently decreases to 0 V, and eventually stabilizes at $+4$ V. The peak value of the N_{ss} is observed at approximately -1.18 V. This corresponds to a magnitude on the order of 10^{15} eV, a sufficiently high value to stabilize the Fermi level at the surface of the Si substrate [68].

Fig. 18 shows the inverse-bias $C_c^{-2} - V$ plots obtained from the $C_c - V$ plots of the Al/TPA/p-Si/Al MIS-type capacitor from 50 kHz to 700 kHz and in the dark. Based on the linearity of the $C_c^{-2} - V$ plot of the Al/TPA/p-Si/Al MIS-type capacitor, it is possible to estimate the trap densities.

The relation between capacitance and applied voltage for diodes is given by [62].

$$C^{-2} = \frac{2(V_{bi} + V)}{q\epsilon_s \epsilon_0 A^2 N_A} \quad (6)$$

where A , C , q , ϵ_s , ϵ_0 are the diode area, the depletion region capacitance, the electron charge, the semiconductor permeability ($\epsilon_s = 11.8$ for Si),

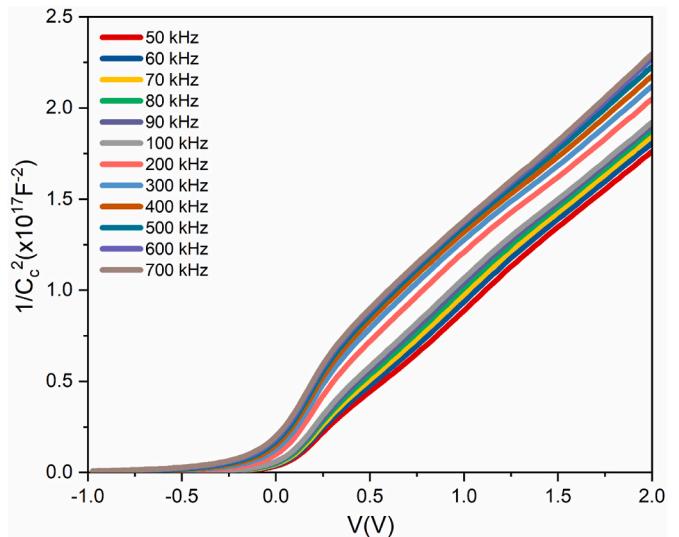


Fig. 18. C_c^{-2} -V plots for Al/TPA/p-Si Schottky diode.

and the vacuum permeability ($\epsilon_0 = 8.85 \times 10^{-12} \text{ F/m}$), respectively. The V_{bi} values were extracted from Eq. (6) by examining the linear relation between C_c^{-2} and V in the strong bias region in Fig. 18. V_{bi} is the diffusion potential at zero bias, which is determined from the extrapolation of the linear $C_c^{-2} - V$ plot to the V-axis were found to be in the range of 0.162–0.508 V for the frequency variations from 50 kHz to 700 kHz. N_A is acceptor concentration which is given as [54]

$$N_A = N_V \exp\left(\frac{qV_p}{kT}\right) \quad (7)$$

Where N_V is the density of states in the valence band for p-Si, V_p is the potential difference between the Fermi energy level (E_F) and the peak of the valence band of Si and is equal to E_F . This value can be calculated after obtaining the N_A carrier concentration via Eq. (7). The value of V_p is obtained from the following relation [42,54].

$$V_p = E_F = kT \ln\left(\frac{N_y}{N_A}\right) \quad (8)$$

The barrier height (Φ_B) of the diodes can be obtained using the following relation [55].

$$\Phi_B(C - V) = V_{bi} + V_p \quad (9)$$

By using the $C_c - V$ measurement, the barrier height and acceptor carrier concentration values for the Al/TPA/p-Si devices were extracted as 0.655 eV and $3.567 \times 10^{16} \text{ cm}^{-3}$ from linear region of its $C_c^{-2} - V$ characteristic for 700 kHz, respectively.

In addition, the depletion layer width (W_D) was determined from $C_c^{-2} - V$ plots by the following Eq. (10) [37,60].

$$W_D = \sqrt{\frac{2\epsilon_S \epsilon_0 V_{bi}}{qN_A}} \quad (10)$$

Various electrical parameters of the Al/TPA/p-Si diode such as is the diffusion potential (V_{bi}), doping concentration of acceptor atoms (N_A), Fermi energy level (E_F), depletion width (W_D) barrier height (Φ_B) were calculated from $C_c^{-2} - V$ plots and listed in Table 3 for different frequencies.

In Table 3, there have been an increase in the V_{bi} , E_F and Φ_B values depending on the increasing frequency. N_A and W_D values also increase with increasing frequency. As can be seen from the table, the interface states and series resistance of the diode affect its important electrical properties depending on the frequency.

4. Conclusions

This work examined a metal-interlayer/insulator-semiconductor (MIS) capacitor made of Al/TPA/p-Si. The spin coating technique was used to create an interlayer of TPA organic compound. The TPA molecule was examined using SEM and AFM techniques. The surface examination carried out using the AFM technique reveals that the surface morphology of the spin-coated TPA film layer is smooth, with uniform grain distribution. The forward and reverse bias capacitance-voltage ($C - V$) and conductance-voltage ($G - V$) characteristics of the Al/TPA/p-Si MIS-type capacitor were measured in the frequency range of 50 kHz–700 kHz and ± 4 V at room temperature. The observed capacitance and conductance were also adjusted to yield the actual MIS capacitance without the influence of series resistance. To examine the interface states distribution of the capacitor, the corrected conductance-voltage ($G - V$) and capacitance-voltage ($C - V$) characteristics at various frequencies were also computed. Following the required series resistance modifications, notable alterations in the capacitance have been noticed, particularly on the conductance curves. Furthermore, corrected $C_c^{-2} - V$ plots were used to determine the device's key parameters, including the barrier height (Φ_B), depletion width (W_D), Fermi energy level (E_F), acceptor doping concentration (N_A), and diffusion

Table 3

Values of different parameter parameters calculated from $C_c^{-2} - V$ plots of the Al/TPA/p-Si/Al MIS-type capacitor.

f (kHz)	V_{bi} (V)	$N_A \times 10^{16}$ (cm $^{-3}$)	E_F (eV)	$W_D \times 10^{-8}$ (cm)	Φ_B (eV)
50	0.162	4.122	0.143	2367	0.305
60	0.197	4.084	0.143	1939	0.341
70	0.226	4.048	0.144	1697	0.369
80	0.249	4.021	0.144	1538	0.393
90	0.267	4.002	0.144	1432	0.411
100	0.284	3.991	0.144	1346	0.428
200	0.390	3.916	0.144	982	0.534
300	0.425	3.842	0.145	900	0.570
400	0.447	3.777	0.145	856	0.593
500	0.470	3.686	0.146	815	0.616
600	0.483	3.626	0.146	793	0.629
700	0.508	3.567	0.147	753	0.655

potential (V_{bi}), as a function of frequency.

Electrical parameters of the capacitor at 700 kHz; the active interface trap density (D_{it}), series resistance (R_s), V_{bi} , N_A , E_F , W_D , and Φ_B are 3.23 $\times 10^{11} \text{ eV}^{-1} \text{ cm}^{-2}$, 6.885 Ω , 0.508 V, $3.567 \times 10^{16} \text{ cm}^{-3}$, 0.147 eV, $0.753 \times 10^{-3} \mu\text{m}$, 0.655 eV, respectively. When the Al/TPA/p-Si diode is compared with the literature, the R_s value of the diode is low, and the D_{it} value is like that of MOS diodes with organic interfaces. The active interface trap density of the diode is of the order of $10^{12} \text{ eV}^{-1} \text{ cm}^{-2}$, which is suitable for electronic device technology, indicating that it can also be used in electronic applications. In other words, the TPA organic compound can be used as an interfacial layer in MIS-based applications with the computed active interface trap density and barrier height.

Data availability

The datasets generated during and/or analyzed during the current study are available from the Corresponding author upon reasonable request.

Ethical approval

The authors maintain high standards of personal conduct, practicing honesty in all our professional relationships and endeavors.

CRediT authorship contribution statement

Selcuk Izmirli: Writing – original draft, Visualization, Investigation, Formal analysis. **Sukru Cavdar:** Writing – review & editing, Writing – original draft, Visualization, Supervision, Resources, Project administration, Investigation. **Pinar Oruc:** Writing – original draft, Investigation, Formal analysis. **Serkan Eymur:** Writing – original draft, Visualization, Resources. **Nihat Tuğluoğlu:** Writing – review & editing, Writing – original draft, Visualization, Investigation.

Declaration of competing interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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