

# Capacitance characterization and current transport mechanism of ZnSnN<sub>2</sub> heterojunctions

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## AFFILIATIONS

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## ABSTRACT

The trap and defect energy levels of ZnSnN<sub>2</sub> and the current transport mechanism of its heterojunctions are studied. A shallow energy level at 105 meV below the conduction band minimum ( $E_c$ ) of ZnSnN<sub>2</sub> is detected and its possible origin is the intrinsic antisite defect of Sn<sub>Zn</sub> (Sn occupy the position of Zn in ZnSnN<sub>2</sub>), besides the traps located at 0.67, 1.03 and 1.06 to 1.21 eV below  $E_c$ . The interface states of ZnSnN<sub>2</sub> heterojunctions form two discrete energy levels with one at  $E_c + 0.05$  eV and another at  $E_c - 0.03$  eV. The current of ZnSnN<sub>2</sub> heterojunctions is controlled by thermionic emission at relatively low bias voltage and limited by space charge at higher bias voltage. The barrier height of the heterojunctions is inhomogeneous, which obeys Gaussian distribution and possibly results from interface roughness.

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Developing solar cells based on Earth-abundant and eco-friendly semiconductors is essential in reducing the cost and expanding the application of photovoltaic (PV) technology.<sup>1–7</sup> ZnSnN<sub>2</sub> is considered as an eco-friendly solar cell absorber material that can meet terawatt-level energy demands at low cost.<sup>4–6,8–21</sup> Its advantages include a direct bandgap, high absorption coefficient ( $\sim 10^5$  cm<sup>-1</sup>), Earth-abundance of elements, non-toxicity of both the elements and the compound, and low cost. It belongs to the Zn-IV-N<sub>2</sub> (IV = Si, Ge, and Sn) family whose bandgap can match the solar spectrum.<sup>10–13</sup> ZnSnN<sub>2</sub> is also considered highly competent in photocatalytic and light emission device applications.<sup>22,23</sup>

Theoretical work shows that ZnSnN<sub>2</sub> is n-type conductive with electron density of  $10^{19}$  cm<sup>-3</sup> or even higher and with antisite defect of Sn<sub>Zn</sub> (Sn occupying the position of Zn) as the major donor.<sup>4,5</sup> In experiments, its electron density has been reduced<sup>11,24–28</sup> to around or below  $10^{16}$  cm<sup>-3</sup>. Devices of ZnSnN<sub>2</sub>, including heterojunctions (HJ), HJ solar cells, Schottky diode, and Schottky barrier solar cells, have been studied.<sup>6,29–32</sup>

However, much still remains unknown about ZnSnN<sub>2</sub> and its devices. The energy levels of defects and traps in ZnSnN<sub>2</sub> and the current transport mechanism of its HJ have not been studied. In this paper, ZnSnN<sub>2</sub> HJ, with Cu<sub>2</sub>O<sup>7,33–37</sup> as the p-type layer, is prepared with sputtering. The properties of ZnSnN<sub>2</sub> and the HJ are studied. The

defects and traps in ZnSnN<sub>2</sub> as well as the interface states and current transport mechanism of the HJ are revealed.

Both ZnSnN<sub>2</sub> and Cu<sub>2</sub>O were prepared with magnetron sputtering. The base pressure of the vacuum chamber was  $5.0 \times 10^{-4}$  Pa. To measure the properties of ZnSnN<sub>2</sub> and Cu<sub>2</sub>O, the substrates used were K9 glass and Si, which were ultrasonically cleaned in acetone, ethanol, and de-ionized water with the cleaning lasting for 15 min in each liquid. The rotational speed of the substrate holder was  $0.6\pi$  rad/s. Before film deposition, the target was sputtered for 5 min to clean the target surface. An alloy target of Zn and Sn (99.999%) was used to deposit ZnSnN<sub>2</sub> and the atomic ratio of Zn to Sn is 6:1. Argon gas (99.99%) and nitrogen gas (99.999%) were introduced into the chamber. The flowrate of argon was eight standard cubic centimeters per minute (sccm) and that of nitrogen was 5 sccm. The working pressure was 5 Pa and the substrate temperature was 100 °C. Radio frequency (RF) sputtering was used and the power was 30 W. The deposition time was 4 h. A circular Cu plate (99.999%) was used as the target to deposit Cu<sub>2</sub>O with direct current (DC) sputtering. The gases were Ar (99.99%) with a flow rate of 30 sccm and O<sub>2</sub> (99.999%) with a flow rate of 4 sccm. The working pressure was 0.6 Pa, the substrate temperature was 400 °C, and the deposition time was 1 h. The DC power was 50 W.

The fabrication of Cu<sub>2</sub>O/ZnSnN<sub>2</sub> pn HJ (glass/ITO/Cu<sub>2</sub>O/ZnSnN<sub>2</sub>/InSn) and the characterization methods of ZnSnN<sub>2</sub>

and Cu<sub>2</sub>O and the PV properties of the HJ are elaborated in the [supplementary material](#). In forward bias, Cu<sub>2</sub>O was connected to the positive electrode of the power source. The room temperature (RT) capacitance  $C$  vs voltage  $V$  (CV) curves of the devices at different frequencies were measured with an instrument (Victor, VC4092E LCR) equipped with a cryogenic platform (JANIS). The DC bias was from  $-1.00$  to  $1.00$  V and the alternative current (AC) excitation source was  $30$  mV. The drive level capacitance profile (DLCP) and admittance spectroscopy (AS) curves were also measured with the same instrument. For RT DLCP measurements, the DC bias was from  $-0.25$  to  $0$  V, the AC amplitude was from  $0.015$  to  $0.135$  V while the frequency was the same as that used for the RT CV curves. The zero-bias AS curves were measured from  $180$  to  $380$  K and the frequency was from  $100$  Hz to  $1$  MHz.

The dark temperature dependent IV or JV ( $I$  is the current and  $J$  is the current density) curves of the HJ were measured using a source measure unit instrument (Keithley 4200-SCS) and the temperature of the HJ was controlled by a constant temperature heating system (JF-956S, JFTOOLS, China).

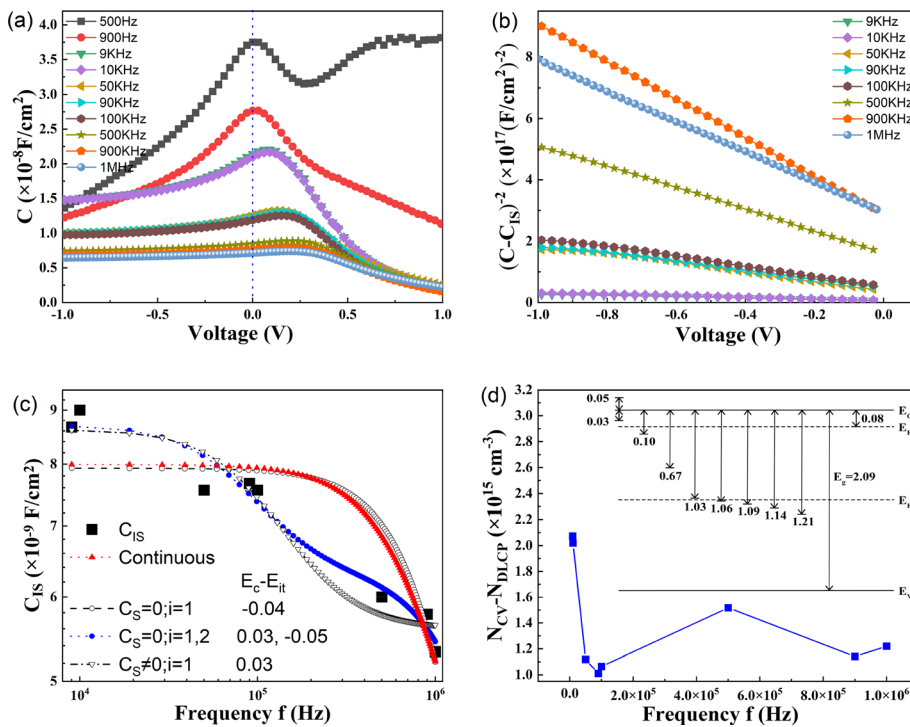
The characterization results of ZnSnN<sub>2</sub> and Cu<sub>2</sub>O and the PV properties of the HJ are presented in Figs. S1–S3 of the [supplementary material](#). Figure 1(a) shows the RT CV curves measured at different frequencies,  $f$ . These CV curves are equivalent to metal/n-type semiconductor Schottky junctions.<sup>38</sup> Besides series resistance, the reverse-biased HJ capacitance  $C$  also results from those induced by interface states ( $C_{IS}$ ), interface layer ( $C_{IL}$ ), and space charge ( $C_{SC}$ ).  $C_{SC}$  equals  $A_{sc}/(V_{bi}-V)^{1/2}$  where  $A_{sc}$  is a constant,  $V_{bi}$  is the built-in potential of the junction, and  $V$  is the applied voltage. The measured  $C$  here decreases with increasing frequency, mainly due to the decrease in response of the interface states with increasing frequency. In the

reverse-biased region,  $C$  is equivalent to  $C_{IL}$  in series with two parallel branches, one is  $C_{SC}$  and another is  $C_{IS}$ .<sup>38</sup>  $C^{-1}$  equals  $C_{IL}^{-1} + (C_{SC} + C_{IS})^{-1}$ . If the interface layer is thin enough,  $C_{IL}^{-1}$  is negligible and so

$$C = C_{SC} + C_{IS} = A_{sc}/(V_{bi} - V)^{1/2} + C_{IS}. \quad (1)$$

The  $C^{-2}$  vs  $V$  curves are shown in Fig. S4 of the [supplementary material](#), and it can be seen that they are not linear, implying that  $C_{IS}$  cannot be ignored.

With  $A_{sc}$ ,  $V_{bi}$ , and  $C_{IS}$  as the fitting parameters, all the CV curves are fitted to Eq. (1) and the fitting results are presented in Table S1 of the [supplementary material](#) (the curves obtained at  $900$  and  $500$  Hz cannot be fitted and will be analyzed later). The successful fitting implies that the interfacial layer is thin enough and so  $C_{IL}$  can be ignored.  $A_{sc}$  and  $C_{IS}$  are found to decrease while  $V_{bi}$  increases with increase in frequency, in agreement with other results.<sup>39</sup> The fitted  $C_{SC}^{-2}$  or  $(C - C_{IS})^{-2}$  vs  $V$  curves are shown in Fig. 1(b). After  $C_{IS}$  is removed, the  $C_{SC}^{-2}$  vs  $V$  curves become linear for  $1$ ,  $0.9$ , and  $0.5$  MHz due to the fact that deep levels respond less at higher frequencies. From the curve at  $1$  MHz, the built-in potential  $V_{bi}$  is  $0.59$  V and the barrier height  $\phi_b$  is calculated to be  $0.67$  V since the gap between the conduction band minimum  $E_c$  and the Fermi level  $E_f$  of ZnSnN<sub>2</sub> is  $0.08$  eV (see the [supplementary material](#)). The  $C_{SC}^{-2}$  vs  $V$  curves at  $100$ – $9$  kHz are still slightly curved (Fig. S5), implying the effect of deep levels or traps. The energy position and density of these deep levels are calculated<sup>39–41</sup> and listed in Table S2 of the [supplementary material](#). Traps at  $1.21$ ,  $1.14$ , and  $1.09$  eV below  $E_c$  are found. Deep energy levels can deteriorate the performance of solar cells by trapping carriers to reduce short circuit current.



**FIG. 1.** (a) CV curves; (b)  $C_{SC}^{-2}$  or  $(C - C_{IS})^{-2}$  vs  $V$  curves; (c)  $C_{IS}$  vs  $f$  data and the theoretical fitting; (d)  $N_{it}$  vs  $f$  [Inset: the energy levels of interface states and defects or traps, the unit is eV; the bandgap energy of ZnSnN<sub>2</sub> is from Fig. S1(e), [supplementary material](#)].

The fitted  $C_{IS}$  data with four fitting models are shown in Fig. 1(c). For interface states forming continuous energy levels,  $C_{IS}$  equals  $q^2 D_{it} (\omega \tau)^{-1} \arctg(\omega \tau)$ , where  $q$  is the unit charge,  $D_{it}$  and  $\tau$  are the density and time constant of interface states,  $\omega$  is the angular frequency ( $\omega$  equals  $2\pi f$ ).<sup>42</sup> A fitting to this model shows that  $D_{it}$  and  $\tau$  are  $4.99 \times 10^{10}$  states  $\cdot$  eV<sup>-1</sup>  $\cdot$  cm<sup>-2</sup> and  $2.40 \times 10^{-7}$  s. However, the overlap between  $C_{IS}$  and the fitting curve is poor, possibly implying this model fails here. For interface states forming discrete levels,  $C_{IS}$ <sup>42</sup> is expressed as follows:

$$C_{IS} = C_S + \sum_1^m C_{it-i} / [1 + (2\pi f \tau_i)^2], \quad (2)$$

where  $C_S$  is the semiconductor capacitance,  $m$  is the total number of discrete levels,  $i$  labels the  $i$ th level,  $\tau_i$  and  $C_{it-i}$  are the time constant and the capacitance of the  $i$ th level. With  $C_S = 0$ , the fitting results show that the curve with two discrete levels overlaps with the  $C_{IS}$ -f data much better than that with one level. If  $C_S$  is used as a fitting parameter, only the model of  $m = 1$  works. The fitting results are listed in Table S3 of the [supplementary material](#). The density of interface states  $D_{it-i}$  and the corresponding interface states  $E_{it-i}$  are also calculated and listed in Table S3 since  $D_{it-i}$  equals  $C_{it-i}/q^2$  while

$$\tau_i = f_i^{-1} = [v_{th} \sigma_n N_c]^{-1} \exp[(E_c - E_{it-i})/(kT)], \quad (3)$$

where  $f_i$  is the  $i$ th level frequency,  $v_{th}$  is the thermal velocity of electrons,  $\sigma_n$  is the capture cross section,  $N_c$  is the density of states [ $v_{th} \sigma_n N_c$  equals  $2.10 \times 10^6$  Hz as shown later; for n-type semiconductors, the measurement frequency is too high for holes to follow and so the interface states detected are around the conduction band but not the valence band in Eq. (3)].<sup>42</sup> From the model with  $C_S = 0$  and  $m = 2$ ,  $E_c - E_{it-1} = 0.03$  eV and this agrees with that obtained from  $C_S \neq 0$  and  $m = 1$ . The model with  $C_S = 0$  and  $m = 2$  also shows that another energy level is 0.05 eV above  $E_c$ . This energy level needs further work though energy levels of impurities,<sup>43</sup> defects,<sup>44</sup> or interface states<sup>45</sup> above  $E_c$  are found in other semiconductors. Interface states are also harmful since they lead to interface recombination, which reduces the open circuit voltage.

The CV curves at 900 and 500 Hz cannot be fitted to Eq. (1) and the corresponding  $C^{-2}$ -V curves are shown in Fig. S6 of the [supplementary material](#). These  $C^{-2}$ -V curves show concave-up curvature and can be divided into three linear sections due to interface states and deep levels. If we suppose that the interface states are in equilibrium with ZnSnN<sub>2</sub>,  $N_D$  and  $V_{bi}$  of the first section can be calculated from the slope and horizontal intercept of section 1, while from section 2 and 3 the energy levels and the density of traps can be calculated (Fig. S6).<sup>46,47</sup> From the curve measured at 500 Hz, two trap energy levels at  $E_c - E_{DL} = 0.67$  and 1.03 eV are found, while from that at 900 Hz  $E_c - E_{DL} = 0.67$  and 1.06 eV are obtained. The deep energy levels and interface states are shown in the inset of Fig. 1(d).

From the CV curves, the depth profile of charge density,  $N_{CV}$ , which results from free carrier density as well as bulk and interfacial defect density can be obtained, while from drive level capacitance (DLC) the depth profile of the charge density,  $N_{DLC}$ , which only results from the former two, can be obtained (Fig. S7 of the [supplementary material](#)).<sup>48</sup> The zero-bias difference between  $N_{CV}$  and  $N_{DLC}$  at each frequency is exactly the total interface defect density  $N_{it}$  and is shown in Fig. 1(d).  $N_{it}$  varies with the frequency and is found to be around  $1.5 \times 10^{15}$  cm<sup>-3</sup>. The possible peak at 0.5 MHz implies the

interface states energy level is 0.04 eV below  $E_c$  according to Eq. (3), roughly in agreement with Fig. 1(c). Below 90 KHz,  $N_{it}$  increases with a decrease in  $f$  in the measurement range. If we suppose that the minimum measurement frequency of 9 KHz is just the peak of  $N_{it}$ , the corresponding energy level of the interface states will be 0.14 eV below  $E_c$  according to Eq. (3). If the  $N_{it}$  peak is below 9 KHz, the corresponding energy level will be much deeper than 0.14 eV.

Figure 2(a) shows the capacitance  $C$  vs  $f$  obtained from AS. A step is seen in the curves and this suggests a discrete defect level is detected.<sup>6,49</sup> To obtain the activation energy  $E_a$  of this defect, the  $-\omega dC/d\omega$  vs  $\omega$  curves at each temperature are calculated, and from these curves, the angular frequency  $\omega_0$  corresponding to the maximum of the curve can be obtained. The relation between  $\omega_0$  and the activation energy  $E_a$  is that  $\omega_0$  equals  $2\pi\nu_0 T^2 \exp[-E_a/(kT)]$  or  $2\pi f_0$ , where  $\nu_0$  is the pre-exponential factor independent of temperature. Figure 2(b) shows the  $\omega_0/T^2$  vs  $1000/T$  curve.  $\nu_0$  is calculated to be 23.34 Hz/K<sup>2</sup> and so  $v_{th} \sigma_n N_c$  equals  $2.10 \times 10^6$  Hz according to Eq. (3).  $E_a$  is found to be 105 meV (or 0.10 eV). The integrated total density based on Gaussian distribution,  $N_b$ , is  $1.66 \times 10^{16}$  cm<sup>-3</sup> [Fig. 2(c)].

There are three possible origins for this energy level: from Cu<sub>2</sub>O, the interface states, or ZnSnN<sub>2</sub>. It is known that the preparation methods have an influence on  $E_a$  of defects. In Ref. 35, the  $E_a$  values for Cu<sub>2</sub>O deposited at 600 and 1070 K are 0.23 and 0.19 eV. The preparation methods of our Cu<sub>2</sub>O are very similar to that except that the substrate temperature used is 673 K here. The  $E_a$  of Cu<sub>2</sub>O here should be between 0.23 and 0.19 eV, larger than 105 meV. This level also cannot result from interface states since the energy levels of interface states are at  $E_c - 0.03$  eV or  $E_c + 0.05$  eV in Fig. 1(c) while one around  $E_c - 0.04$  eV and another deeper than  $E_c - 0.14$  eV are estimated in Fig. 1(d). Therefore, this level can only come from ZnSnN<sub>2</sub> and the possible origin is the antisite defect of Sn<sub>Zn</sub> since in ZnSnN<sub>2</sub>, Sn<sub>Zn</sub> is the major donor having the lowest formation energy.

Figure 2(d) shows the experimental and fitted Nyquist spectra of the HJ and the inset shows the equivalent circuit used for fitting.<sup>6</sup> The HJ can be fitted with one resistance  $R_s$  in series with two parallel branches: one is the parallel resistance  $R_p$  and another is the constant phase element (CPE, which includes two parameters: CPE-P and CPE-T—CPE-P is within 0–1, CPE-P = 1 implies that it is a pure capacitor while CPE-P = 0 implies it is a pure resistance). The fitted  $R_s$ ,  $R_p$ , CPE-P, and CPE-T are 30.18  $\Omega$ , 3199  $\Omega$ , 0.81, and  $1.24 \times 10^{-8}$ , respectively. The time constant of the CPE, which equals  $[R_p \times (CPE - T)]^{1/(CPE - P)}$ , is calculated to be  $3.97 \times 10^{-6}$  s. This is larger than that of the interface states (Table S3), suggesting that interface states have a weak influence on the current transport. The existence of CPE also implies interface roughness.

The dark JV curves at different temperatures are shown in Fig. 3(a). As the temperature increases,  $J$  increases. In the forward double log JV curves [Fig. 3(b)], the slope of the curves at lower bias (<1 V) is close to 1, indicating Ohmic conduction and the current is controlled by the junction while the slope approaches 2 under high bias voltage, indicating that space charge limited current (SCLC) gradually becomes dominant.

The possible transport models of HJ<sup>50–53</sup> currents include tunneling, diffusion, generation/recombination in the space charge region, and thermionic emission (TE). In these models, the forward bias current density  $J$  equals  $J_s \{\exp[A(T)V] - 1\}$ , where  $J_s$  is the saturation

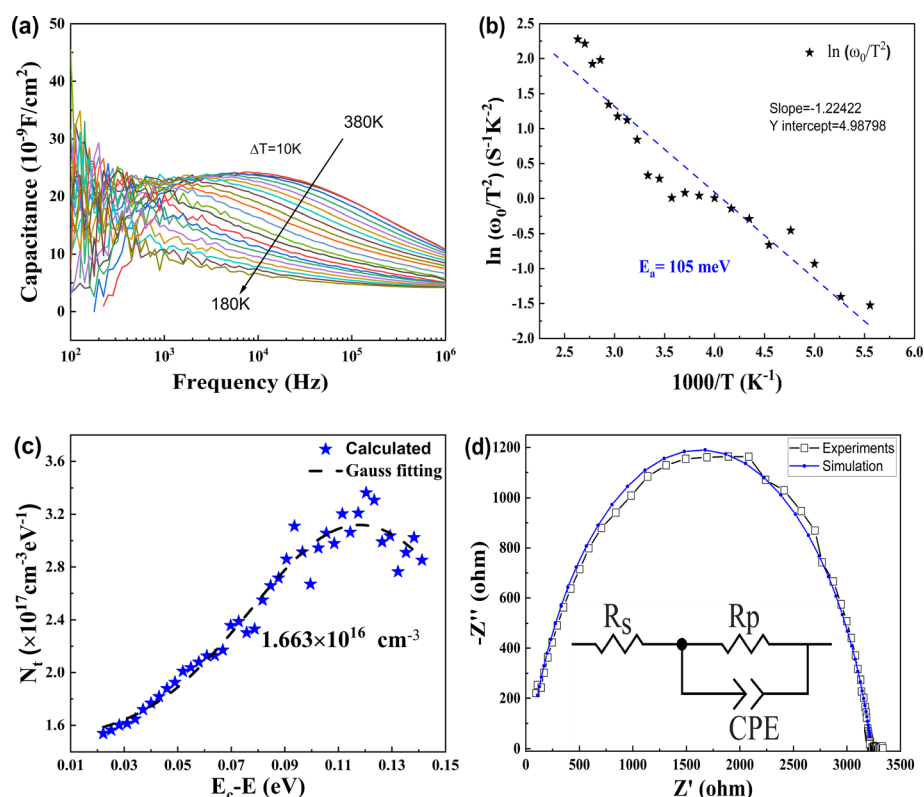


FIG. 2. (a) The admittance spectra of the HJ. (b) The Arrhenius plot of  $\ln(\omega_0/T^2)$  vs  $1000/T$ . (c) The integrated density of the defect or trap. (d) The impedance spectrum of the HJ.

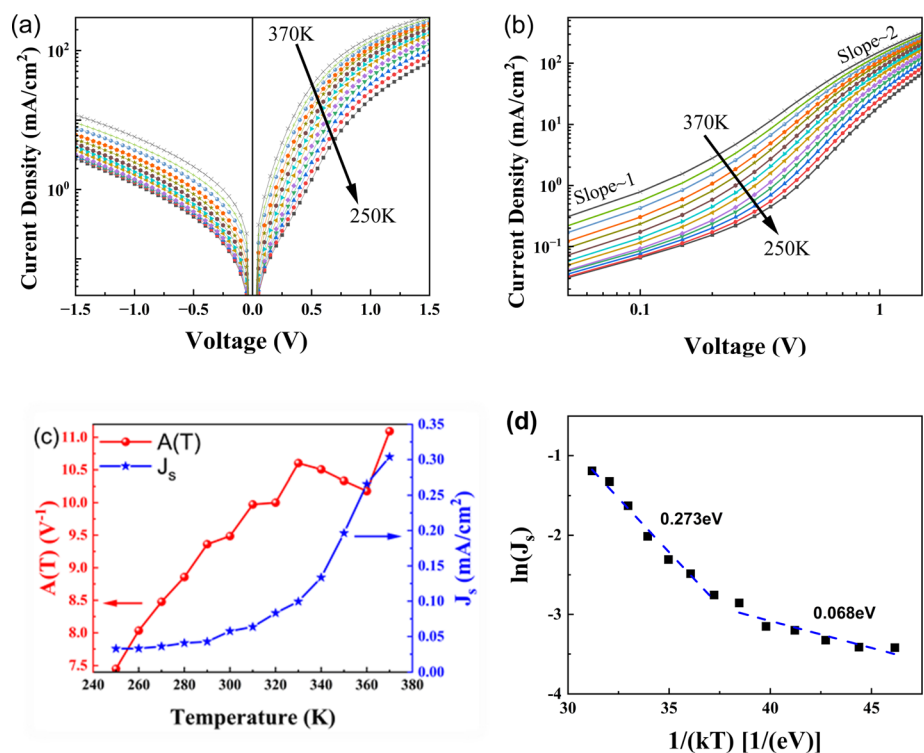


FIG. 3. (a)  $J \sim V$  curves at 250–370 K. (b) Forward bias logarithmic  $J \sim \logarithmic V$  curves. (c)  $A(T)$  and  $J_s$  vs  $T$ . (d)  $\ln(J_s)$  vs  $1/(kT)$ .



current density,  $A(T)$  is the exponential factor. When the applied voltage is over  $3kT/q$ ,  $J$  just equals  $J_s \exp[A(T)V]$ .  $J_s$  and  $A(T)$  can then be obtained from the linear region of the  $\ln J$  vs  $V$  plot, as shown in Fig. 3(c). It is seen that  $A(T)$  is not a constant in the whole temperature range and this excludes the possibility of tunneling. In addition, if the current is due to diffusion or generation/recombination in the space charge region, the activation energy from the  $\ln J_s$  vs  $1/(kT)$  curve would be the bandgap or half the bandgap. However, the activation energy from the  $\ln J_s$  vs  $1/(kT)$  curves is just about 0.273/0.068 eV [Fig. 3(d)], far smaller than that of  $\text{Cu}_2\text{O}$  or  $\text{ZnSnN}_2$ . Therefore, the current at low bias is controlled by TE.

According to the TE model, the JV relation is given by

$$J = J_s \{ \exp[q(V - J R_s)/(nkT)] - 1 \} + J_{sh}, \quad (4)$$

where

$$J_s = A^{**} T^2 \exp(-q\phi_b/kT). \quad (5)$$

$J_{sh}$  equals  $(V - IR_s)/R_{sh}$ ,  $R_s$  is the resistance in series with the diode,  $R_{sh}$  is a resistance in parallel to  $R_s$  and the diode,  $A^{**}$  is the effective Richardson coefficient [ $A^{**}$  of  $\text{ZnSnN}_2$  is 14.40 (Ref. 29)  $\text{A} \cdot \text{cm}^{-2} \cdot \text{K}^{-2}$ ],  $n$  is the ideality factor,  $\phi_b$  is the barrier height, and the other symbols are consistent with the previous equations. With  $\phi_b$ ,  $n$ ,  $R_s$ , and  $R_{sh}$  as the fitting parameters, the current density in the junction-controlled region (below 1 V) can be fitted to Eq. (4) and the fitting results are presented in Figs. 4(a) and 4(b). The barrier height  $\phi_b$  increases with temperature increase while the ideality factor  $n$  is over 1 and decreases with temperature increase, implying barrier height inhomogeneity (BHI).<sup>54–56</sup> The series resistance  $R_s$  decreases with temperature decrease due to carrier freezing-out while  $R_{sh}$  increases with temperature increase.<sup>50</sup>

From Eq. (5), the following equation can be obtained:

$$\ln(J_s/T^2) = \ln(A^{**}) - q\phi_b/(kT). \quad (6)$$

Equation (6) shows that  $\ln(J_s/T^2)$  vs  $q/(kT)$  curves (the Richardson plot) will be linear. In the Richardson plot in Fig. 4(c), the linearity in the whole temperature range is not good.  $\phi_b$  and  $A^{**}$  are 0.12/0.26 V and  $3.69 \times 10^{-8}/1.28 \times 10^{-5} \text{ A} \cdot \text{cm}^{-2} \cdot \text{K}^{-2}$  in the low/high temperature range. The obtained  $A^{**}$  is much smaller than the theoretical value, also suggesting possible BHI.<sup>54–56</sup> With the presence of BHI, the relations between  $\phi_b$ ,  $n$ , and  $T$  are as follows:<sup>54,55</sup>

$$\phi_b = \overline{\phi_{b0}} - q\sigma_0^2/(2kT), \quad (7)$$

$$n^{-1} - 1 = -\rho_2 + q\rho_3/(2kT), \quad (8)$$

where  $\overline{\phi_{b0}}$  and  $\sigma_0$  are standard barrier height and deviation at zero-bias while  $\rho_2$  and  $\rho_3$  are two coefficients. Figure 4(d) shows the plots of  $\phi_b$  and  $n^{-1} - 1$  vs  $q/(2kT)$ . From Eq. (7),  $\overline{\phi_{b0}}$  and  $\sigma_0$  are obtained to be 1.05 and 0.15 V while from Eq. (8),  $\rho_2$  and  $\rho_3$  are 0.44 and  $-0.01$  V. Since  $\phi_b$  is related to temperature according to Eq. (7), Eq. (6) can now be turned into the modified Richardson relationship given by

$$\ln(J_s/T^2) - q^2\sigma_0^2/(2k^2T^2) = \ln(A^{**}) - q\overline{\phi_{b0}}/(kT). \quad (9)$$

Equation (9) shows that  $\ln(J_s/T^2) - q^2\sigma_0^2/(2k^2T^2)$  vs  $q/(kT)$  will be linear. The modified Richardson plot is presented in Fig. 4(c) and good linearity is observed in the whole temperature range. The calculated  $\overline{\phi_{b0}}$  is 1.05 V, in agreement with that obtained from Fig. 4(d) while  $A^{**}$  is  $14.41 \text{ A} \cdot \text{cm}^{-2} \cdot \text{K}^{-2}$ , very close to the theoretical data. Therefore, the temperature dependence of barrier height can be explained by BHI, which obeys the Gaussian distribution and possibly results from interface roughness. With the presence of BHI, the open circuit voltage will be much smaller than the barrier height extracted from IV curves and factors resulting in BHI should try to be avoided during HJ preparation.

In summary,  $\text{ZnSnN}_2$  HJ was prepared and studied. CV curves show that the capacitance is affected by interface states and trap energy levels. The interface states form two discrete energy levels with one

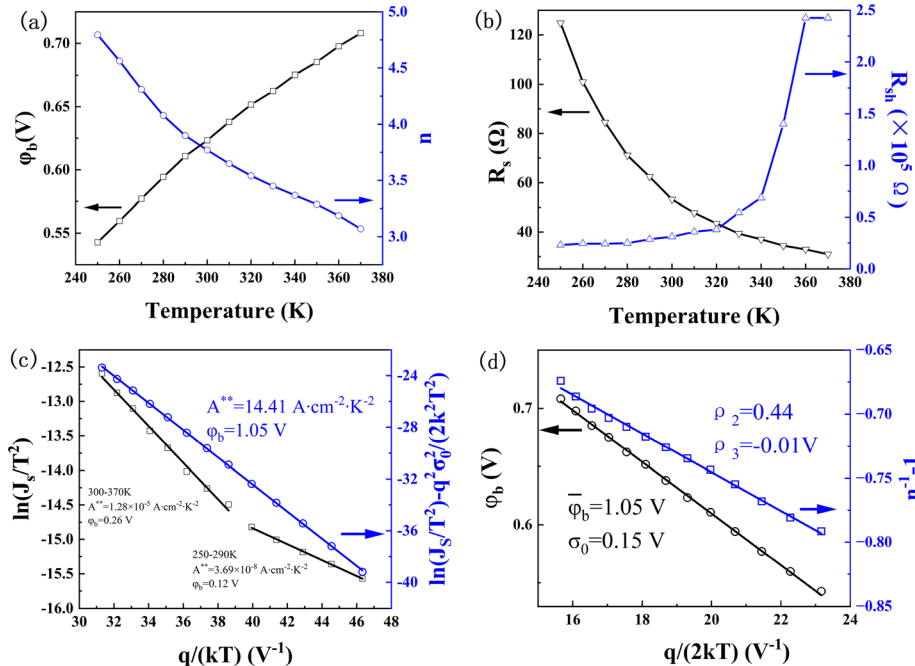


FIG. 4. (a)  $\phi_b$  and  $n$  vs temperature. (b)  $R_s$  and  $R_{sh}$  vs temperature. (c) The Richardson plot and the modified one. (d)  $\phi_b$  and  $n^{-1} - 1$  vs  $q/(2kT)$  plot.

below and one above  $E_c$  of  $\text{ZnSnN}_2$ . The former is also confirmed by the zero-bias difference of the charge profile. The deep energy levels are located at 0.67, 1.03 and 1.06 to 1.21 eV below  $E_c$  of  $\text{ZnSnN}_2$ . A shallow energy level at 105 meV below  $E_c$  is detected and the possible origin is the intrinsic antisite defect of  $\text{Sn}_{\text{Zn}}$ . JV curves show that the dominant transport mechanism is TE at lower voltage. The extracted barrier height is inhomogeneous and the possible origin is interface roughness as revealed by impedance spectra.

See the [supplementary material](#) for the following content: Methods about preparing the  $\text{Cu}_2\text{O}/\text{ZnSnN}_2$  heterojunctions; Characterization methods of  $\text{ZnSnN}_2$  and  $\text{Cu}_2\text{O}$ ; Characterizations of the photovoltaic properties of the HJ; The XRD spectra, Raman scattering spectra and Tauc plots of  $\text{ZnSnN}_2$  and  $\text{Cu}_2\text{O}$ ; SEM images and Hall effect measurements; The illuminated JV curve, EQE spectrum and  $[E \times \ln(1 - \text{EQE})]^2$  vs  $E$  plot of the HJ; The reverse-biased  $C^{-2}$ - $V$  curves; The fitting results based on  $C = C_{\text{SC}} + C_{\text{IS}}$ ; The fitted  $C_{\text{sc}}^{-2}$  or  $(C - C_{\text{IS}})^{-2}$  vs  $V$  curves for 100, 90, 50, 10, and 9 KHz and the calculation about the energy levels and the density; The fitting results based on Eq. (2); The  $C^{-2}$  vs  $V$  curves for 900 and 500 Hz and the calculation about the energy levels and the density; The calculation about  $N_{\text{CV}}$  and  $N_{\text{DLCF}}$ .

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## AUTHOR DECLARATIONS

### Conflict of Interest

The authors have no conflicts to disclose.

## Author Contributions

**Fan Ye:** Conceptualization (equal); Data curation (equal); Formal analysis (equal); Funding acquisition (equal); Investigation (equal); Methodology (equal); Project administration (equal); Resources (equal); Software (equal); Supervision (equal); Validation (equal); Visualization (equal); Writing – original draft (equal); Writing – review & editing (equal). **Zi-Cheng Zhao:** Data curation (equal); Formal analysis (equal); Investigation (equal); Methodology (equal); Software (equal); Writing – original draft (supporting). **Cang-Shuang He:** Formal analysis (supporting); Visualization (supporting). **Jian-Lin Liang:** Data curation (supporting); Formal analysis (supporting). **Qian Gao:** Data curation (supporting); Visualization (supporting). **Yi-Zhu Xie:** Resources (supporting). **Dong-Ping Zhang:** Resources (supporting). **Xing-Min Cai:** Conceptualization (equal); Data curation (equal); Formal analysis (equal); Investigation (equal); Writing – original draft (equal); Writing – review & editing (equal).

## DATA AVAILABILITY

The data that support the findings of this study are available from the corresponding author upon reasonable request.

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