

Quasi-open circuit response times of single- and multi-junction InGaAs photonic power converters

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ABSTRACT

Photonic power converters provide an emerging alternative to traditional metallic cabling due to their immunity to electromagnetic interference and for minimizing fire hazards as well as their ability to wirelessly power loads. In this work, we study the electrical response as a function of laser power in single and multi-junction In_{0.53}Ga_{0.47}As-based structures using current-voltage and impedance spectroscopy techniques. The resistance, capacitance, and quasi-open-circuit response times are discussed. Estimated values for single- and ten-junction devices were 314 and 911 ns, respectively, measured under 1520 nm laser radiation with an irradiance of 88 mW cm⁻². A methodology with analytical modeling is proposed for comparing single- and multi-junction devices based on the average sub-cell performance. Our analysis provides useful information for the optimization of multi-junction photonic power converters in applications where the device response time is relevant such as simultaneous power and data transfer applications.

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I. INTRODUCTION

The extensive implementation of communication devices, sensors, and smart technology has created a demand for reliable power sources in electrically isolated and extreme environments. Traditional copper cabling, sensitive to electromagnetic interference and posing risks of heating and sparking, can be replaced by photonic power transmission through optical fibers which can also be beneficial for galvanic isolation.¹ This method is immune to electromagnetic interference and minimizes hazards in challenging conditions. The process involves converting electrical power to monochromatic light using, e.g., a laser, transmitting it through optical fibers or over free-space, and converting it back to electrical power using a photonic power converter (PPC).^{2,3} Similar to solar cells, the PPC utilizes the photovoltaic effect to convert this laser light into electricity. However, the monochromatic nature of the light source allows to match bandgap energy of the absorber material and photon energy and, thus, minimize thermalization and transmission

losses, resulting in much higher photovoltaic conversion efficiencies than conventional solar cells.^{4–6} To achieve output voltages at the receiver side sufficient to power the downstream electronics without the need of step-up voltage converters, several *p-n* junctions can be vertically stacked and connected electrically in series by tunnel diodes using principles derived from multi-junction (MJ) solar cell technology.^{7–9}

Unlike MJ solar cells, MJ PPCs have absorber layers with similar or equivalent bandgap energy (E_g) in each sub-cell.³ The absorber layer thicknesses are carefully designed for each sub-cell to generate the same photocurrent for operation in current-matched conditions. State-of-the-art PPC devices composed of III–V semiconductors have demonstrated monochromatic power conversion efficiencies (PCEs) approaching 70%.¹⁰ Single-junction GaAs devices have demonstrated a record efficiency of 68.9% leveraging optical resonance effects.¹¹ GaAs-based PPCs have also shown efficiencies well above 50% even at high irradiances of 500 W cm⁻² (0.5 mm diameter devices)¹² and input powers above 60 W (1 cm² devices).¹³

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Moreover, PPCs designed for operations in the telecom range starting from the O-band near 1310 nm and ideally closer to 1550 nm could enable long-distance transmission up to tens of km due to minimal fiber attenuation. These longer wavelengths, however, require smaller bandgap materials such as $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ (InGaAs in the remainder of this manuscript) or InGaAsP lattice matched to InP.^{14–16}

Due to the advantages associated with optical power transmission, there is an increasing interest in utilizing existing telecommunication optical networks for the dual purpose of simultaneous power and data transmission.¹⁷ This concept holds significant potential for transforming systems where both power delivery and communication capabilities are essential, such as in sensor networks and medical instrumentation.

The electrical characterization of PPCs is usually done in the direct current (DC) mode, in which the device electrical response is measured in steady-state conditions.¹⁸ Typically, current density–voltage (J – V) measurements under nominal illumination conditions are used to assess the PCE and related device performance parameters. Although effective for DC operation, this approach is not suited for studying the characteristic response times of the samples, which are relevant for data transmission purposes.

Understanding the time-dependent characteristics of the device's electrical response requires transient and/or alternating current (AC) mode methods. In the latter case, impedance spectroscopy^{19,20} is an effective technique which provides insights not only on the resistive energy dissipation which influences efficiency but also on the

capacitive energy storage kinetics. The resistive–capacitive coupling defines the transport properties for data transmission in PPC applications. Specifically, impedance spectroscopy measurements can be performed in several conditions to assess the corresponding response time τ behavior of the PPCs.²¹

In this article, DC and AC approaches are integrated to investigate fundamental device characteristics of PPCs featuring single-junction (1 J) and ten-junction (10 J) structures. Key methodologies applied encompass steady-state J – V and impedance spectroscopy spectra in the dark and under increasing laser irradiances. The conducted experiments were systematically scrutinized using the parametric Shockley equation²² and equivalent circuit models.²³ Estimations of the characteristic response times are reported for relevant operational conditions.

II. MODELING AND EXPERIMENTAL

A. PPC models

The illustrations in Fig. 1(a) depict the simplified structures of a single-junction (1 J) and a ten-junction (10 J) PPC. For the MJ device, each sub-cell is represented as a p^+n junction realized with the given absorber material. Tunnel diodes interconnect each sub-cell and are assumed to have negligible ohmic conductivity and high optical transparency; these structures have recently been made transparent for PPCs, targeting 1550 nm laser wavelengths using higher bandgap materials.²⁴

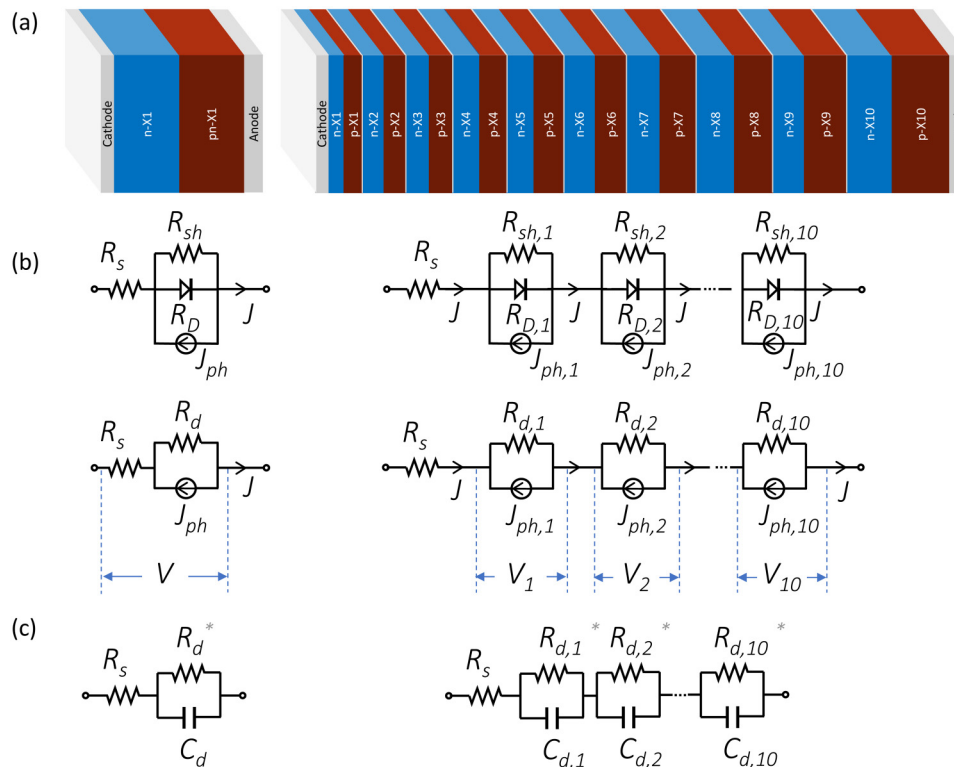


FIG. 1. Schemed device (a) layer structure, and equivalent circuits in (b) direct and (c) alternating current modes for the single-junction (left), and ten-junction (right) samples. In (b), the same circuit is represented above and below, separating shunt R_{sh} and diode R_D resistances and representing the equivalent device resistance R_d , respectively. In (c), the gray asterisk indicates that the device resistance is only the same in DC and AC, if no series connection of resistive–capacitive branches is present. All equivalent circuits neglected tunnel junctions.

Figure 1(b) illustrates simplified DC equivalent circuits for 1 and 10 J devices. Regardless of the configuration, the parasitic series resistance (R_s) accounts for ohmic voltage losses, but depending on the junction, the i th sub-cell can be simulated with a parallel connection of parasitic shunt resistance ($R_{sh,i}$), voltage-dependent diode recombination nonlinear resistance ($R_{D,i}$), and photocurrent ($J_{ph,i}$). The same circuit can also be represented [shown below in Fig. 1(b)] using the equivalent voltage-dependent nonlinear device resistance $R_{d,i}$ which accounts for the parallel connection of shunt and diode resistances, $R_{d,i} \approx R_{sh,i}R_{D,i}/(R_{sh,i} + R_{D,i})$. For operating conditions where the diode resistance is much smaller than the shunt resistance ($R_{D,i} \ll R_{sh,i}$), this simplifies to $R_{d,i} \approx R_{D,i}$. This more “condensed” form of the DC equivalent circuit is introduced as a visual bridge between the traditional representation (with the diode symbol emphasizing current flow) and the resistor-element-focused AC circuit.

Figure 1(c) presents simplified AC equivalent circuits for 1 and 10 J junction devices in terms of linear equivalent circuit elements. For instance, the i th sub-cell can be simulated with a parallel connection of a device resistance ($R_{d,i}$) and capacitance ($C_{d,i}$). Even though the equivalent circuits of Fig. 1(c) are arguably oversimplifications of more complex models with multiple resistive, capacitive, and even inductive contributions, they describe the fundamental device time response, i.e., the i th sub-cell will respond to a small perturbation with a characteristic effective relaxation time $\tau_i = R_{d,i}C_{d,i}$ as long as resistances and capacitances can be effectively defined per sub-cell. The applicability of this model may be limited in real devices, where capacitive elements often behave as constant-phase elements and resistive terms may include leakage pathways. In such cases, the system exhibits a distribution of time constants, making the parallel RC representation only a lumped-element approximation, and the extracted τ may not directly correspond to a single physical process.²⁵ Notably, an asterisk is used for the $R_{d,i}$ elements in the equivalent circuit of Fig. 1(c). This notation emphasizes two points: first, the transition of the device resistance from a nonlinear to a linear form under the tested DC operating point with small perturbations, in contrast to the representations in Fig. 1(b); and second, that DC and AC resistances may differ due to capacitive or inductive circuit branches, which do not contribute to the low-frequency limit in the DC regime.

B. PPC fabrication

Monolithic 1 and 10 J PPCs based on InGaAs absorber material with a bandgap of $E_g = 0.736$ eV were fabricated at Fraunhofer ISE.^{26,27} The devices were grown lattice matched on 4-in. InP wafers by metal-organic vapor phase epitaxy (MOVPE) with standard precursor and reactor conditions.²⁸ Each individual sub-cell consists of an absorber layer sandwiched between high-bandgap barrier layers providing front (FSF) and back surface fields (BSF) for improved carrier collection efficiency. The absorption coefficient of the absorber material was reported elsewhere.^{27,29}

The 1 J PPC features an absorber thickness of 540 nm. This structure was intended as a test structure for absorption calibration and is by design only partially absorbing. From an optical simulation of the PPC performed with the transfer matrix

method,³⁰ the absorptance at 1520 nm is 36%. In the 10 J case, the absorber thicknesses of the individual sub-cells were optimized to achieve current-matching conditions and an estimated absorption close to 90% at 1520 nm. Further details on the design can be found elsewhere.²⁷

The epi-wafers were processed into 5.4-mm² cells (designated area, i.e., chip area minus busbars) with front and back side metalization realized with metal evaporation, photolithography, and wet chemical etching. An anti-reflective coating was designed and deposited to optimize absorption for wavelengths around 1520 nm.

C. PPC characterization

The dark $J - V$ curves were measured using an Agilent 4155C semiconductor parameter analyzer using low noise tri-axial cables with a ground floor of <100 fA. For the $C - V$ and impedance spectroscopy spectra measurements, an Agilent 4284A precision LCR meter was employed, applying an AC perturbation of 10 mV at a frequency of 10 kHz.

The light $J - V$ and $C - V$ curves under increasing irradiances were measured on a temperature-controlled stage held at 25 °C. The DC measurements were recorded with a Keithley 2400 source meter in a four-wire configuration and for the impedance spectroscopy spectra, an E4980A precision LCR meter from Keysight was employed with AC perturbations from 30 to 50 mV in the frequency range from 20 Hz to 2 MHz.

III. RESULTS AND DISCUSSION

A. Dark current-voltage characteristic

The electrical response is modeled using the DC equivalent circuits of Fig. 1(b). Assuming negligible series resistances ($R_s \approx 0$), shunt resistance can be considered with the empirical form of Shockley's equation²² to express the dark current throughout a single-junction device as

$$J = J_0 \left(\exp \left[\frac{qV}{mk_B T} \right] - 1 \right) + \frac{V}{R_{sh}}, \quad (1)$$

where k_B is the Boltzmann constant; T the temperature; and m and J_0 are the ideality factor and the diode saturation current density, respectively. We consider sub-cells with similar junction characteristics, i.e., all equivalent sub-cell resistances of the 10 J device under investigation are the same ($\forall R_{d,i}, R_{d,i} = R_{d,i+1}$). In this case, the external voltage V evenly distributes among N sub-cells, so that each junction operates at the same voltage,

$$V_1 = V_2 = \dots = V_N = \bar{V} = \frac{V}{N}. \quad (2)$$

Assuming also a similar shunt resistance for each sub-cell, Eq. (1) can be used to describe the current in the N -junction device with V replaced by \bar{V} . The assumption of equal sub-cells allows the normalization of related parameters such as the ideality factor and the resistance, which is useful for comparison purposes between single- and multi-junction devices. Therefore, the individual sub-cell ideality factor³¹ in an N -junction device can be obtained from the

measured current–voltage characteristic as the average value,

$$\bar{m} = \frac{q}{Nk_B T} \left(\frac{d \ln \left[\frac{J}{J_0} \right]}{dV} \right)^{-1}, \quad (3)$$

where the hat symbol indicates the dimensionless value of the current density $J = J/J_u$, with $J_u = 1 \text{ A cm}^{-2}$, for convenient numerical calculus after simplification of the J/J_0 argument inside the logarithm. Moreover, from the DC equivalent circuit of Fig. 1(b), the total resistance of a N -junction device can be approximated to $R = N \bar{R}(V)$, with $\bar{R}(V)$ being the identical sub-cell resistance in the regime $\forall R_{d,i}, R_{d,i} = R_{d,i+1}$. Therefore, applying the definition of differential resistance,³² the individual sub-cell resistance in an N -Junction device can be deduced from the current–voltage characteristic as

$$\bar{R} = \left(N \frac{dJ}{dV} \right)^{-1}. \quad (4)$$

Note that Eq. (4) does not include effects possibly present in real devices, including sub-cells with intentionally or unintentionally poor junction properties, and/or parasitic contact junctions,³³ which can limit its validity. Nevertheless, these average properties could be useful for comparison purposes.

The $J - V$ curves as measured in dark conditions are shown in Fig. 2(a) (left axis) as functions of the normalized average voltage per sub-cell for the 1J and the 10J PPCs. Both samples show similar response per \bar{V} with only slightly higher current values for the 10J device compared to the 1J in forward bias. In contrast, the 1J cell shows nearly twice more current in reverse bias than the 10J device.

The differential resistance in the DC operating point of the devices can be obtained by applying Eq. (4) to the experimental $J - V$ characteristics. This is presented in Fig. 2(a) (right axis) for the 1 and 10J samples under evaluation, showing similar current and resistance values in forward bias, where both the voltage and the resistances of the 10J sample were averaged to the junction number. This agrees with the approach of equal sub-cells in the 10J sample.

Contrastingly, in reverse bias, two main trends are found in Fig. 2 when comparing the 1 and 10J devices. First, both 1 and 10J samples nearly saturate toward a similar approximate shunt resistance in this normalized illustration. A clearer analysis of this trend would require larger reverse bias ranges, to be considered in future experiments. Second, the resistance values between -0.2 and 0 V are between 1.4 and 1.9 times lower for the 1J PPCs in comparison to the average sub-cell of the 10J device. This difference is attributed to the one-to-one variation of device characteristics, particularly the shunt resistance, and is, therefore, not analyzed in further detail in this work.

Figure 2(b) shows the extracted ideality factors of the 1 and 10J samples in forward bias, showing a similar magnitude and trends with values between 1.0 and 1.7 up to 0.4 V . This agrees with the approach of average sub-cell metrics. Beyond 0.4 V , the 1J

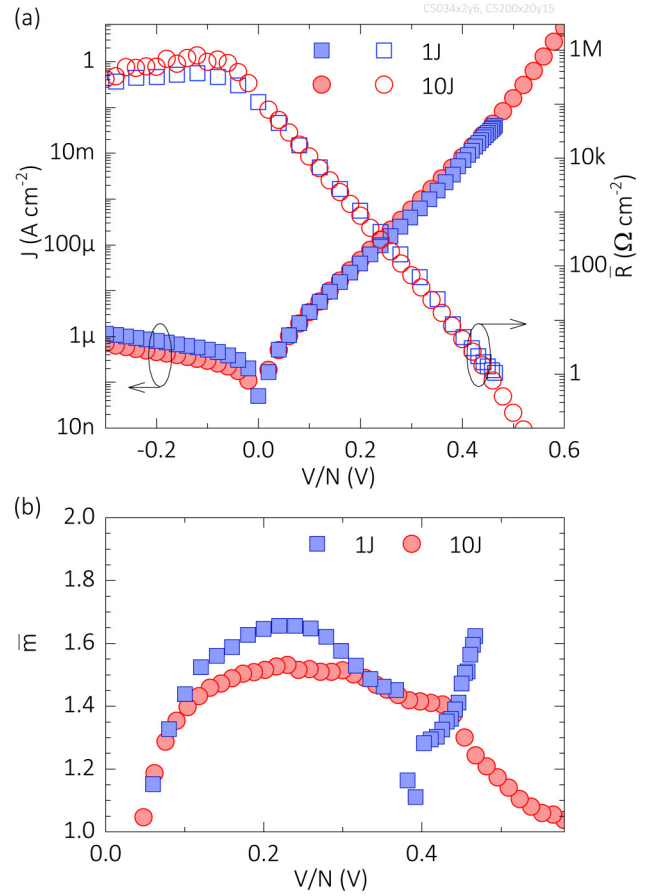


FIG. 2. (a) Dark current density J (filled symbols, left axis) and corresponding normalized resistance R (open symbols, right axis) and (b) ideality factors \bar{m} as a function of the normalized junction voltage per sub-cell V/N for 1J ($N=1$, blue symbols) and 10J ($N=10$, red symbols) InGaAs PPCs.

sample's ideality factor decreases sharply toward $m = 1$, followed by an increase possibly due to the proximity of the onset of series resistance effects in the $J - V$. The 10J sample, on the other hand, shows a steady decrease in average sub-cell ideality factor toward $m = 1$, as recombination in the quasi-neutral regions becomes more dominant without the onset of series resistance. The total series resistances for these samples were estimated from the impedance spectra [see Figs. 4(a)–4(c)] as ~ 0.1 and $\sim 0.01 \Omega \text{ cm}^2$ for the 1 and 10J samples, respectively. Notably, the increase in series inductance—particularly at higher irradiance—not only reduces the accuracy of series resistance measurements but also requires more detailed consideration under operating conditions where the device time response is significantly influenced by series resistance.

B. Current-voltage under monochromatic irradiance

A selection of the DC measurements for the 1 and 10J PPC samples under different irradiances are shown in Figs. 3(a) and 3(b).

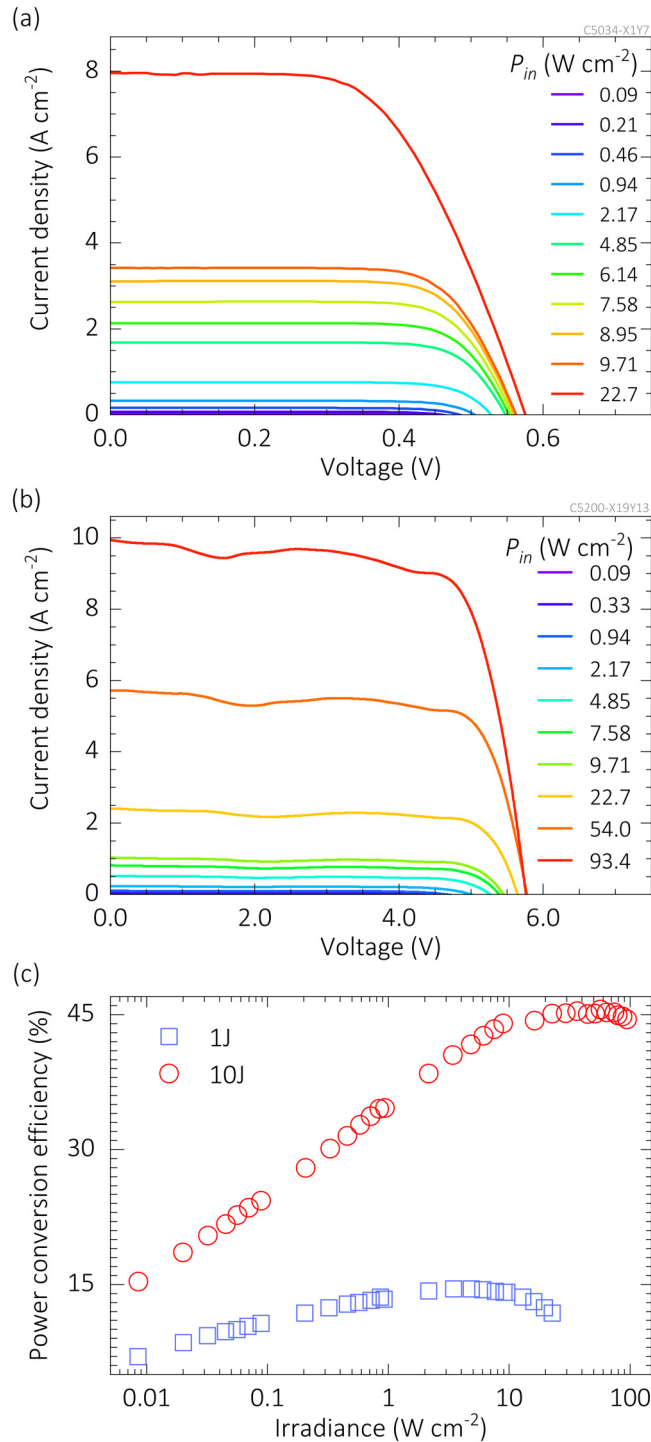


FIG. 3. Selected J - V curves with incident irradiances indicated from the (a) 1 J and (b) 10 J PPCs (designated area $A = 0.054 \text{ cm}^2$) and (c) corresponding PCE values under different incident irradiance values from a 1520 nm laser, as indicated in (c).

It is worth noting that the voltage of the 10 J device is effectively boosted by a factor of almost 10 with respect to the 1 J device due to the series connected nature of the device. Moreover, the 10 J device exhibits current “wiggles” in the J - V curves between short-circuit condition and the maximum power point (MPP) at high irradiances. This phenomenon has been attributed to the Franz-Keldysh effect³⁴ on the thinner sub-cell of the structure; these are not observed in the 1 J device.

The efficiency of each of the devices as a function of laser irradiance is shown in Fig. 3(c). The 10 J device shows high efficiencies over the range of irradiances considered, with peak value of 45.7% at an irradiance of $\sim 54 \text{ W cm}^{-2}$. At higher irradiances, the efficiency drops. Aside from the onset of ohmic losses, in this case, this drop is also attributed to thermal management limitations of the experimental setup, which cause the cell to heat up significantly during the light J - V measurement and results in a reduction in V_{oc} . The low efficiency of the 1 J PPC can be associated with the high transmission losses due to the semi-transparent thin 540 nm absorber layer of this test device.

C. Impedance spectroscopy

The devices were characterized both in dark and under monochromatic irradiance, combining DC and AC measurements. For a series of incident power density values P_{in} applied to the samples, the corresponding open-circuit voltages (V_{oc}) were recorded.^{19,31} During impedance spectroscopy measurements, a DC bias equal to V_{oc} was applied so that the device operated in a quasi-open-circuit condition, ensuring that negligible DC current flowed through the sample. Under this condition, the resistive component of the impedance can be directly associated with the recombination resistance, as contributions from transport or charge extraction resistances are negligible.

On the other hand, for the capacitive component of the device impedance, the experiment is designed to probe what can be termed *flatband*, *diffusion* (in dark) or *chemical capacitance*. This capacitance is not only linked to forward-bias operation with applied voltages close to the built-in potential and to the maximum attainable V_{oc} , but more importantly, it reflects a capacitive signal that is no longer associated with “surface” depletion-layer capacitance. Instead, it arises from the charge-carrier “volume” modulation of the quasi-Fermi levels under small voltage perturbations. Historically,²² such experiments were performed on p-n junctions near to or at flatband dark conditions, where diffusion current dominates over drift current. This led to the definition of the *diffusion capacitance*,²² a capacitive contribution proportional to the diffusion current arising from field-independent quasi-Fermi level gradients. The concept was later extended to dye-sensitized solar cells under illumination at quasi-open-circuit conditions, where it was reformulated in electrochemical terms as the *chemical capacitance*³⁵ and subsequently applied to solid-state p-i-n solar cells as well.³⁶ Moreover, since the capacitance originates from charge-carrier density modulation and the resistance from recombination processes, these impedance components can be coupled through a characteristic time constant and associated with an effective charge-carrier lifetime, which has been shown to agree well with photoinduced transient photovoltage measurements.^{36,37}

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The $\tau = RC$ response times from impedance spectroscopy depart from the recombination lifetimes, including dielectric relaxation contribution,³⁸ for cases where the main capacitive response does not relate to the charge/discharge response of electronic occupation of the quasi-Fermi levels. This is true for low forward bias and reverse bias, where junction capacitance is the main contribution, and for high injection forward bias, where the low resistivity of the device short-circuits its capacitive features, hindering any charge/discharge capacitive process. In addition, the high inductive artifacts and laser-related instability of the signal (e.g., temperature) further hinder the measurement of impedance spectroscopy in “pure” diffusion or high injection transport regimes. Since these additional contributions cannot be ruled out from the measurement data, the degree of proportionality between the extracted τ values and the effective recombination lifetimes remains indeterminate. Furthermore, it should be noted that impedance spectroscopy can only probe recombination processes within its sampling time window—typically those slower than the microsecond range. Consequently, faster processes, such as the minority-carrier lifetimes characteristic of GaAs devices,³⁹ cannot be accurately captured by this technique.

The impedance spectroscopy measurement under different illumination intensities for the 1 and 10 J devices is presented as Nyquist plots in Figs. 4(a) and 4(b). The obtained experimental spectra [dots in Figs. 4(a) and 4(b)] display a distinct “one-semicircle” pattern, indicating good agreement with the equivalent circuit models depicted in Fig. 1(c) and ruling out significant contributions from multiple transport mechanisms within the investigated frequency range.^{36,40} The fitted data [lines in Figs. 4(a) and 4(b)] provide parameterized values for resistance, capacitance, and response times, which are plotted as functions of the incident photon power (P_{in}) in Figs. 4(c)–4(e) and the normalized photovoltage per sub-cell (V_{oc}/j) in Figs. 4(f)–4(h).

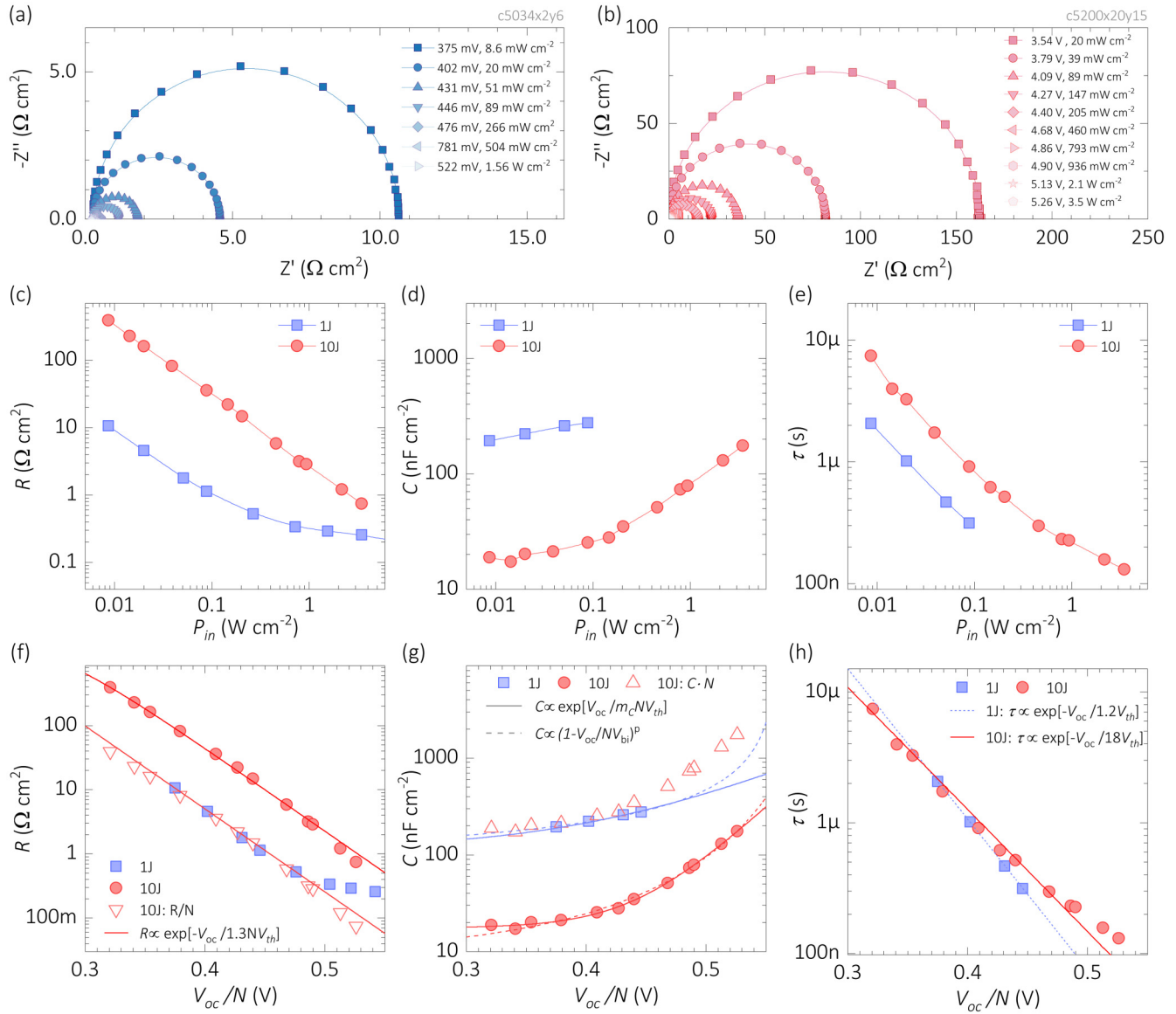
In terms of the incident irradiance, the resistance and capacitance values in Figs. 4(c) and 4(d) (solid symbols) are consistently higher and lower, respectively, for the 10 J device compared to those of the 1 J PPC. Notably, below 100 mW cm^{-2} , the resistance begins to deviate from its inverse decay trend ($R \propto P_{\text{in}}^{-1}$) and approaches a saturation toward the series resistance of the 1 J sample in Fig. 4(c). In this ohmic regime, capacitance measurements using the employed LCR meter are no longer feasible and the estimation of response times requires alternate methods, such as time-of-flight.^{25,41} The inductive-like or negative capacitance dominates the imaginary part of impedance signal for these high values of irradiance/voltage, preventing the estimation of the capacitance, even though the resistance (real part of impedance) is well defined up to 3.5 W cm^{-2} . For higher irradiance intensities, the instrument was unable to resolve the spectra due to the low impedance of the sample that would require more specialized frequency response analyzers. Moreover, Fig. 4(d) reveals a power-law relationship $C \propto P_{\text{in}}^{\alpha}$ with $0.2 < \alpha < 0.6$, leading to an approximate trend $\tau = RC \propto P_{\text{in}}^{\alpha-1}$ for the resulting response times in Fig. 4(e). This indicates that the 10 J device requires nearly five times the irradiance of the 1 J sample to achieve the same characteristic response time. Moreover, the response time (τ) of the 10 J device is consistently higher than that of the 1 J PPC; for instance, under 88 mW cm^{-2} irradiance, τ values are approximately 314 and 911 ns for the 1 and 10 J samples, respectively.

The analysis of the normalized photovoltage per sub-cell (V_{oc}/N) is presented in Figs. 4(f)–4(h). The sub-cell-normalized resistance values [open triangle symbols in Fig. 4(f)] for the 1 and 10 J samples follow the trend $R \propto \exp[-V_{oc}/(NmV_{th})]$, with $V_{th} = k_B T/q$ and $m \approx 1.3$ for $V_{oc}/N < 0.45 \text{ V}$. This m value is slightly smaller than those shown in Fig. 2(b), suggesting that dark operation in the same forward-bias range may favor recombination within the space-charge region, compared to analogous quasi-open-circuit conditions under illumination.

In Fig. 4(g), the sub-cell-normalized capacitance of the 10 J sample is slightly higher than that of the 1 J sample when plotted as a function of the average sub-cell voltage. For the 1 J device, strong inductive artifacts under high irradiance conditions prevented reliable capacitance extraction and limited the accuracy of parameters describing the overall trends. The data were nonetheless fitted to examine the relation $C = C_0 + C_1(1 - V_{oc}/NV_{bi})^{-p}$, where V_{bi} is the built-in voltage and $C_{0,1}$, fitting parameter. A value $p > 0.5$ indicates a departure from depletion-layer capacitance, and for the 1 J sample, we obtained $p > 1$ [blue dashed line in Fig. 4(g)]. Although no clear exponential increase was observed—likely due to the inductive artifacts and the limited voltage range below flatband conditions—the 1 J data were also fitted to the form $C = C_0 + C_1 \exp[V_{oc}/(Nm_C V_{th})]$, yielding $m_C \approx 3.3$ [blue solid line in Fig. 4(g)], albeit with low accuracy. By contrast, the 10 J device exhibits a much clearer exponential dependence, with $m_C = 1.47 \pm 0.02$ and $p > 2$ as shown by the red solid and dashed lines in Fig. 4(g), respectively.

Figure 4(h) shows good agreement between the response times from both samples at $V_{oc}/N < 0.45 \text{ V}$. However, for higher V_{oc}/N values, the 10 J PPC exhibits greater τ than the 1 J sample, with the difference increasing alongside the photovoltage and the irradiance. This behavior is likely related to the capacitance [see Fig. 4(g)], as the resistance values in this range remain nearly identical [see in Fig. 4(f)]. This slight capacitance increase primarily indicates a higher average concentration of charge carriers for the 10 J sub-cells compared to that for the 1 J sub-cells. A primary reason for this could be the more effective photon management of the 10 J device including current matching and radiative coupling between sub-cells. In addition, from the point of view of the diffusion capacitance formalism, slight differences in the diffusion coefficient and/or average density of states due to heat distribution between sub-cells could also be contributing factors.^{38,40}

The impedance spectroscopy results reveal distinct behaviors between 1 and 10 J full devices (solid dots in Fig. 4). The 10 J device exhibits longer response times, increased resistance, and reduced capacitance compared to the 1 J device at equivalent power densities as a consequence of the architecture and similarities between the properties of the sub-cells of the MJ device and those of the 1 J PPC. However, when analyzed per normalized sub-cell open-circuit voltage (open dots in Fig. 4), the resistances and response times of both devices are nearly identical under low irradiance conditions ($P_{\text{in}} < 30 \text{ mW cm}^{-2}$, $V_{oc}/N < 0.4 \text{ V}$). At higher irradiances ($P_{\text{in}} > 30 \text{ mW cm}^{-2}$, $V_{oc}/N > 0.4 \text{ V}$), the 10 J device shows increased capacitance and longer characteristic response times compared to the 1 J device. Despite the limited data following this behavior, we speculate on a transition in recombination mechanisms that favors the multi-junction device for energy conversion applications, where longer response times correlate with improved



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FIG. 4. Impedance spectroscopy characterization in open-circuit conditions under different irradiance intensities of a 1520 nm laser. The Nyquist plots [(a) and (b)] for the experimental data (symbols) and corresponding fittings (lines) to the equivalent circuit model in Fig. 1 are shown for the single-junction (a) and the ten-junction (b) devices with lighter colors representing higher irradiance. The resulting parameterized resistance R [(c) and (f)], capacitance C [(d) and (g)], and characteristic response times τ [(e) and (h)] are plotted as functions of the incident power P_{in} [(c)–(e)] and the normalized open-circuit voltage per sub-cell V_{oc}/N [(f)–(h)].

PCE , but comes at the cost of slower response times. Other possible causes might be subtle differences between the 1 and 10 J device related to the length of the quasi-neutral regions compared to the diffusion length, i.e., depending on whether the diodes are working in the limit of short or long diodes. However, further investigation with an expanded statistical data set is necessary to elucidate this observation.

IV. CONCLUSIONS

Steady-state current–voltage and impedance measurements under dark and increasing monochromatic irradiances were employed to investigate power conversion efficiency, resistance, capacitance, and quasi-open-circuit response times in single-junction (1 J) and ten-junction (10 J) $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ photonic power converters.

A methodology for analyzing normalized sub-cell properties was introduced, enabling a more effective comparison between the two devices. The impedance spectroscopy analysis revealed the decrease and increase of resistance and capacitance of the 1 and 10 J devices, respectively, confirming their response time to scale with the number junctions in each device. At low irradiances, the individual sub-cells exhibit nearly identical resistance and capacitance behavior as well as response time, validating the average sub-cell approach used in the investigation. At irradiances above 100 mW cm^{-2} , sub-cells in the 10 J device exhibit a larger capacitance and consequently longer characteristic response times as compared to the 1 J device, highlighting a difference of the average recombination activity in the device.

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AUTHOR DECLARATIONS

Conflict of Interest

The authors have no conflicts to disclose.

Author Contributions

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DATA AVAILABILITY

The data that support the findings of this study are available from the corresponding authors upon reasonable request.

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