

Weak Frequency Dispersion of C–V Characteristics of AlGa_N/Ga_N Metal-Insulator-Semiconductor Devices Despite High Interface Trap Density

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For AlGa_N/Ga_N metal-insulator-semiconductor (MIS) devices, it is shown that there is a case where the frequency dispersion of the capacitance–voltage (C–V) characteristics is very weak despite a rather high insulator–semiconductor interface trap density. We fabricated metal/Al₂O₃/AlGa_N/Ga_N MIS devices, and carried out C–V characterization of them. Although they exhibit very weak C–V frequency dispersion for 100 Hz–1 MHz, we found that, based on the conductance method, the insulator–semiconductor interface trap densities are rather high $\approx 3 \times 10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$. As a possible explanation, a double-peak model is considered, which underlines the possibility that a high interface trap density does not necessarily lead to strong C–V frequency dispersion. The model can explain the observed weak C–V frequency dispersion, implying that it is not appropriate to naively discuss the interface traps only from the apparent C–V frequency dispersion.

conductance (the real part of the trap admittance) is analyzed as a function of frequency, and the trap density can be obtained as well as the trapping time constant. In many cases, strong frequency dispersion of the capacitance–voltage (C–V) characteristics is associated with a high interface trap density, while weak C–V frequency dispersion is associated with a low interface trap density.^[32,33] This is natural because the capacitance, which is obtained from the imaginary part of the trap admittance, is closely related to the conductance (the real part). Thus, in many cases, a low interface trap density is inferred when the C–V frequency dispersion is weak.^[34] However, this is not always true; apparent weak frequency dispersion is not always associated with a low interface trap density.

^[35,36] Therefore, there are uncertainties in discussing the interface trap density just based on the apparent C–V frequency dispersion.

In this article, we shed light on the relation between the insulator–semiconductor interface trap density and the apparent C–V frequency dispersion for metal/Al₂O₃/AlGa_N/Ga_N MIS capacitor devices. First, we show that, although the devices exhibit very weak C–V frequency dispersion for 100 Hz–1 MHz, the insulator–semiconductor interface trap densities obtained by the conductance method are rather high $\approx 3 \times 10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$. Next, we consider a double-peak model to explain this, showing the possibility that a high interface trap density does not necessarily lead to strong C–V frequency dispersion. We should note that it is not appropriate to naively discuss the interface traps only from the apparent C–V frequency dispersion.

1. Introduction


GaN-based metal-insulator-semiconductor (MIS) devices have been extensively investigated, owing to the merit of gate leakage reduction. As a gate insulator, various materials have been employed and studied, such as Al₂O₃,^[1] AlTiO₃,^[2–8] AlSiO₃,^[9,10] HfO₂,^[11,12] TiO₂,^[13] TaON,^[14] AlON,^[15] AlN,^[16–20] and BN.^[21,22] In any cases, the insulator–semiconductor interface properties are quite important. Since the threshold voltage is strongly affected by insulator–semiconductor interface fixed charges, interface charge engineering^[23,24] has been explored.^[6,7,25–27] Moreover, device instability caused by insulator–semiconductor interface traps is a critical issue.^[28] The interface traps significantly depend on the device fabrication processes and have been extensively investigated by several methods. In particular, the conductance method^[29–31] is a very strong tool, where the

2. Device Fabrication and Characterization

Using an Al_{0.24}Ga_{0.76}N (20 nm)/Ga_N (3 μm) heterostructure grown by metal–organic vapor phase epitaxy on a sapphire (0001) substrate, we fabricated metal/Al₂O₃/AlGa_N/Ga_N MIS capacitor devices with a gate area of 75 μm^2 , whose schematic is shown in **Figure 1a**. Ti-based Ohmic electrodes were formed, using Ti (5 nm)/Al (200 nm)/Ti (100 nm)/Au (50 nm) annealed at 575 °C in N₂ ambient. The Ohmic contact resistance of $\approx 1 \Omega \text{ mm}$ was confirmed by transfer length method measurements. After that, several thicknesses of Al₂O₃ gate insulators were formed by atomic layer deposition at 150 °C, using

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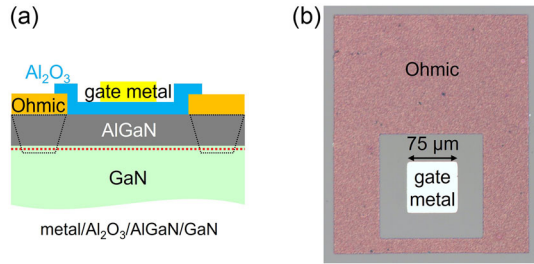


Figure 1. a) The schematic and b) the top view of the metal/Al₂O₃/AlGaIn/GaN capacitor devices.

trimethyl aluminum and water. The Al₂O₃ thicknesses $d_{\text{Al}_2\text{O}_3}$ are 16, 22, and 28 nm, which were confirmed by ellipsometry measurements of reference samples obtained by side-by-side deposition. We completed the device fabrication by gate metal formation using a Ni film ≈ 5 nm covered by an Au film ≈ 100 nm, both obtained by vacuum evaporation. The top view of the fabricated devices is shown in **Figure 1b**.

For the fabricated metal/Al₂O₃/AlGaIn/GaN MIS devices, the gate current densities J between the gates and the grounded Ohmic electrodes were measured in comparison with a metal/AlGaIn/GaN Schottky device. **Figure 2** shows the measured J - V characteristics, J as a function of the gate voltage V_G . The gate leakage currents, both on forward and reverse biases, are effectively suppressed by the Al₂O₃ gate insulator, ensuring that the capacitance and conductance of the devices can be evaluated properly for $V_G \leq 7$ V. Moreover, we measured C - V characteristics between the gates and the grounded Ohmic electrodes of the MIS devices at frequency $f = 1$ MHz. **Figure 3** shows the measured capacitance C_{meas} as a function of the gate voltage V_G , as well as the 2D electron gas concentration n_s obtained by integrating the C - V characteristics using $C_{\text{meas}} = \partial(qn_s)/\partial V_G$. From the n_s - V characteristics, we find linear relations $qn_s \approx C(V_G - V_{\text{th}})$, from which we can determine the threshold voltage V_{th} and C , where $1/C = 1/C_{\text{Al}_2\text{O}_3} + 1/C_{\text{AlGaIn}} = d_{\text{Al}_2\text{O}_3}/(k_{\text{Al}_2\text{O}_3}\epsilon_0) + 1/C_{\text{AlGaIn}}$ using the insulator capacitance $C_{\text{Al}_2\text{O}_3}$, the AlGaIn capacitance C_{AlGaIn} , the insulator thickness $d_{\text{Al}_2\text{O}_3}$, and the insulator dielectric constant $k_{\text{Al}_2\text{O}_3}$. The inset of Figure 3 shows $1/C$ as a function of $d_{\text{Al}_2\text{O}_3}$. By linear fitting, we obtain $k_{\text{Al}_2\text{O}_3} \approx 8$ from the slope, which

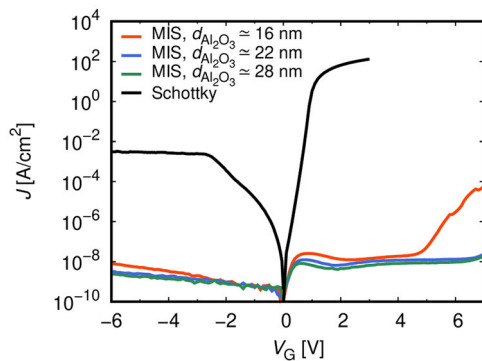


Figure 2. J - V characteristics of metal/Al₂O₃/AlGaIn/GaN MIS devices, in comparison with those of a metal/AlGaIn/GaN Schottky device.

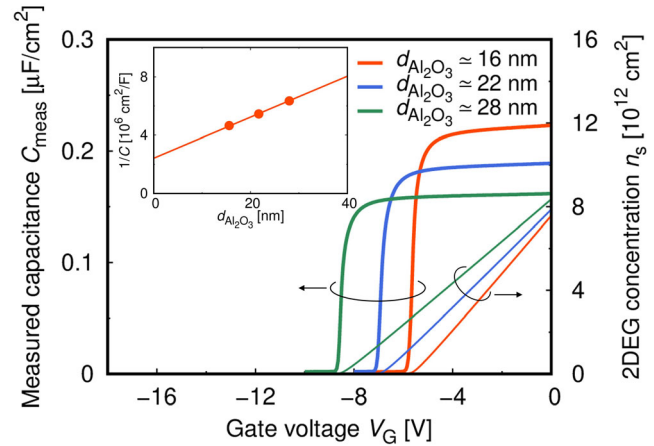


Figure 3. C - V and n_s - V characteristics of the metal/Al₂O₃/AlGaIn/GaN MIS devices. Inset: $1/C$ as a function of $d_{\text{Al}_2\text{O}_3}$.

is a reasonable value for amorphous Al₂O₃. **Figure 4** shows the obtained V_{th} as a function of $d_{\text{Al}_2\text{O}_3}$, where we obtain a linear relation given by

$$V_{\text{th}} = \frac{\sigma_{\text{GaN}} - \sigma_{\text{int}}}{k_{\text{Al}_2\text{O}_3}\epsilon_0} d_{\text{Al}_2\text{O}_3} + \text{const.} \quad (1)$$

using the GaN polarization charge density σ_{GaN} and the insulator-semiconductor interface fixed charge density σ_{int} . From the slope, we obtain $\sigma_{\text{int}}/q \approx 3.2 \times 10^{13} \text{ cm}^{-2}$ assuming $\sigma_{\text{GaN}}/q \approx 2.1 \times 10^{13} \text{ cm}^{-2}$ [37–41] as shown in the inset of Figure 4 in comparison with AlGaIn polarization charge density σ_{AlGaIn} , where the error bar stands for the three-sigma standard deviations of the linear fitting. This result indicates that the Al₂O₃/AlGaIn interface is nearly neutral, being consistent with other studies. [23–27,31,42,43]

Furthermore, we investigated f -dependent C - V characteristics in order to examine the insulator(Al₂O₃)-semiconductor (AlGaIn) interface trap densities of the devices. **Figure 5** shows an example of the measured f -dependent C - V characteristics for $d_{\text{Al}_2\text{O}_3} = 22$ nm at frequency $f = 100 \text{ Hz}$ -1 MHz, where we find that the devices exhibit very weak frequency dispersion of the C - V

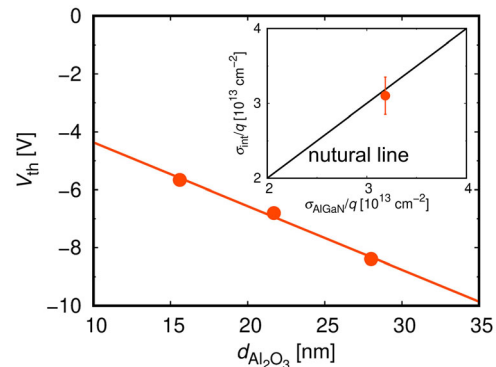


Figure 4. The threshold voltages V_{th} as a function of $d_{\text{Al}_2\text{O}_3}$. Inset: the interface fixed charge density σ_{int} in comparison with the AlGaIn polarization charge density.

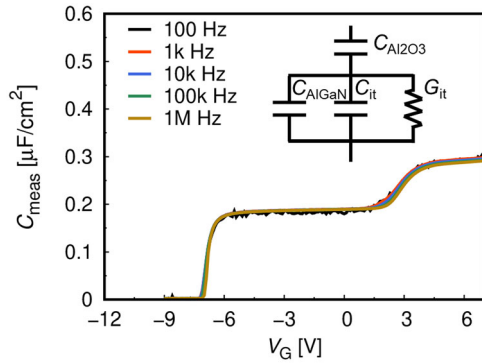


Figure 5. f -dependent C - V characteristics of the metal/ Al_2O_3 /AlGaIn/GaN MIS device with $d_{\text{Al}_2\text{O}_3} = 22$ nm. Inset: the equivalent circuit.

characteristics. By using the conductance method, we evaluated the interface trap densities of the devices based on the circuit shown in the inset of Figure 5, which is a small signal equivalent circuit at a fixed bias voltage. To describe electron trapping (and also detrapping) by the interface trap level at the Fermi energy, the circuit includes the interface trap admittance (as a function of $\omega = 2\pi f$), whose real part is the trap conductance G_{it} , where

$$\frac{G_{it}}{\omega} = \frac{q^2 D_{it} \ln(1 + \omega^2 \tau^2)}{2\omega\tau} \quad (2)$$

and the imaginary part is $C_{it}\omega$, where

$$C_{it} = \frac{q^2 D_{it} \text{atan}(\omega\tau)}{\omega\tau} \quad (3)$$

is the trap capacitance, using the trapping time constant τ of the trap level at the Fermi energy. It should be noted that, while generally the trap levels are continuously distributed in the semiconductor energy gap, at the fixed bias voltage, the corresponding trap level is specified by the Fermi energy position. According to Equation (2), usually G_{it}/ω exhibits a single-peaked behavior as a function of f , from which τ can be evaluated by the peak frequency $f_p = 1/(\pi\tau)$ and the interface trap density D_{it} by the peak value $G_{it}/\omega \approx 0.4q^2 D_{it}$. **Figure 6a** shows the measured G_{it}/ω for several forward bias voltages, showing the single-peaked behavior and $D_{it} \approx 3 \times 10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$. Electron trapping and detrapping take place between the AlGaIn conduction band and Al_2O_3 /AlGaIn interface trap level at the Fermi energy. With continuous increase in positive bias voltages, continuous increase in the peak frequency of G_{it}/ω is observed, corresponding to decrease in the energy difference between the AlGaIn conduction band bottom E_C and the Fermi energy E_F of the AlGaIn/GaN, indicating that the AlGaIn layer is depleted and plays the role of the depletion layer. The time constant τ is given by

$$\tau = \tau_0 e^{(E_C - E_F)/k_B T} \quad (4)$$

where $\tau_0 \propto 1/\sigma_e$ is a time constant inversely proportional to the electron capture cross section σ_e of the trap. Even though τ_0 is ambiguous due to the uncertainty of σ_e , by assuming a wide range of $\tau_0 \approx 0.1$ –1000 ps, we can estimate the “activation energy” $E_C - E_F \approx 0.6$ eV from the evaluated τ . **Figure 6b** shows D_{it} as a

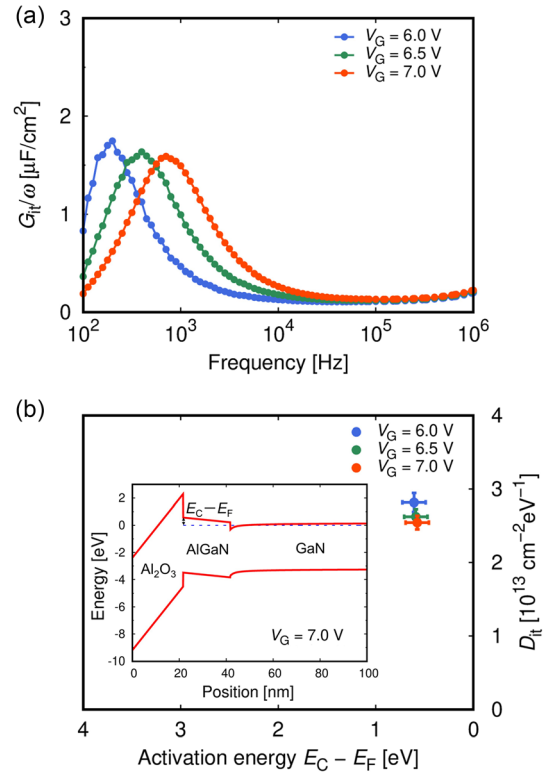


Figure 6. a) G_{it}/ω as functions of frequency for the metal/ Al_2O_3 /AlGaIn/GaN MIS device with $d_{\text{Al}_2\text{O}_3} = 22$ nm. b) The interface trap density D_{it} as a function of the activation energy $E_C - E_F$. Inset: the band diagram obtained by Poisson–Schrödinger calculation for $V_G = 7$ V.

function of $E_C - E_F$, with horizontal and vertical error bars corresponding to the wide-range τ_0 and the three-sigma asymptotic standard errors of the fittings using Equation (2), respectively. Based on this, we obtain the band diagram of the metal/ Al_2O_3 /AlGaIn/GaN MIS devices by Poisson–Schrödinger calculation. The inset of **Figure 6b** shows the band diagram for $V_G = 7$ V. At this bias voltage, owing to the high $D_{it} \approx 3 \times 10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$, high-density electrons are admitted at the Al_2O_3 /AlGaIn interface, and the AlGaIn layer stays to be depleted, leading to electron trapping and detrapping between the AlGaIn conduction band and the Al_2O_3 /AlGaIn interface trap level at E_F .

It should be noted that, while the interface trap density for the device is rather high over $10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$, the frequency dispersion of the C - V characteristics is very weak. This implies that very weak C - V frequency dispersion is not necessarily associated with a low interface trap density. On the other hand, tails of G_{it}/ω are observed, slightly increasing at $\lesssim 1$ MHz. These tails are specific to the present gate insulator; different gate insulators exhibit no tails.^[4,35] Thus, it is suggested that, for the present gate insulator, a second peak may exist at a higher frequency.

3. Double-Peak Model

As a possible explanation for the weak C - V frequency dispersion despite the high D_{it} , we consider a double-peak model,^[44–47] whose equivalent circuit is shown in **Figure 7**. In this model,

G_{it} and C_{it} are parallel connections of two components, corresponding to two different types of traps, with trap densities D_{it1} and D_{it2} , and distinct trapping time constants τ_1 and τ_2 .^[48,49] In fact, two clear peaks at the frequencies of ≈ 1 kHz and 10 MHz ranges have been observed, corresponding to long and short trapping time constants, respectively.^[45,47] This model assumes that there are two types of continuously distributed traps, and thus, two degenerate trap levels at the Fermi energy determined by the fixed bias voltage. The two trap levels have the same activation energy $E_C - E_F$, but different trap densities and also different trapping time constants owing to different $\tau_0 \propto 1/\sigma_e$. The circuit is, thus, described by

$$\frac{G_{it}}{\omega} = \frac{q^2 D_{it1} \ln(1 + \omega^2 \tau_1^2)}{2\omega \tau_1} + \frac{q^2 D_{it2} \ln(1 + \omega^2 \tau_2^2)}{2\omega \tau_2} \quad (5)$$

and

$$C_{it} = \frac{q^2 D_{it1} \text{atan}(\omega \tau_1)}{\omega \tau_1} + \frac{q^2 D_{it2} \text{atan}(\omega \tau_2)}{\omega \tau_2} \quad (6)$$

As a result, G_{it}/ω exhibits a double-peaked behavior as a function of f .

By a model calculation, let us see the behavior of the double-peak model in comparison with the single-peak model, where the latter does not include the second terms of Equation (5) and (6). Assuming $C_{Al_2O_3} = 300 \text{ nF cm}^{-2}$, $C_{AlGaN} = 400 \text{ nF cm}^{-2}$, $D_{it1} = 2.5 \times 10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$, $\tau_1 = 10^{-4} \text{ s}$, $D_{it2} = 3.0 \times 10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$, and $\tau_2 = 10^{-8} \text{ s}$, we calculated G_{it}/ω and C_{it} as functions of frequency for the double-peak model. For comparison, G_{it}/ω and C_{it} for the single-peak model without the second terms of Equation (5) and (6) are also calculated. **Figure 8a** shows the calculated G_{it}/ω for both models, where a single- or double-peaked behavior can be observed. For relatively low frequency $\lesssim 1$ MHz, we observe similar behavior of G_{it}/ω for both the single- and double-peak models. However, for relatively high frequency $\gtrsim 1$ MHz, a second peak of G_{it}/ω exists for the double-peak model, while G_{it}/ω vanishes for the single-peak model. **Figure 8b** shows the calculated C_{it} for both models. While the variation of C_{it} is from 0 to $\approx 4 \mu\text{F cm}^{-2}$ for the single-peak model, the double-peak model always exhibits C_{it} larger than $\approx \mu\text{F cm}^{-2}$ order. This different behavior of C_{it} will lead to different frequency dispersion of the measured capacitance C_{meas} . **Figure 9** shows the calculated prediction of the measured capacitance C_{meas} as a function of frequency, based on the single- and double-peak models. For the single-peak model, at frequency $\lesssim 100$ kHz, C_{it} is large (larger than $\approx \mu\text{F cm}^{-2}$ order), and thus, C_{meas} will be similar to the capacitance of the gate insulator $C_{meas} = C_{Al_2O_3}(C_{AlGaN} + C_{it})/(C_{Al_2O_3} + C_{AlGaN} + C_{it}) \simeq C_{Al_2O_3}$. When the frequency increases, the vanishing C_{it} leads to

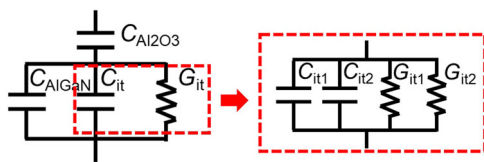


Figure 7. The equivalent circuit for the double-peak model.^[44–47]

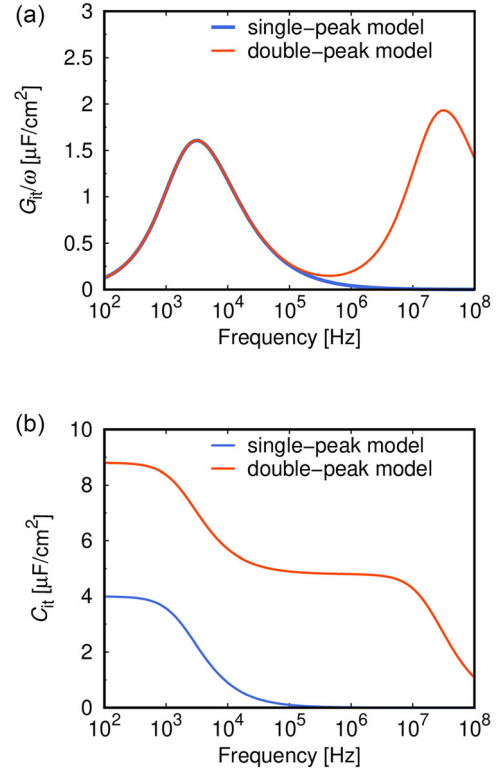


Figure 8. a) G_{it}/ω and b) C_{it} as functions of frequency obtained by the single- and double-peak models. We assume $C_{Al_2O_3} = 300 \text{ nF cm}^{-2}$, $C_{AlGaN} = 400 \text{ nF cm}^{-2}$, $D_{it1} = 2.5 \times 10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$, $\tau_1 = 10^{-4} \text{ s}$, $D_{it2} = 3.0 \times 10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$, and $\tau_2 = 10^{-8} \text{ s}$.

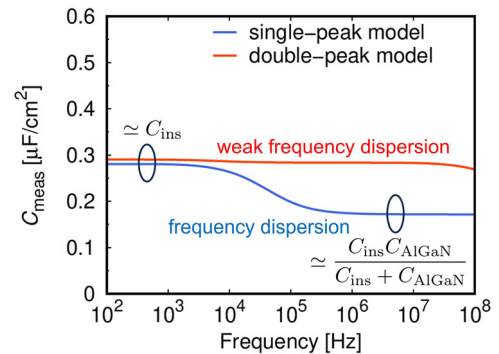


Figure 9. The calculated prediction of the measured capacitance C_{meas} as a function of frequency based on the single- and double-peak models.

$C_{meas} \simeq C_{Al_2O_3} C_{AlGaN} / (C_{Al_2O_3} + C_{AlGaN})$. As a result, frequency dispersion can be observed around the frequency ≈ 10 kHz–100 kHz for the single-peak model. On the other hand, since C_{it} is larger than $\approx \mu\text{F cm}^{-2}$ order up to 10^8 Hz for the double-peak model, C_{meas} will remain to be $C_{meas} = C_{Al_2O_3}(C_{AlGaN} + C_{it})/(C_{Al_2O_3} + C_{AlGaN} + C_{it}) \simeq C_{Al_2O_3}$. This leads to very weak frequency dispersion of C_{meas} for the double-peak model, despite the assumed high interface trap densities over $10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$. These results at least show that there is a case where a MIS device exhibits very weak C–V frequency dispersion even with a high

interface trap density, and therefore an apparent weak $C-V$ frequency dispersion does not necessarily indicate a low interface trap density.

The double-peak model is applied to the measurement results shown in the previous section, in comparison with the single-peak model. **Figure 10** shows the measured G_{it}/ω for the metal/ Al_2O_3 / $\text{AlGaIn}/\text{GaIn}$ MIS device, fitted by the single- or double-peak model. Both models well describe the observed peak at ≈ 1 kHz. On the other hand, around $\lesssim 1$ MHz, there exists a tail of a second peak of G_{it}/ω in the measurement results, with a peak frequency higher than the measurement frequency range, suggesting the existence of another kind of traps with a rather short trapping time constant $\approx 10^{-8}$ s. This tail of the second peak can be described only by the double-peak model. **Figure 11** shows the measured capacitance C_{meas} of the device as a function of frequency, in comparison to the calculation by single- or double-peak model, shown by dotted and solid lines, respectively. The double-peak model well explains the weak frequency dispersion of C_{meas} in the measurement frequency range. The interface traps significantly depend on the device fabrication processes and the insulator quality. For the present devices, owing to the fact that the double-peak model explains the experimental results well, we assume two types of traps, including the one that has a short trapping time constant corresponding to the second peak. Although it is difficult to clarify the origin of the traps, it should be noted that the trapping time

constant is given by $\tau_0 \propto 1/\sigma_e$, inversely proportional to the capture cross section of the traps. Thus, in the present devices, it is plausible to assume that there exist traps with a rather large capture cross section.

4. Conclusion

We fabricated metal/ Al_2O_3 / $\text{AlGaIn}/\text{GaIn}$ MIS devices and evaluated the interface trap density by the conductance method. We found a rather high interface trap density $\approx 3 \times 10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$, even though we observed very weak frequency dispersion of the $C-V$ characteristics. As a possible explanation, we consider a double-peak model, which includes two different types of traps with distinct trapping time constants. The model underlines the possibility that a high interface trap density does not necessarily lead to strong $C-V$ frequency dispersion. This implies that it is not appropriate to naively discuss the trap density just by apparent $C-V$ frequency dispersion.

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Conflict of Interest

The authors declare no conflict of interest.

Data Availability Statement

The data that support the findings of this study are available from the corresponding author upon reasonable request.

Keywords

$\text{AlGaIn}/\text{GaIn}$ device, frequency dispersion, interface trap

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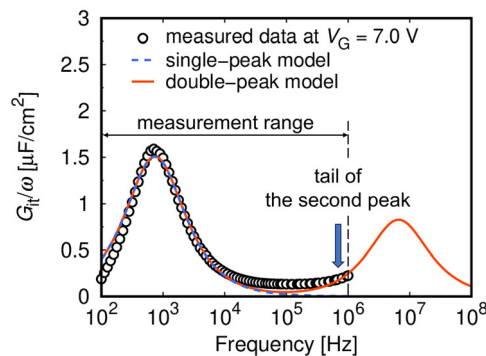


Figure 10. Measured G_{it}/ω in comparison with calculated curves obtained by the single- and double-peak models.

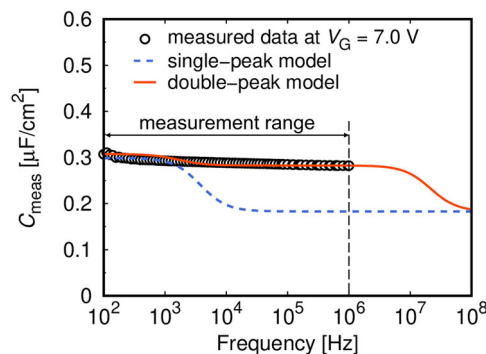


Figure 11. Measured capacitance C_{meas} in comparison with calculated curves obtained by the single- and double-peak models.

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