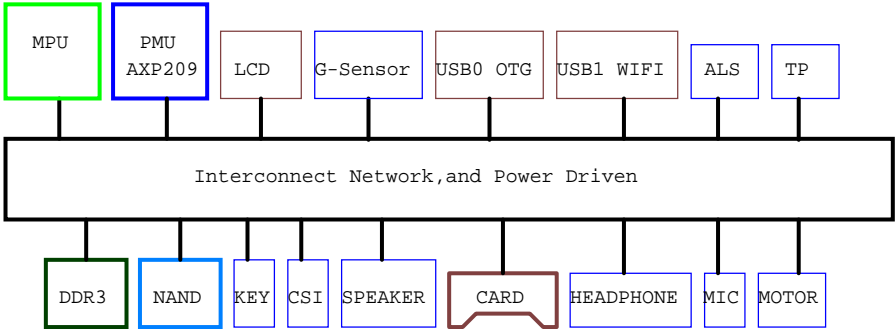


COVER

BLOCK



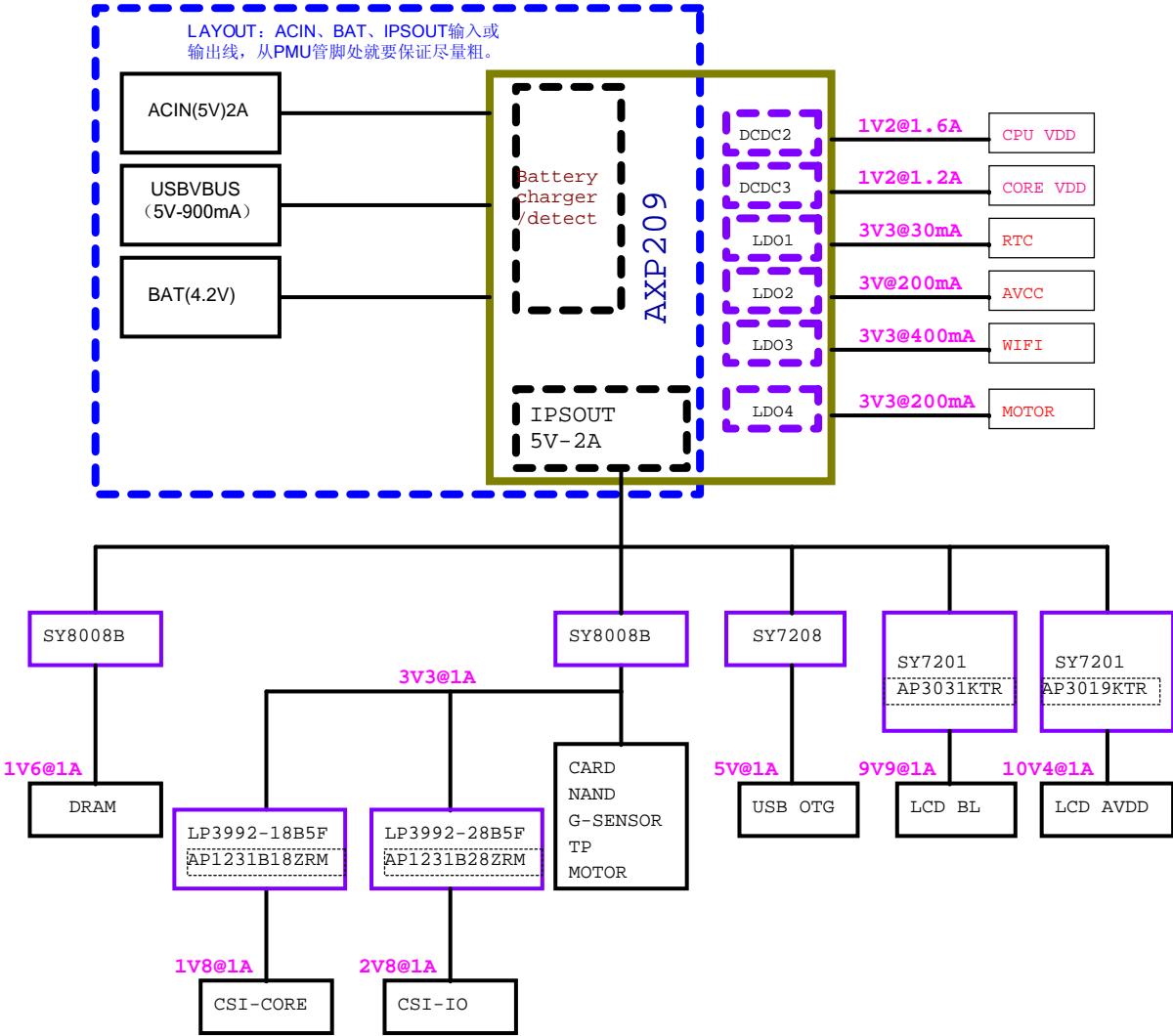
GPIO ASSIGNMENT

CPU	Define	Function
PB0	TWI0_SCK	TWI0
PB1	TWI0_SDA	
PB2	PWM0	LCD
PB3	GPIO-IN	SD0-DET-N
PB4	EINT	ALS-INT
PB10	GPIO-OUT	CSI-STY
PB15	TWI1_SCK	TWI1
PB16	TWI1_SDA	
PB17	TWI2_SCK	TWI2
PB18	TWI2_SDA	
PG0	GPIO-OUT	PA-SHDN
PG1	GPIO-OUT	MT-EN
PG2	GPIO-OUT	TP-WAKE-RST
PG3	UART-TX	UART-TX
PG4	UART-RX	UART-RX
PG9	EINT	TP-INT
PG10	GPIO-IN	USB0-IDDET
PG11	GPIO-IN	USB0-VBUSDET
PG12	GPIO-OUT	USB0-DRV
PMU	Define	Function
GPIO0	GPIO-OUT	LCD-PWR
GPIO1	GPIO-OUT	LCD-BL-EN
GPIO2	GPIO-OUT	CSI-PWR-EN
GPIO3	GPIO-OUT	CSI-RST

REVISION HISTORY

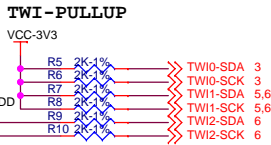
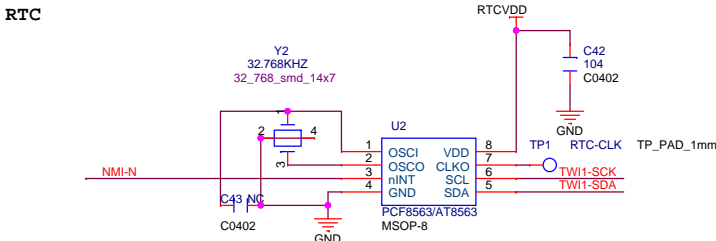
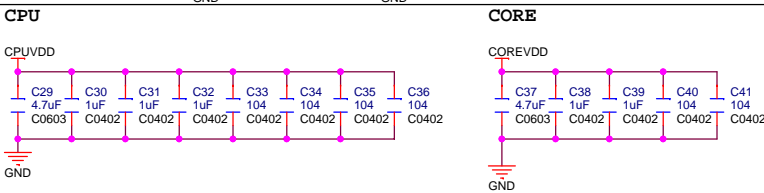
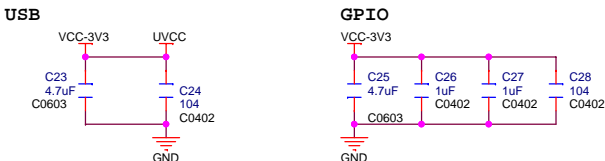
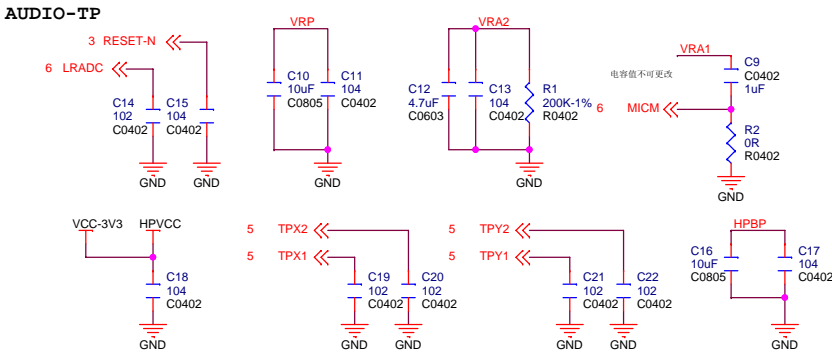
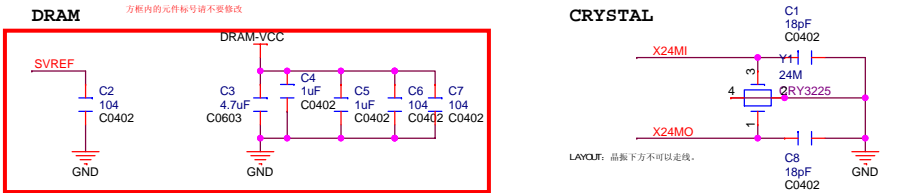
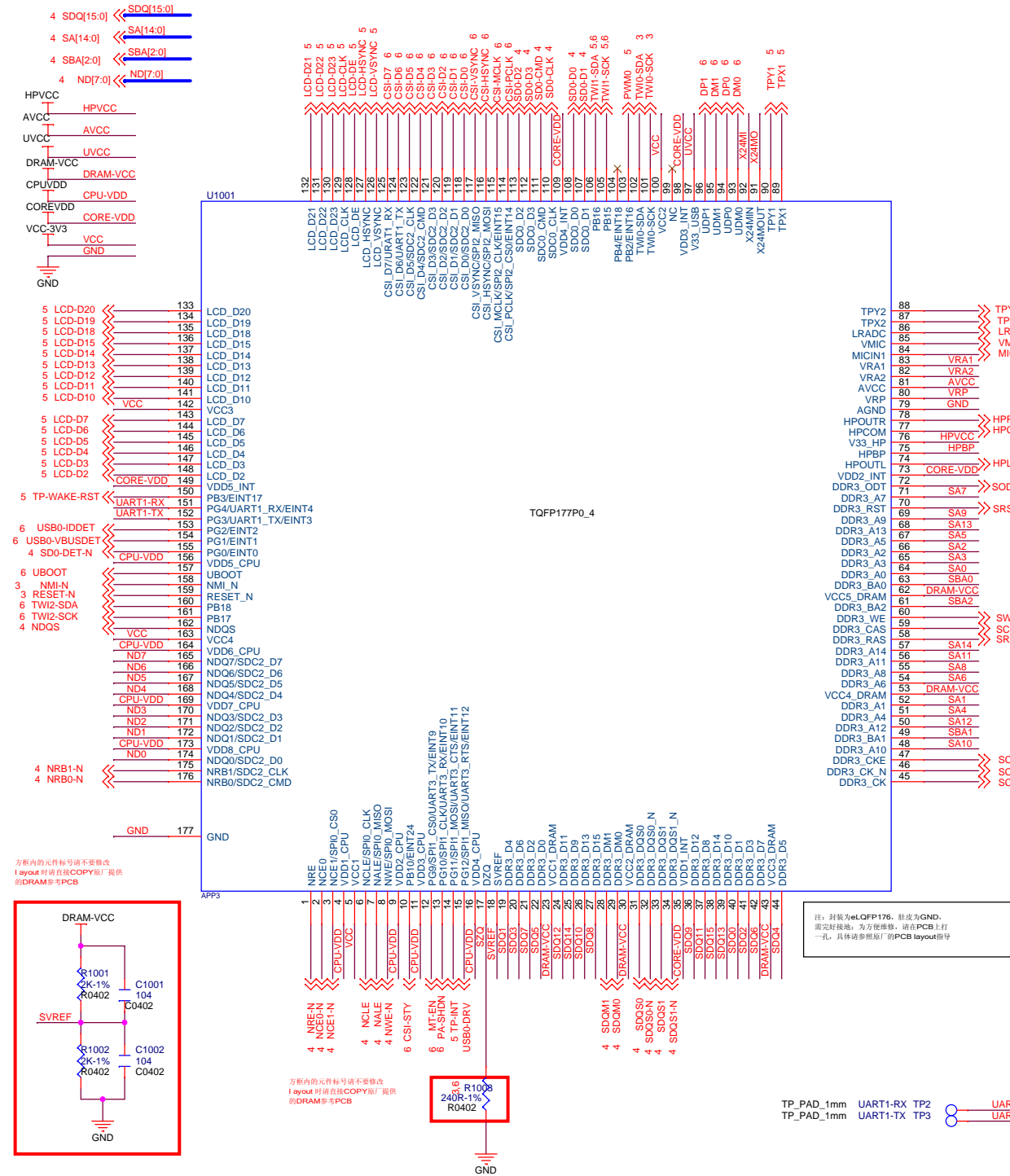
Revision	Description	Date	Drawn	Checked	Approved

POWER TREE



Title		
Size	Document Name	Rev
A3	COVER	
Date:	Sheet	1 of 6

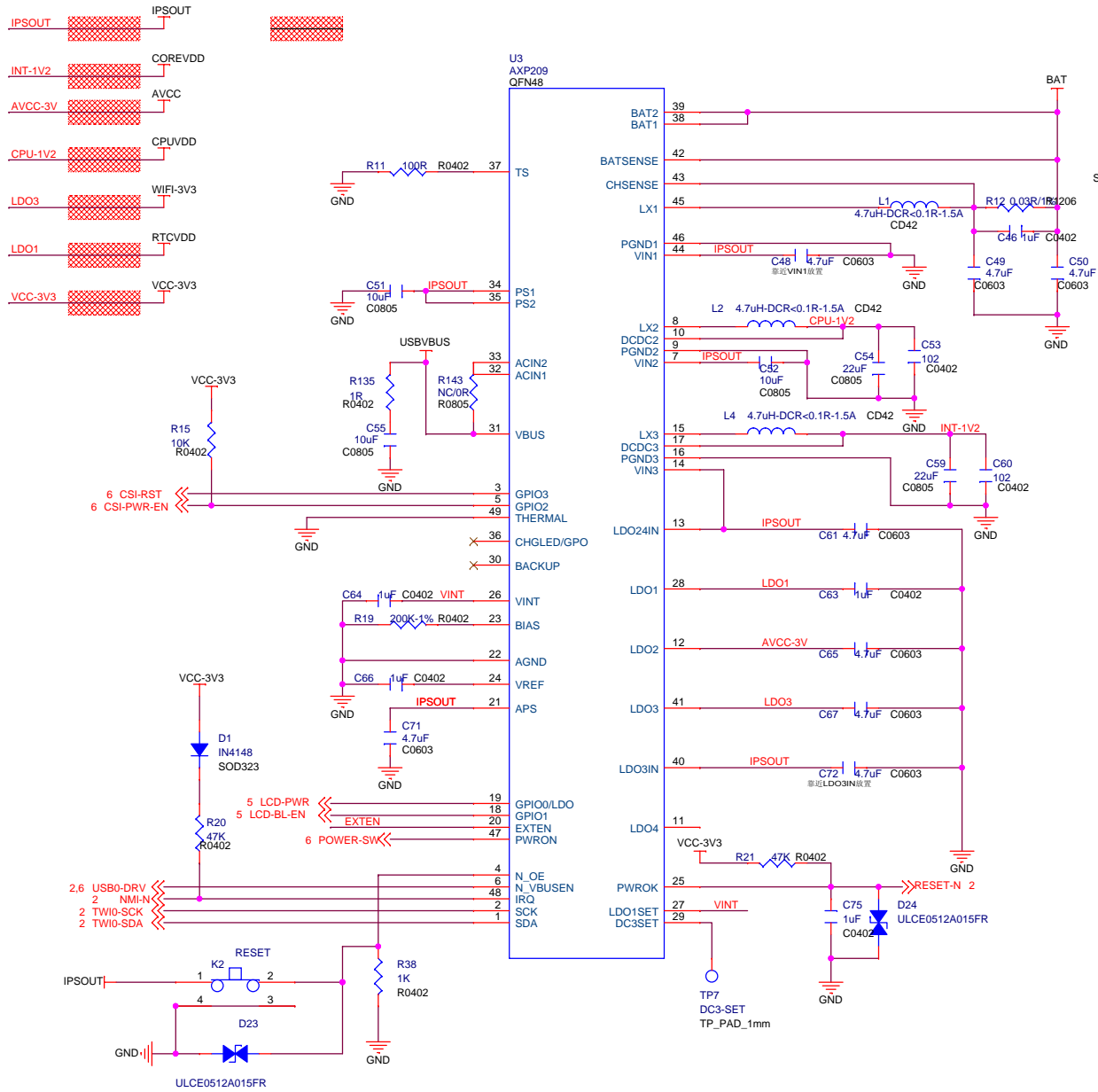
CPU



Title			
APP3_PAD_DDR3			
Size	Document Name		Rev
A3	CPU		
Date:	Sheet		1 of 2

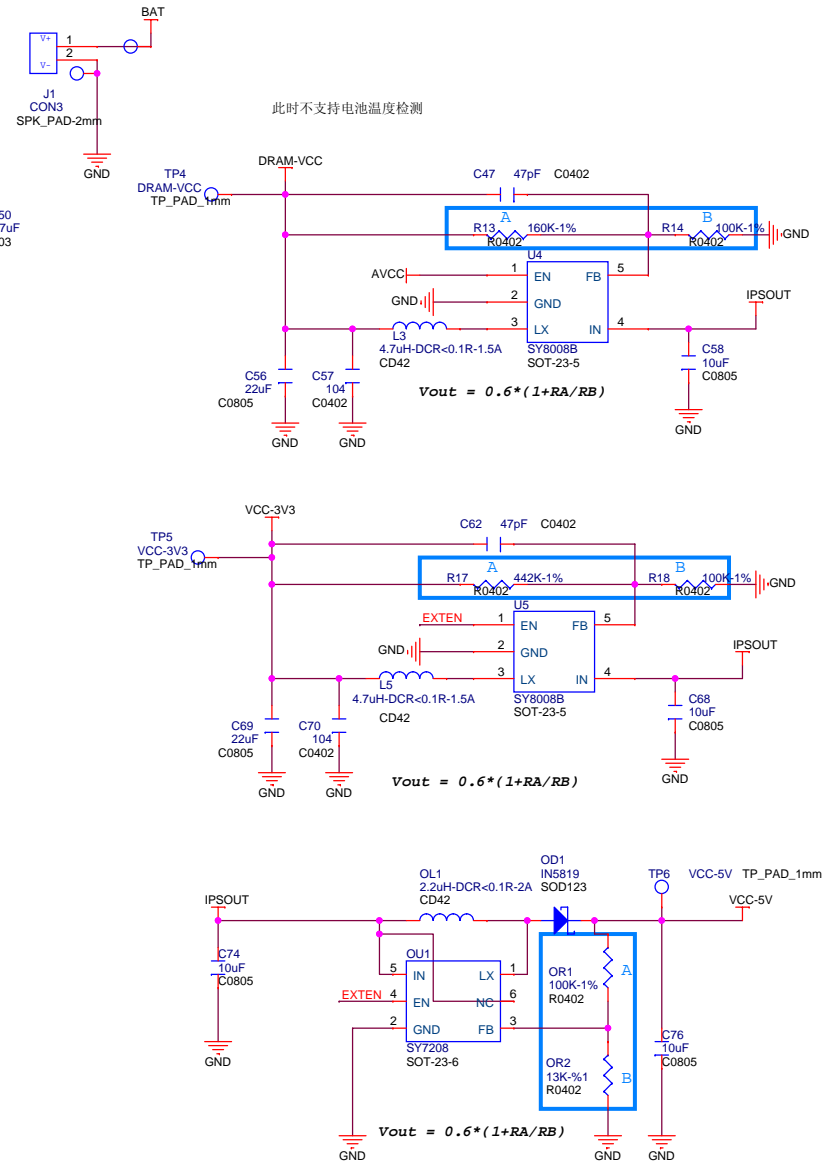
POWER

POWER LINE:Width>=60mil



注: 4.7uH-DCR<0.1R-1.5A

表示在1.5A的电流下电感感量还满足4.7uH, DCR<0.1R表示电感直流电阻要小于0.1欧姆



Title			
Size	Document Name		Rev
A3	POWER		
Date:	Sheet		3 of 6

