Operating systems – supervision 1

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- (1) A general-purpose computer requires a way to store data, a way to process data and a way to do input and output.
- (2) Since most computers work with just high and low values, these are used to encode binary numbers. Hence, binary numbers are used to address memory, and thus, if all of address space is to have a correspondence to memory, the size of memory will be a power of 2.
- (3) Processors usually contain an arithmetic logic unit, providing addition, subtraction, multiplication, division and comparisons for integers of a predefined size.
- (4) Two's complement allows positive and negative numbers to be summed by a standard full adder, saving on the amount of circuitry needed in the ALU. It also means that a separate subtraction circuit isn't needed.
- (5) Little endian means that any number made up of multiple words can be interpreted directly as a base w (where w is the word size) where word 0 gives the w^0 digit, word 1 gives the w^1 digit, &c. Big endian means that, for two's complement integers, the sign of a multi-word number can be deduced from the first word.
- (6) A stack frame will contain an instruction number denoting the part of the program to return to, along with the values for each of its parameters.
- (7) In the average case, the time taken to access a datum is the time taken for the arm to reach the correct radius plus half of the time it takes for the disk to rotate. This hard disk spins at $7200\,\mathrm{r/min}$, giving a latency of $\frac{60}{7200\times2}\,\mathrm{s}$, which is $1/240\,\mathrm{s}$. For a 1 GHz processor, this takes about 4.2 million cycles.

- (8) 2764800000 bits per second (approximately 2.6 Gib/s).
- (9) DMA gives a way for components to access memory without the CPU being in control of the transaction. This frees up CPU time, and is useful if there are a lot of IO requests going on. Because DMA avoids the CPU, it makes attacks targeted on DMA-accessible memory easier. These attacks can be avoided by having, rather than direct access, access via an IOMMU. This limits DMA devices to specific areas of memory.

(10)	attacks	denial of service	confidentiality
	processor	A job takes up all of CPU	-
		time.	
	video card	A job takes up all of GPU	-
		time.	
	memory	A job fills up physical mem-	A process reads memory
		ory.	from outside its allocated
			space.
	storage	A job causes the hard disk to	A process reads stored files it
		be filled up.	shouldn't have access to.
(11)	prevention	denial of service	confidentiality
(11)			Confidentiality
	processor	Use a scheduling method	-
		that doesn't allow one pro-	
		cess to maintain priority.	
	video card	Don't allow processes direct	-
		access to the GPU.	
	memory	Have a large amount of mem-	Have a hardware component
		ory, so that jobs can be inter-	for converting between log-
		rupted before using it up.	ical addresses and physical addresses.
	storage	-	Have the operating system
			be in complete control of file
			IO, and have it manage per-
			missions.

- (12) The new code gets its own PCB, so does not remain part of the old process's PCB after it has been constructed.
- (13) Context switches require register contents to be swapped, which is more expensive than writing to a stack. Context switching often involves IO, but this is not the case for function calls.