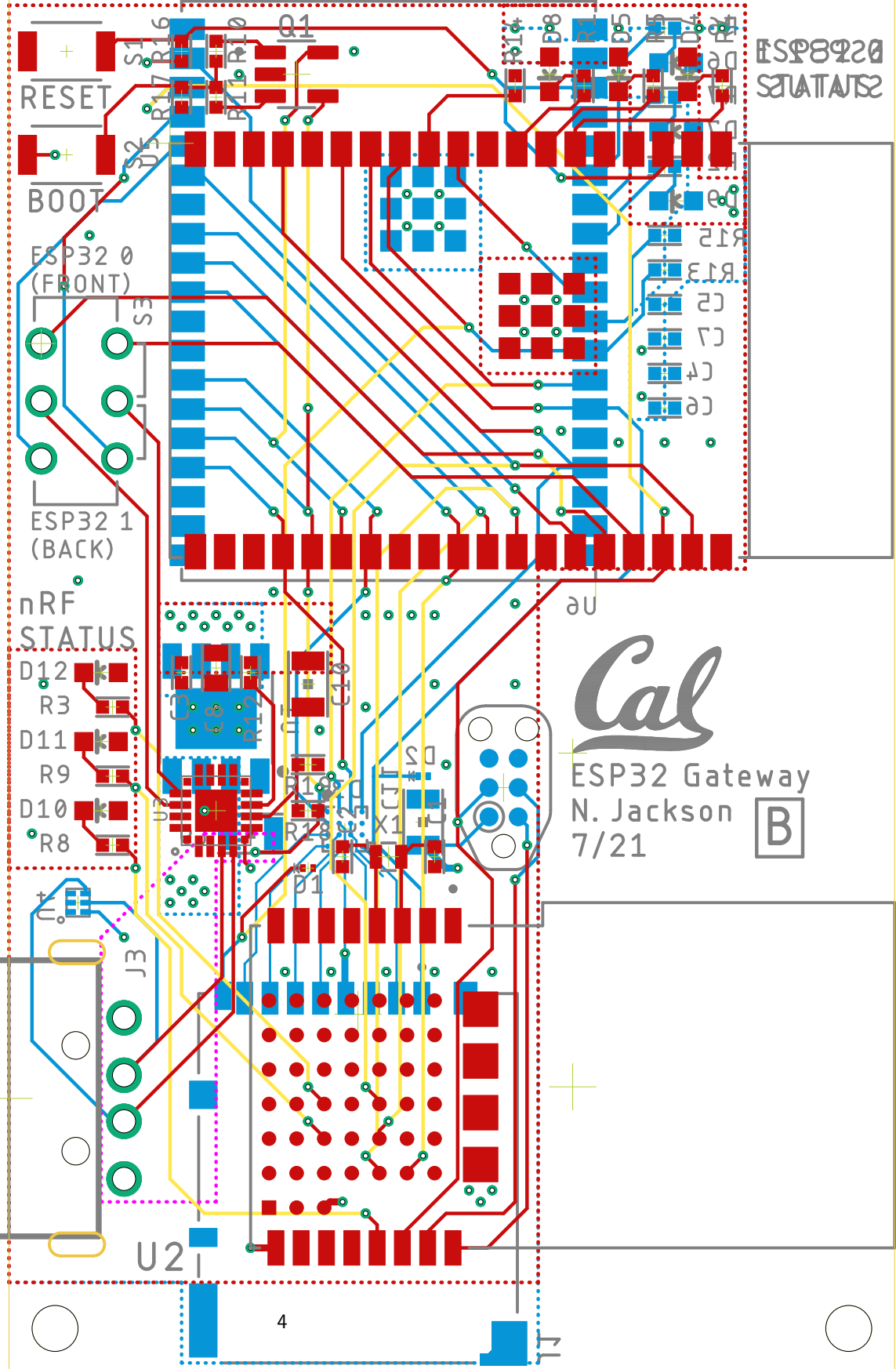
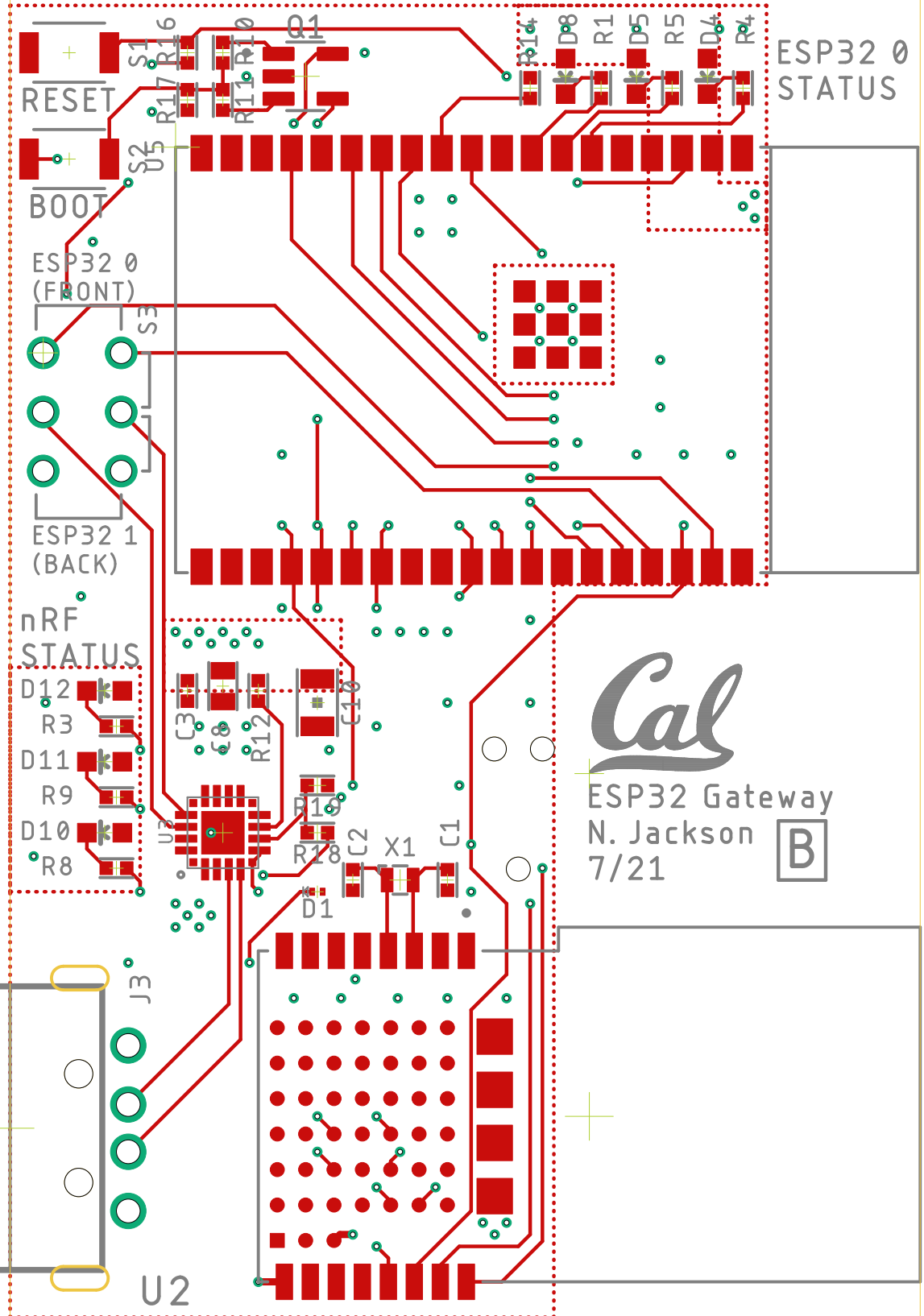


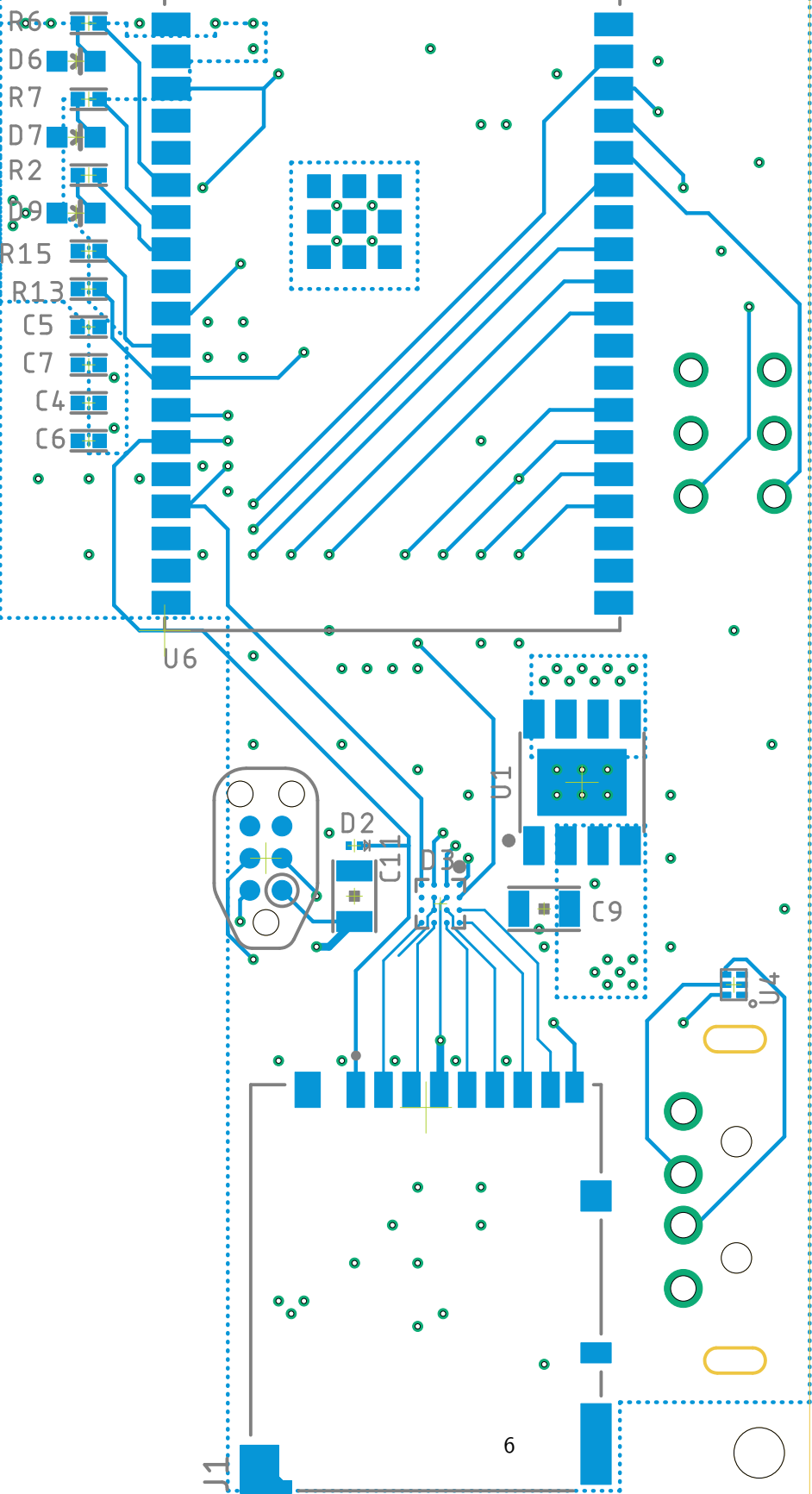
Top and Bottom Layers

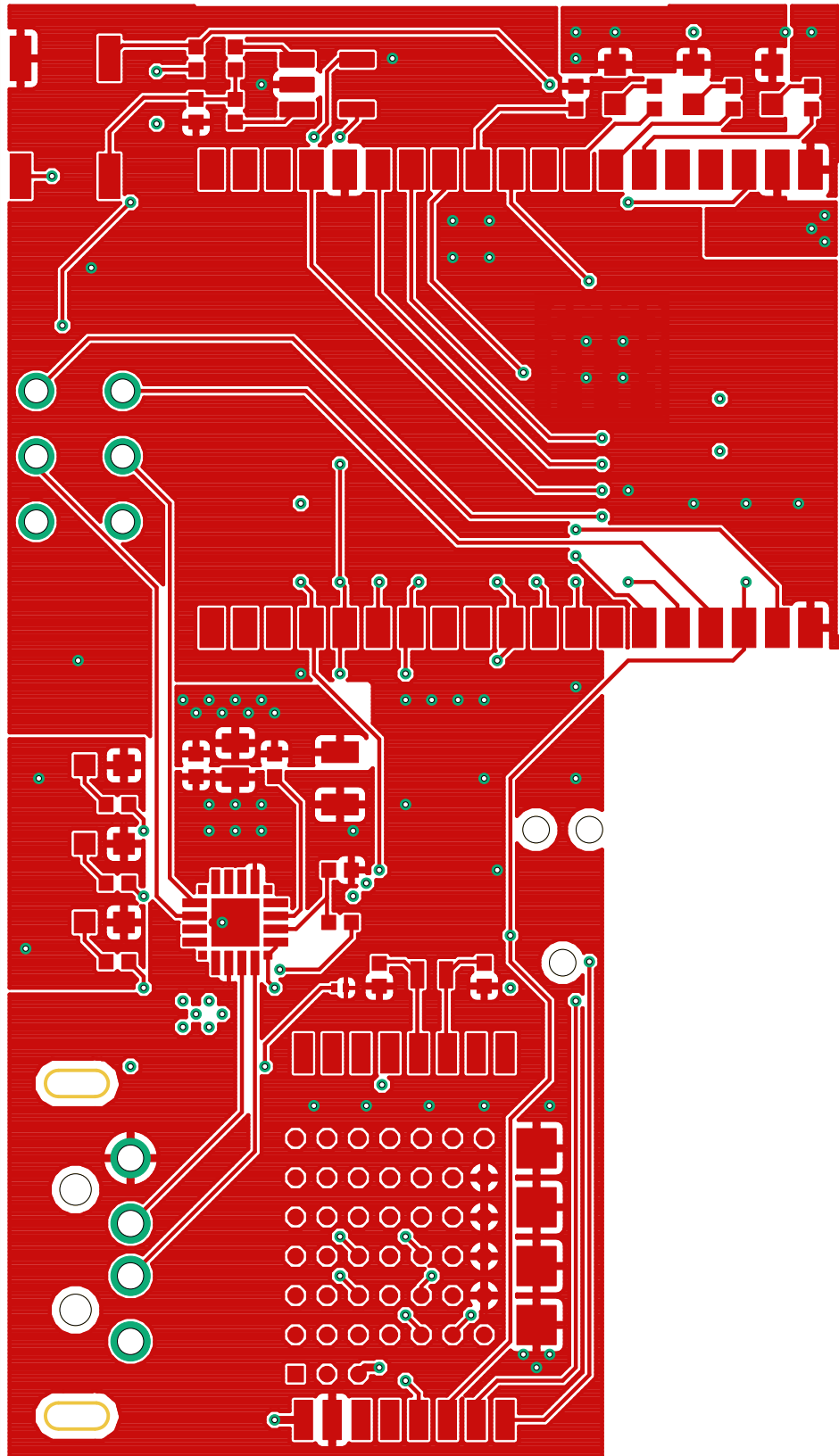


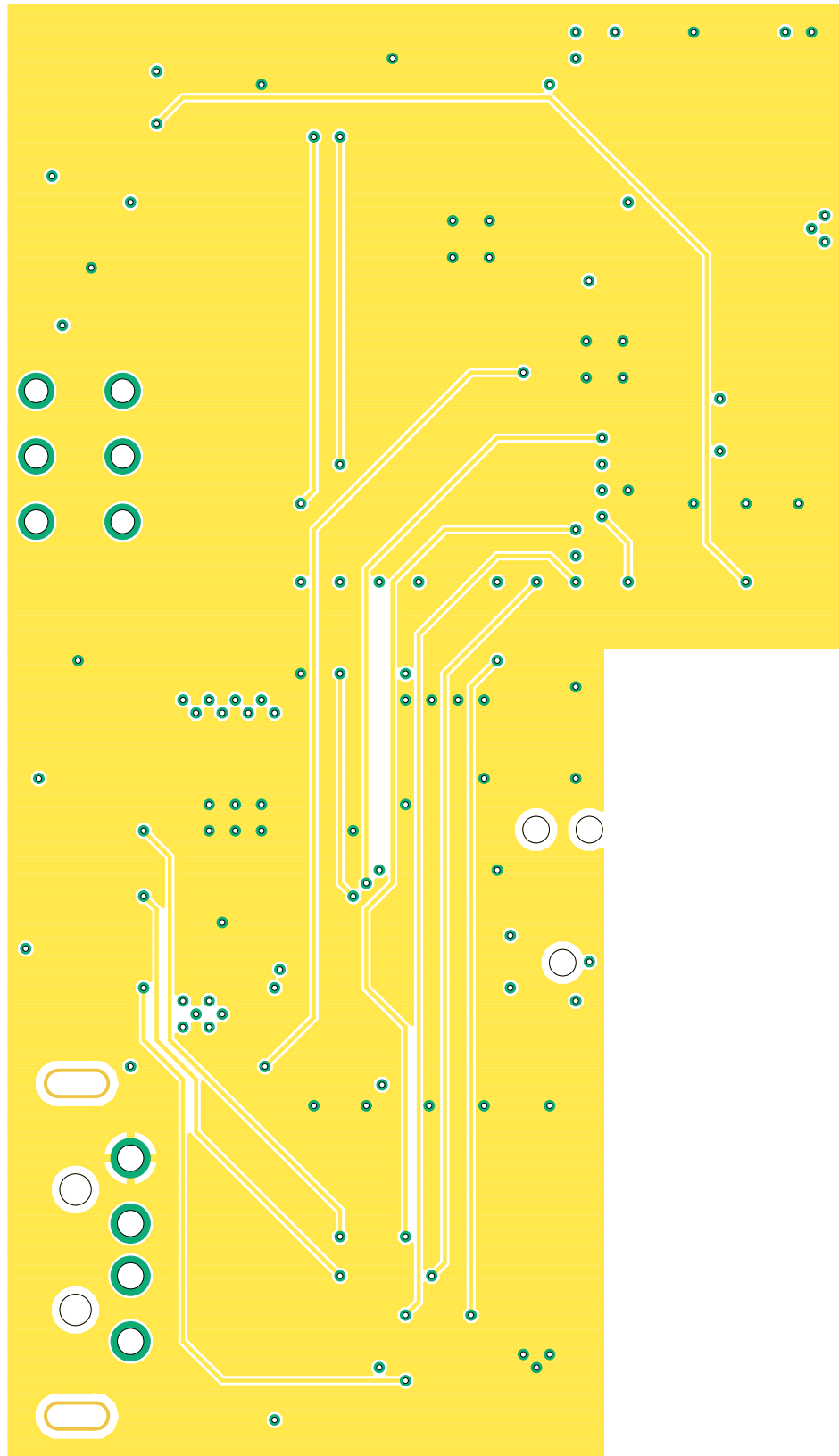


Bottom Layer

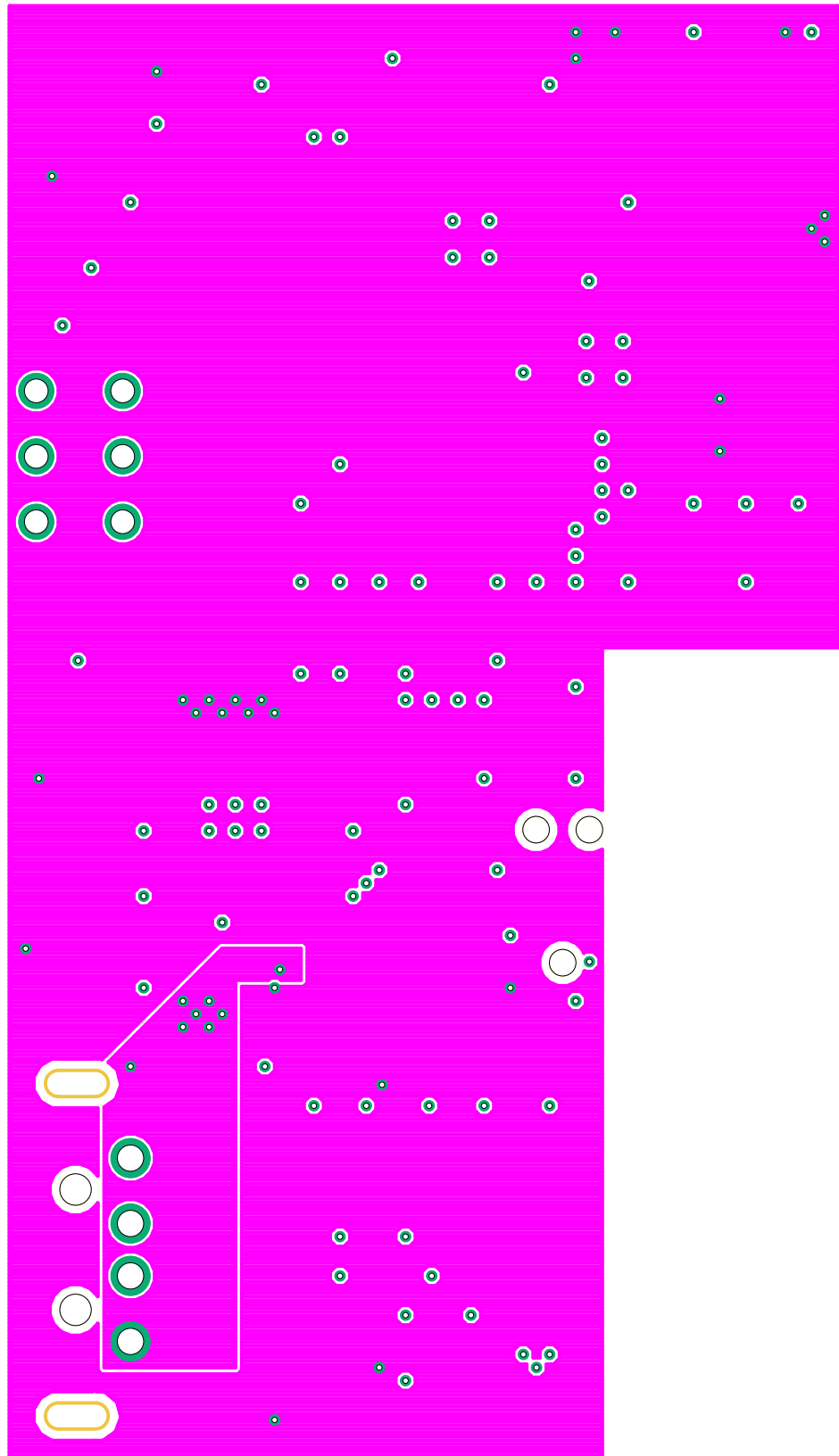
ESP32 1  
STATUS



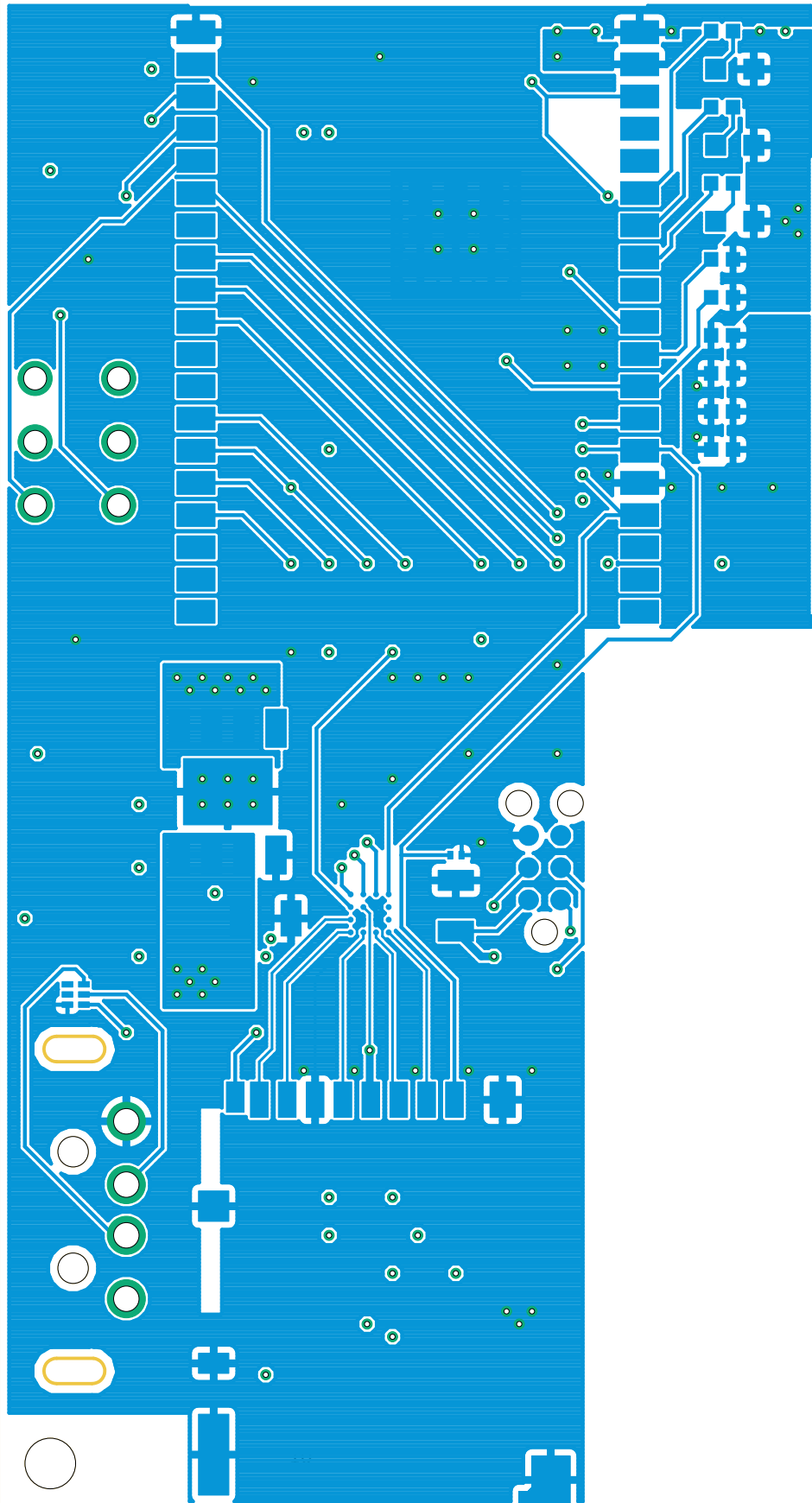


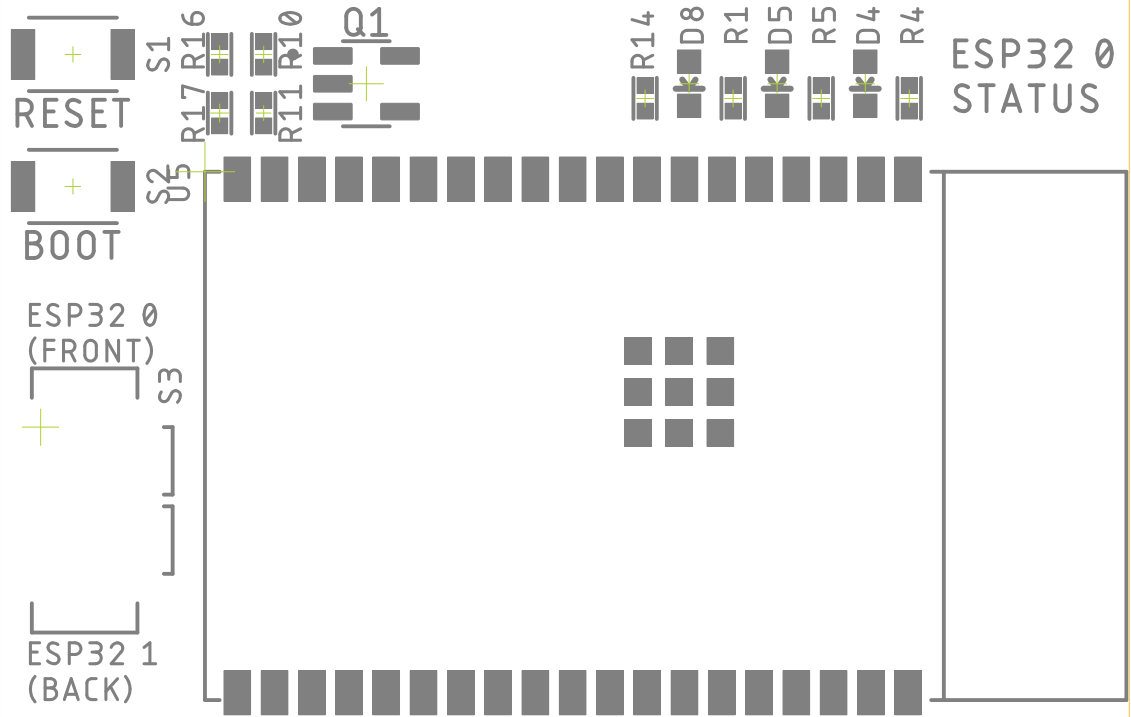






Bottom Copper Layer





*Cal*  
ESP32 Gateway  
N. Jackson  
7/21

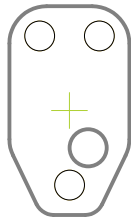


Bottom Paste Layer with Silkscreen

ESP32 1  
STATUS

R6  
D6  
R7  
D7  
R2  
D9  
R15  
R13  
C5  
C7  
C4  
C6

U6



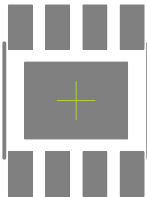
D2



D3



U1



C9

U4

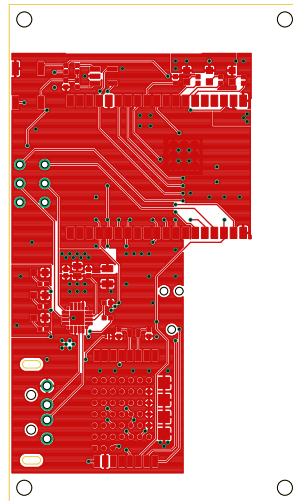


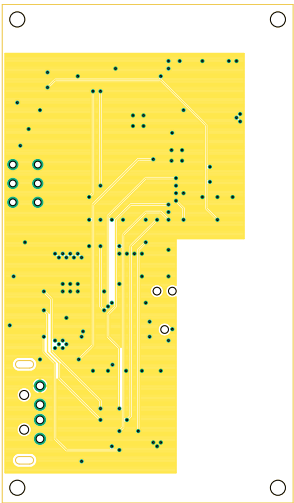
J1

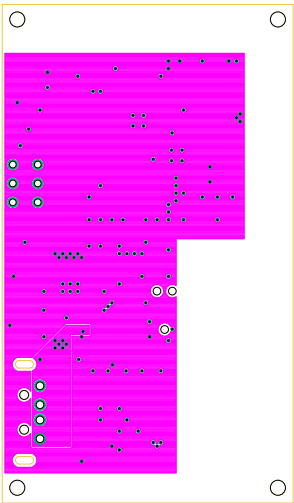
12



## Top Layer 1:1 Scale









## Bottom Copper Layer 1:1 Scale

