SPECIFICATION FOR LCD MODULE

Model No. ___TM162AAA6-1___

Prepared by:	**Date:**
Checked by :	**Date:**
Verified by :	**Date:**
Approved by:	**Date:**

TIANMA MICROELECTRONICS CO., LTD

REVISION RECORD

Date	Ref. Page	Revision No.	Revision Items	Check & Approval

1. General Specifications:

1.1 Display type: TN

1.2 Display color*:

      Display color: Blue-Black

      Background: White

1.3 Polarizer mode: Reflective/Positive

1.4 Viewing Angle: 6:00

1.5 Driving Method: 1/16 Duty 1/5 Bias

1.6 Backlight: None

1.7 Display Fonts: 5 x 7 dots + Cursor (1 Character)

1.8 Controller: HCD66701A00

1.9 Data Transfer: 8 Bit Parallel

1.10 Operating Temperature: 0----+50℃

      Storage Temperature: -20----+60℃

1.11 Outline Dimensions: Refer to outline drawing on next page

1.12 Dot Matrix: 16 Characters X 2 Lines

1.13 Dot Size: 0.55X0.50(mm)

1.14 Dot Pitch: 0.60X0.55 (mm)

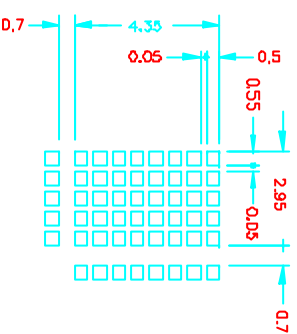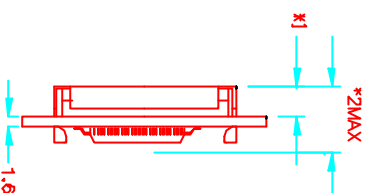1.15 Weight: 20g

1.16 PCB edtion: TM162AD P1-2

    * Color tone is slightly changed by temperature and driving voltage.
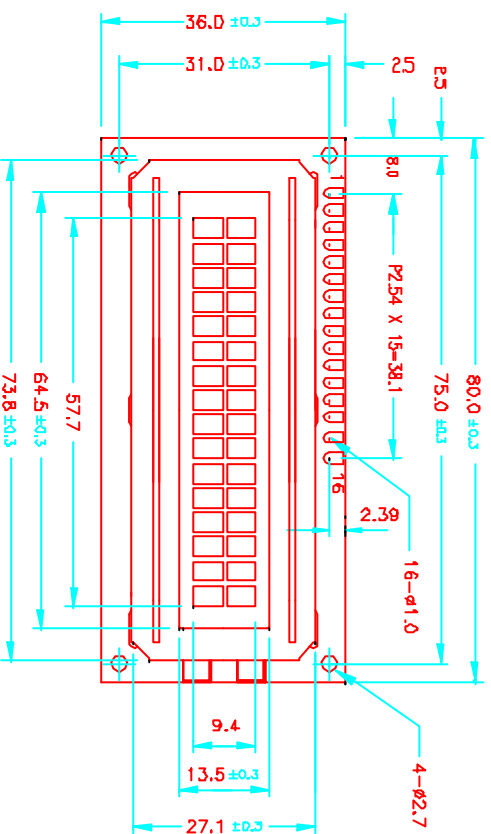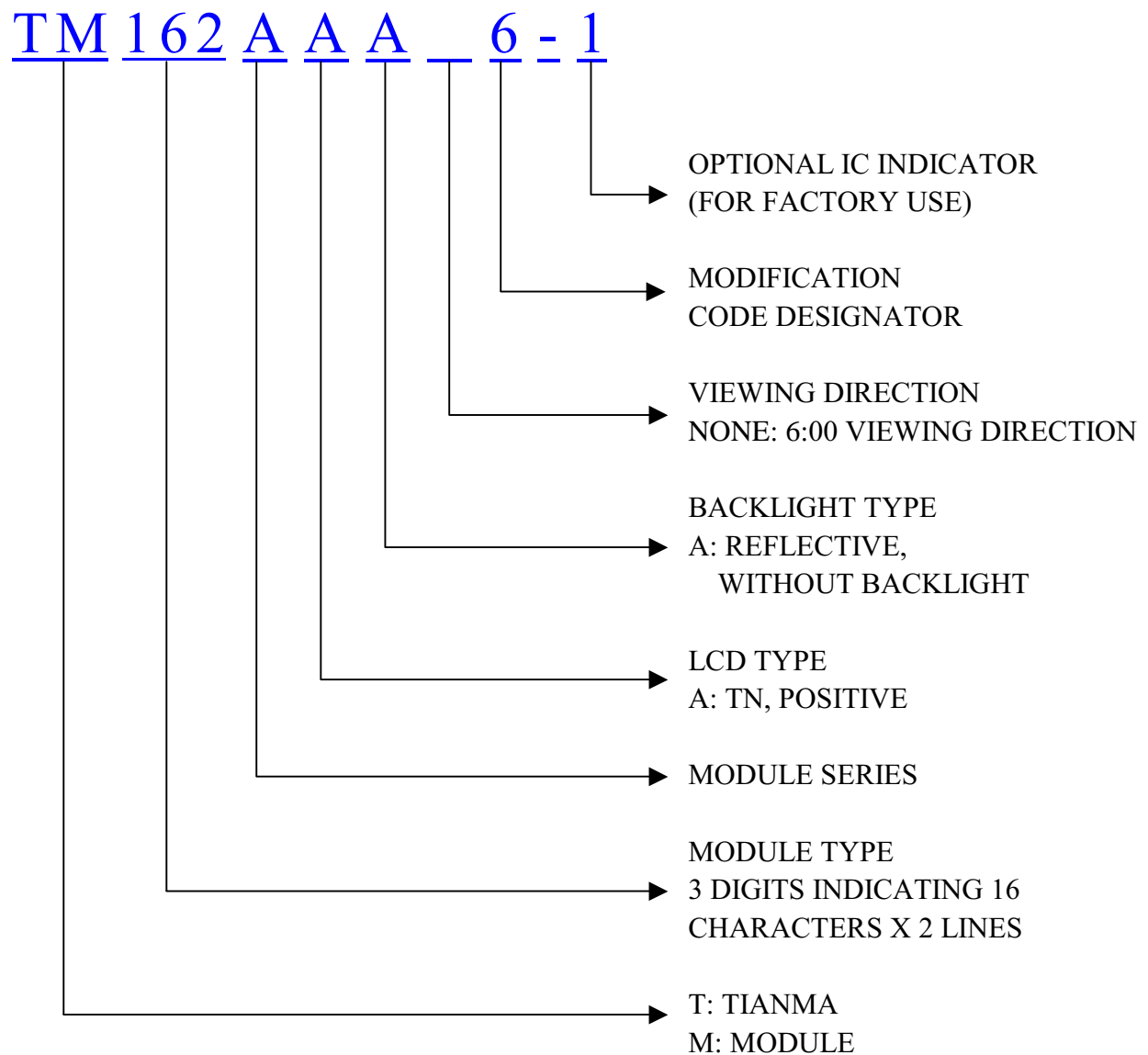
1	Vss		
2	Vcc		
3	Vee		
4	RS		
5	R/W		
6	E		
7	DB0		
8	DB1		
9	DB2		
10	DB3		
11	DB4		
12	DB5		
13	DB6		
14	DB7		
15	LED+		
16	LED−		

TYPE	*1	*2
R&EL	4.5	11.0
LED	7.6	15.0

3. LCD Module Part Numbering System

TM 162 A A A _ 6 - 1

- → OPTIONAL IC INDICATOR (FOR FACTORY USE)
- → MODIFICATION CODE DESIGNATOR
- → VIEWING DIRECTION NONE: 6:00 VIEWING DIRECTION
- → BACKLIGHT TYPE A: REFLECTIVE, WITHOUT BACKLIGHT
- → LCD TYPE A: TN, POSITIVE
- → MODULE SERIES
- → MODULE TYPE 3 DIGITS INDICATING 16 CHARACTERS X 2 LINES
- → T: TIANMA M: MODULE

4. Circuit Block Diagram

DB7 ~ DB0
E
R/W
RS
Vee
Vcc
Vss

HCD66701A00
CONTROL LSI

SEG80

COM16

LCD PANEL
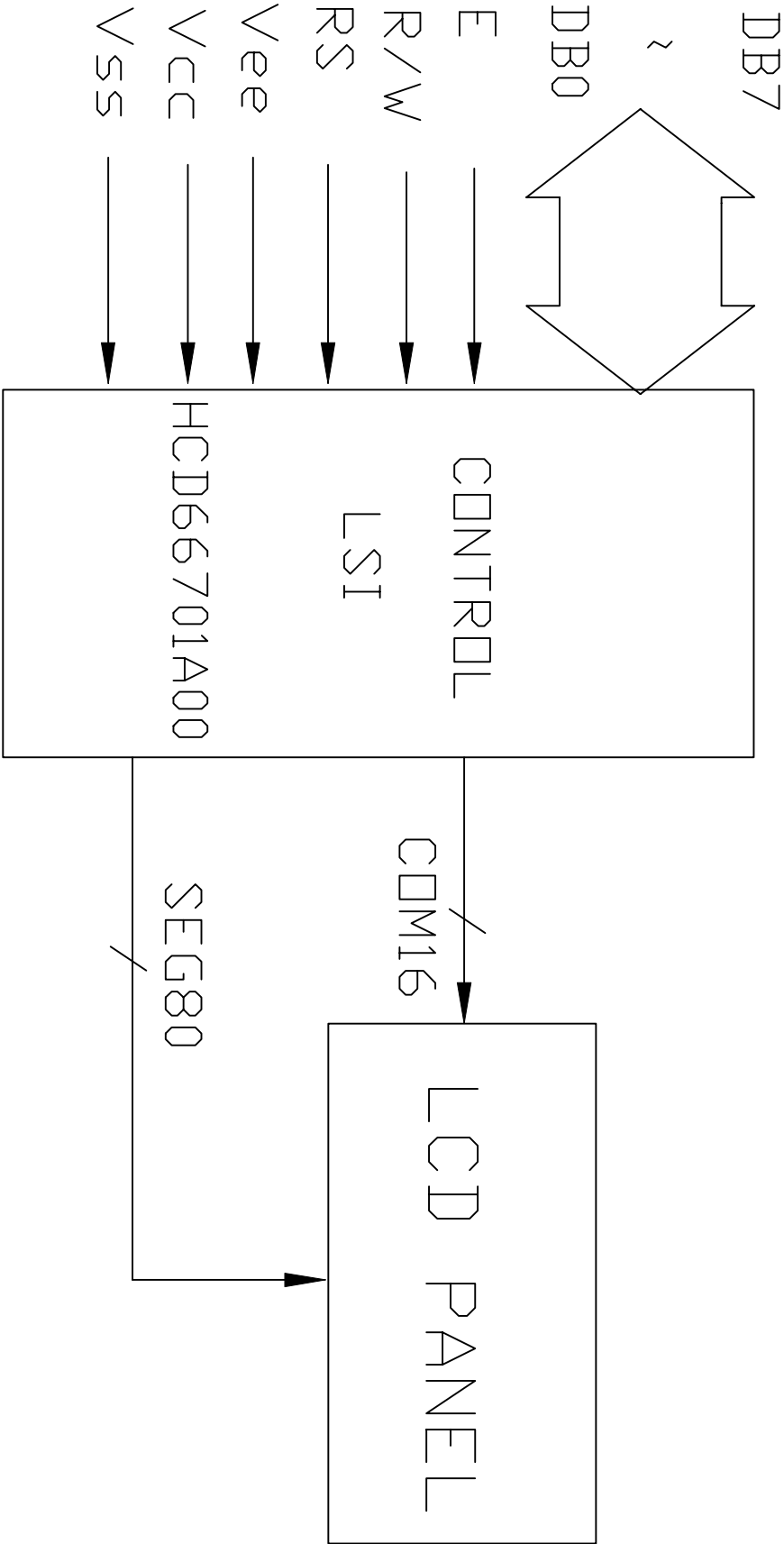
5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	$V_{DD} - V_{SS}$	-0.3	7.0	V	
LCD Driving Voltage	V_{LCD}	-0.3	13.0		
Operating Temperature Range	T_{OP}	0	+50	℃	No Condensation
Storage Temperature Range	T_{ST}	-20	+60		

6. Electrical Specifications and Instruction Code

6.1 Electrical characteristics

Item		Symbol	Min.	Typ.	Max.	Unit
Supply Voltage (Logic)		$V_{DD}-V_{SS}$	4.5	5.0	5.5	V
Supply Voltage (LCD Drive)		V_{LCD}	-	4.7	-	V
Input Signal Voltage	High	V_{IH} （V_{DD}=5.0）	$0.7V_{DD}$	-	V_{DD}+0.3	V
	Low	V_{IL} （V_{DD}=5.0）	-0.3	-	$0.2\,V_{DD}$	V
Supply current (Logic)		I_{DD} （$V_{DD}-V_{SS}$ =5.0）	-	1.5	2.0	mA
Supply current (LCD Drive)		I_{EE}	-	0.40	0.6	mA

6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	Vss	0V	Ground
2	Vcc	5.0V	Power supply voltage for logic and LCD(+)
3	Vee	0.3V	Power supply voltage for LCD(-)
4	RS	H/L	Selects registers
5	R/W	H/L	Selects read or write
6	E	H/L	Starts data read/write
7	DB0	H/L	Data bit0
8	DB1	H/L	Data bit1
9	DB2	H/L	Data bit2
10	DB3	H/L	Data bit3
11	DB4	H/L	Data bit4
12	DB5	H/L	Data bit5
13	DB6	H/L	Data bit6
14	DB7	H/L	Data bit7
15	NC	-	No signal
16	NC	-	No signal

6.3 Interface Timing Chart

AC Characteristics (V_{CC} = 2.7 to 5.5V, T_a = –30 to +75°C*³)

Write Operation

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Enable cycle time	t_{cycE}	1000	—	—	ns	
Enable pulse width (high level)	PW_{EH}	450	—	—		
Enable rise/fall time	t_{Er}, t_{Ef}	—	—	25		
Address set-up time (RS, R/$\overline{W}$ to E)	t_{AS}	40	—	—		
Address hold time	t_{AH}	20	—	—		
Data set-up time	t_{DSW}	195	—	—		
Data hold time	t_{H}	10	—	—		

Read Operation

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Enable cycle time	t_{cycE}	1000	—	—	ns	
Enable pulse width (high level)	PW_{EH}	450	—	—		
Enable rise/fall time	t_{Er}, t_{Ef}	—	—	25		
Address set-up time (RS, R/$\overline{W}$ to E)	t_{AS}	40	—	—		
Address hold time	t_{AH}	20	—	—		
Data delay time	t_{DDR}	—	—	350		
Data hold time	t_{DHR}	10	—	—		

Timing Characteristics
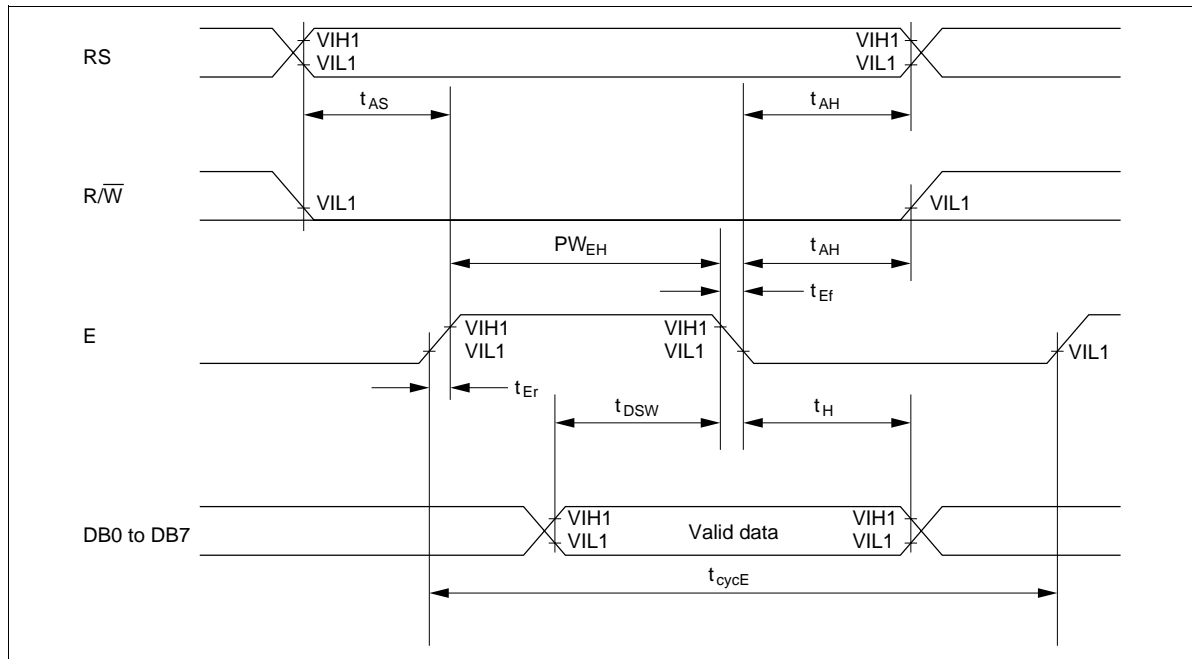
Write Operation
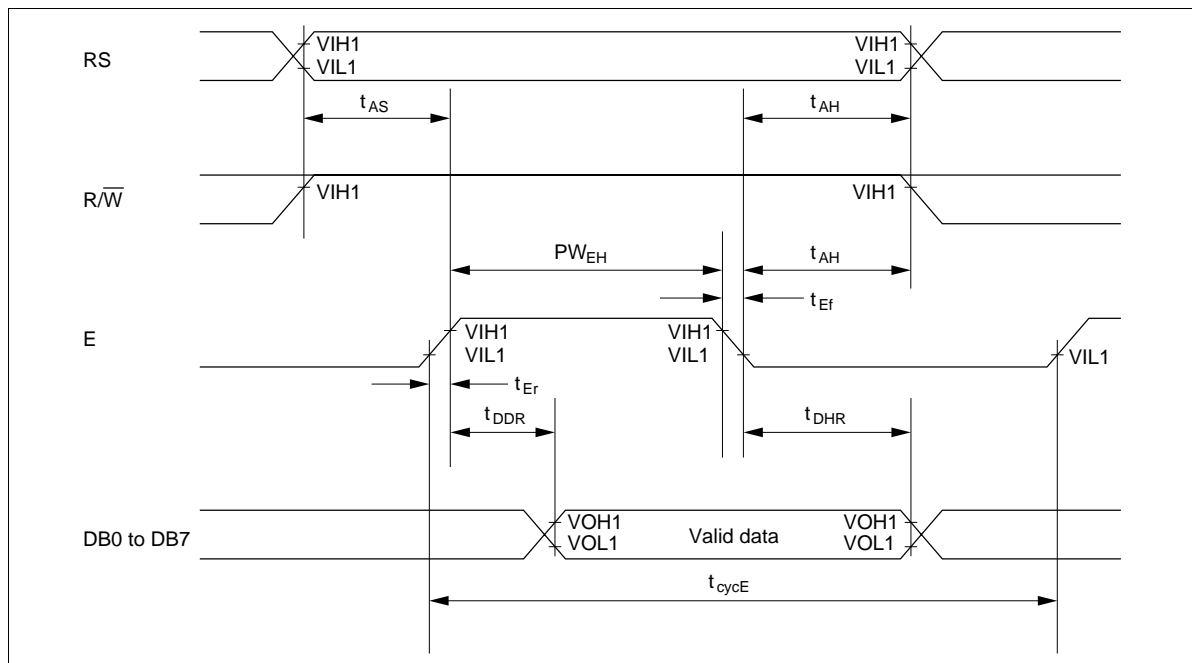
Read Operation

6.4 Instruction Code

Instruction	RS	R/W̄	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	Execution Time (max) (when f_{cp} or f_{osc} is 320 kHz)
Clear display	0	0	0	0	0	0	0	0	0	1	Clears entire display and sets DDRAM address 0 in address counter.	1.28 ms
Return home	0	0	0	0	0	0	0	0	1	—	Sets DDRAM address 0 in address counter. Also returns display from being shifted to original position. DDRAM contents remain unchanged.	1.28 ms
Entry mode set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.	31 µs
Display on/off control	0	0	0	0	0	0	1	D	C	B	Sets entire display (D) on/off, cursor on/off (C), and blinking of cursor position character (B).	31 µs
Cursor or display shift	0	0	0	0	0	1	S/C	R/L	—	—	Moves cursor and shifts display without changing DDRAM contents.	31 µs
Function set	0	0	0	0	1	DL	N	F	—	—	Sets interface data length (DL), number of display lines (L), and character font (F).	31 µs
Set CGRAM address	0	0	0	1	ACG	ACG	ACG	ACG	ACG	ACG	Sets CGRAM address. CGRAM data is sent and received after this setting.	31 µs
Set DDRAM address	0	0	1	ADD	ADD	ADD	ADD	ADD	ADD	ADD	Sets DDRAM address. DDRAM data is sent and received after this setting.	31 µs
Read busy flag & address	0	1	BF	AC	AC	AC	AC	AC	AC	AC	Reads busy flag (BF) indicating internal operation is being performed and reads address counter contents.	0 µs

Instruction	RS	R/$\overline{W}$	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	Execution Time (max) (when f_{cp} or f_{OSC} is 320 kHz)
					Code							
Write data to CG or DDRAM	1	0	Write data								Writes data into DDRAM or CGRAM.	31 μs t_{ADD} = 4.7 μs*
Read data from CG or DDRAM	1	1	Read data								Reads data from DDRAM or CGRAM.	31 μs t_{ADD} = 4.7 μs*

I/D = 1: Increment
I/D = 0: Decrement
S = 1: Accompanies display shift
S/C = 1: Display shift
S/C = 0: Cursor move
R/L = 1: Shift to the right
R/L = 0: Shift to the left
DL = 1: 8 bits, DL = 0: 4 bits
N = 1: 2 lines, N = 0: 1 line
F = 1: 5 × 10 dots, F = 0: 5 × 7 dots
BF = 1: Internally operating
BF = 0: Instructions acceptable

DDRAM: Display data RAM
CGRAM: Character generator RAM
ACG: CGRAM address
ADD: DDRAM address (corresponds to cursor address)
AC: Address counter used for both DD and CGRAM addresses

Execution time changes when frequency changes
Example:
When f_{cp} or f_{OSC} is 270 kHz,
$31\ \mu s \times \dfrac{320}{270} = 37\ \mu s$

Note: — indicates no effect.

* After execution of the CGRAM/DDRAM data write or read instruction, the RAM address counter is incremented or decremented by 1. The RAM address counter is updated after the busy flag turns off. In Figure 10, t_{ADD} is the time elapsed after the busy flag turns off until the address counter is updated.
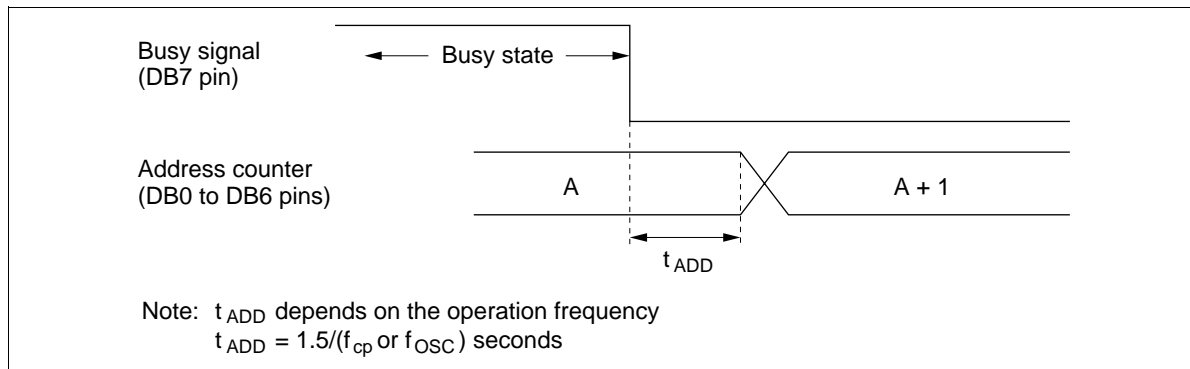

Note: t_{ADD} depends on the operation frequency
t_{ADD} = 1.5/(f_{cp} or f_{OSC}) seconds

Figure 10   Address Counter Update

6.5 Character generator ROM(HCD66701A00)

Upper 4 Bits / Lower 4 Bits	0000	0010	0011	0100	0101	0110	0111	1010	1011	1100	1101	1110	1111
xxxx0000	CG RAM (1)												
xxxx0001	(2)												
xxxx0010	(3)												
xxxx0011	(4)												
xxxx0100	(5)												
xxxx0101	(6)												
xxxx0110	(7)												
xxxx0111	(8)												
xxxx1000	(1)												
xxxx1001	(2)												
xxxx1010	(3)												
xxxx1011	(4)												
xxxx1100	(5)												
xxxx1101	(6)												
xxxx1110	(7)												
xxxx1111	(8)												

Note: The user can specify any pattern for character-generator RAM.

7. Optical Characteristics

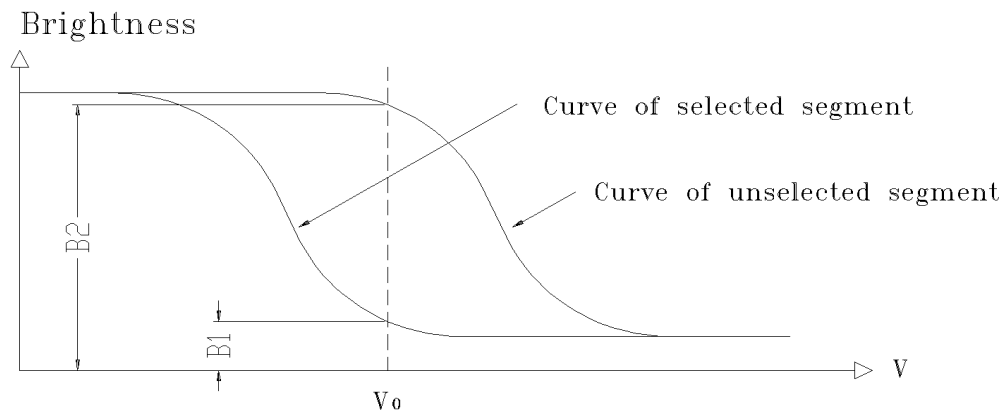7.1 Optical Characteristics   Ta=25℃

Item		Symbol	Condition		Min.	Typ.	Max.	Unit
Viewing Angle		θ_x	$Cr{\geq}2$	$\theta_y{=}0°$	35	--	10	Deg
		θ_y		$\theta_x{=}0°$	-30	--	30	
Contrast Ratio		Cr	$\theta_x{=}20°$ $\theta_y{=}0°$		3.0	-	-	
Response Time	Turn on	T_{on}	$\theta_x{=}20°$ $\theta_y{=}0°$		-	-	150	ms
	Turn off	T_{off}			-	-	150	

7.2 Definition of Optical Characteristics

7.2.1 Definition of Viewing Angle

Top   Top

Bottom   Bottom
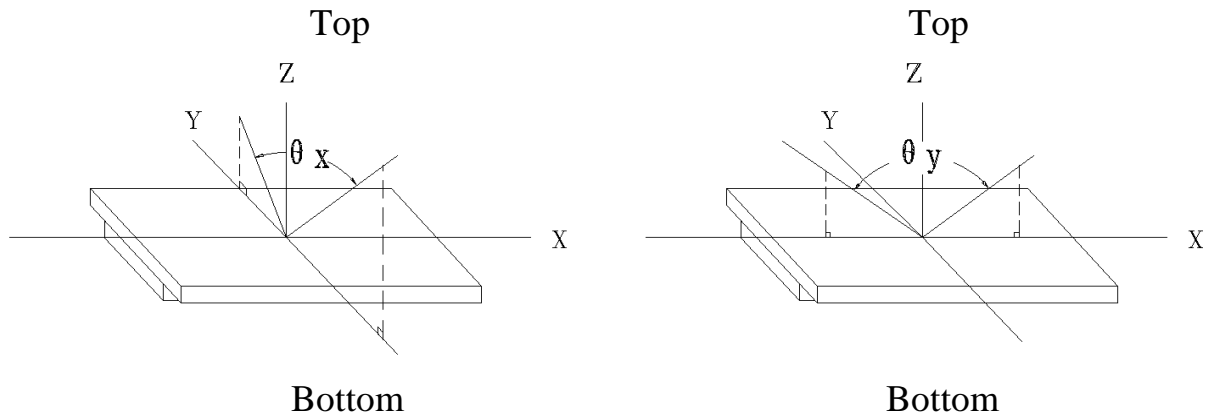
7.2.2 Definition of Contrast Ratio

$$\text{Contrast Ratio} = B2/B1 = \frac{\text{unselected state brightness}}{\text{selected state brightness}}$$

Measuring Conditions:

    1) Ambient Temperature: 25℃;   2) Frame frequency: 64Hz

7.2.3 Definition of Response time
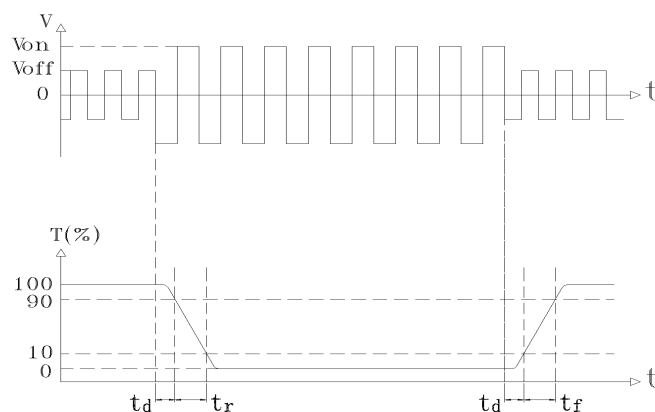
Turn on time: $t_{on} = t_d + t_r$ Turn off time: $t_{off} = t_d + t_f$

Measuring Condition:

    1) Operating Voltage: 4.7V   2) Frame frequency: 64Hz

8. Reliability

8.1 Content of Reliability Test   Ta=25℃

No.	Test Item	Content of Test	Test condition
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	60℃ 96H
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	-20℃ 96H
3	High Temperature Operation	Endurance test applying the electric stress (voltage ＆ current) and the thermal stress to the element for a long time	50℃ 96H
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	0℃ 96H
5	High Temperature /Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time	40℃ 90%RH 96H
6	Temperature Cycle	Endurance test applying the low and high temperature cycle -20℃⟷25℃⟷60℃⟷25℃ 30min　5min　30min　5min ⟵──────────────⟶ 1 cycle	-20℃/60℃ 10 cycles
7	Vibration Test (package state)	Endurance test applying the vibration during transportation	10Hz~150Hz, 50m/s^2, 40min
8	Shock Test (package state)	Endurance test applying the shock during transportation	Half- sine wave, 100m/s^2, 11ms
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	40kPa 16H

8.2 Failure Judgment Criterion

Criterion Item	Test Item No.									Failure Judgement Criterion
	1	2	3	4	5	6	7	8	9	
Basic Specification	√	√	√	√	√	√	√	√	√	Out of the basic Specification
Electrical specification	√	√	√	√	√					Out of the electrical specification
Mechanical Specification							√	√		Out of the mechanical specification
Optical Characteristic	√	√	√	√	√	√			√	Out of the optical specification
Note	For test item refer to 8.1									
Remark	Basic specification = Optical specification + Mechanical specification									

9. QUALITY LEVEL

Examination or Test	At Ta=25℃ (unless otherwise stated)	Inspection				
		Min.	Max.	Unit	IL	AQL
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See Appendix A			II	Major 1.0 Minor 2.5
Display Defects	Under normal illumination and eyesight condition, display on inspection.	See Appendix B			II	Major 1.0 Minor 2.5
Note: Major defects: Open segment or common, Short, Serious damages, Leakage 　　　Miner defects: Others 　　　Sampling standard conforms to GB2828						

10. Precautions for Use of LCD Modules

10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

— Isopropyl alcohol
— Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

— Water
— Ketone
— Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

a. Be sure to ground the body when handling the LCD Modules.

b. Tools required for assembly, such as soldering irons, must be properly ground.

c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:
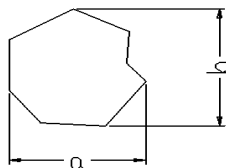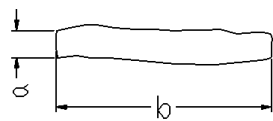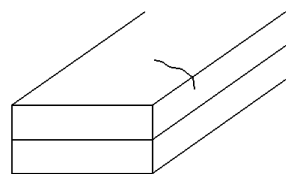
           Temperature : $0℃ \sim 40℃$

           Relatively humidity: $\leqslant 80\%$

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.
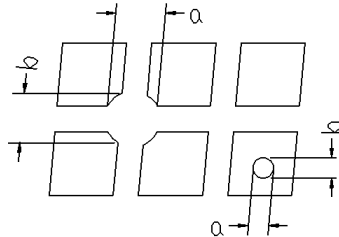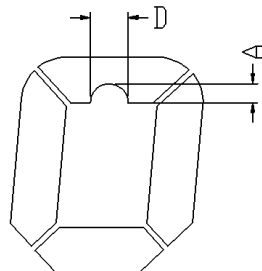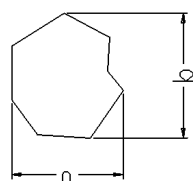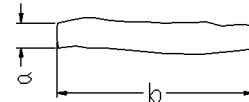
Appendix A

Inspection items and criteria for appearance defects

Items	Contents	Criteria			
Leakage		Not permitted			
Rainbow		According to the limit specimen			
Polarizer	Wrong polarizer attachment	Not permitted			
	Bubble between polarizer and glass	Not counted		Max. 3 defects allowed	
		$\phi<0.3$mm		0.3mm$\leqslant\phi\leqslant$0.5mm	
	Scratches of polarizer	According to the limit specimen			
Black spot (in viewing area)		Not counted	Max. 3 spots allowed		Max. 3 spots (lines) allowed
		X<0.2mm	0.2mm$\leqslant$X$\leqslant$0.5mm		
		X=(a+b)/2			
Black line (in viewing area)		Not counted	Max. 3 lines allowed		
		a<0.02mm	0.02mm$\leqslant$a$\leqslant$0.05mm b$\leqslant$2.0mm		
Progressive cracks		Not permitted			

Appendix B

Inspection items and criteria for display defects

Items	Contents	Criteria		
Open segment or open common		Not permitted		
Short		Not permitted		
Wrong viewing angle		Not permitted		
Contrast radio uneven		According to the limit specimen		
Crosstalk		According to the limit specimen		
Pin holes and cracks in segment (DOT)		Not counted	Max.3 dots allowed	Max.3 dots allowed
		X<0.1mm	0.1mm≤X≤0.2mm	
		X=(a+b)/2		
		Not counted	Max.2 dots allowed	
		A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm	
Black spot (in viewing area)		Not counted	Max.3 spots allowed	Max.3 spots (lines) allowed
		X<0.1mm	0.1mm≤X≤0.2mm	
		X=(a+b)/2		
Black line (in viewing area)		Not counted	Max.3 lines allowed	
		a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm	