Sheet1

			Data Transfer Type								
	AM	XAM	D08_0	D08_1	D08_2	D08_3	D16_12	D16_23	D32	D64	
CR/CSR	0x2f	1	V	V	V	V	V	V	V	Err	
A16_S	0x29	1	V	V	V	V	V	V	V	Err	
A24_S	0x39	1	V	V	V	V	V	V	V	Err	
A24_BLT	0x3b	I	Err	Err	Err	Err	Err	Err	V	Err	
A24_MBLT	0x38	1	Err	Err	Err	Err	Err	Err	Err	V	
A32_S	0x09	1	V	V	V	V	V	V	V	Err	
A32_BLT	0x0b	1	Err	Err	Err	Err	Err	Err	V	Err	
A32_MBLT	0x08	1	Err	Err	Err	Err	Err	Err	Err	V	
A64_S	0x01	1	V	V	V	V	V	V	V	Err	
A64_BLT	0x03	I	Err	Err	Err	Err	Err	Err	V	Err	
A64_MBLT	0x00	1	Err	Err	Err	Err	Err	Err	Err	V	
2eVME-A32	0x20	0x01	l l	1	1	1	I	1	I	1	
2eVME-A64	0x20	0x02	1	1	1	1	I	I	1	1	
2eSST-A32	0x20	0x11	l l	1	1	1	I	1	I	1	
2eSST-A64	0x20	0x12	1	1	1	1	I	I	1	1	
A16_S_sup	0x2d	1	V	V	V	V	V	V	V	Err	
A24_S_sup	0x3d	I	V	V	V	V	V	V	V	Err	
A24_BLT_sup	0x3f	I	Err	Err	Err	Err	Err	Err	V	Err	
A24_MBLT_sup	0x3c	I	Err	Err	Err	Err	Err	Err	Err	V	
A32_S_sup	0x0d	1	V	V	V	V	V	V	V	Err	
A32_BLT_sup	0x0f	I	Err	Err	Err	Err	Err	Err	V	Err	
A32_MBLT_sup	0x0c	1	Err	Err	Err	Err	Err	Err	Err	V	

Err = The VFC will answer with a Bus Error, not with an acknowledge V = Access mode supported by the VME64x core

I = Not yet implemented

## 20/06/2012