

MPEG4 Advanced Simple Profile Decoder (v02.01.00) on C64x+

FEATURES

www.ti.com

- eXpressDSP Digital Media (XDM 1.0 IVIDDEC2) interface compliant
- Validated on the OMAP3530 EVM
- MPEG4 advanced simple profile, level 0, 1, 2, 3, 4 and 5 supported
- MPEG4 visual simple profile, level 0, 1, 2, 3, and 4A supported
- Supports H.263 profile 3 and level 10, 20, 30, 45, 50, 60, and 70
- Supports H.263 Annex-IJKT
- Post-processing filter, de-blocking, and de-ringing supported
- Spatial and temporal error concealment supported only for I and P progressive frames
- Contains optimized I and P flow to decode frames up to WVGA (854 x 480 and 480 x 854) and D1 (720x576) resolution at 30 fps
- Half-pel and quarter-pel interpolation supported
- Outputs are available in YUV 420 planar and 422 interleaved little endian formats
- Display width feature supported
- Single object supported
- Supports streams that are non-multiple of 16
- Global Motion Compensation (GMC) 0 and 1 warp supported for progressive frame

- Global Motion Compensation (GMC) 2 and 3 warp not supported
- Supports Frame level byte-swap. If it is enabled, algorithm will do byte-swap conversion at frame level dynamically. Also, encoded bytes per frame information need not be provided as input to the application.
- This codec can be used on any of TI's C64x+ based platforms such as DM644x, DM643x, OMAP35xx and their derivatives.

DESCRIPTION

MPEG4 is a popular video algorithm defined by Motion Picture Expert Group (MPEG) for video conferencing applications. This codec has been built and tested on the OMAP3430 EVM with XDS560 JTAG. This version of codec is compiled, assembled, archived, and linked using the code generation tools version 6.0.8 and developed using Code Composer Studio version 3.2.40.8.





Performance Summary

This section describes the performance of the MPEG4 Advanced Simple Profile Decoder on C64x+.

Table 1. Configuration Table

CONFIGURATION	ID
MPEG4 visual simple profile, levels 0, 1, 2, 3, and 4A H263 profile 3, level 10, 20, 30, 45, 50, 60, and 70 MPEG4 Advanced Simple Profile level 0 to 5	

Table 2. Cycles Information for MPEG-4 ASP Streams - Profiled on OMAP3530 EVM with Code Generation Tools Version 6.0.8

CONFIGURATION ID	PERFORMANCE STATISTICS (MEGA CYCLES PER SECOND) ⁽¹⁾				
CONFIGURATION ID	TEST DESCRIPTION	AVERAGE ⁽²⁾	PEAK ⁽³⁾		
	akiyo.cif.890kbps.mta.cmp, YUV420, CIF @ 715 kbps, IQ and Interlace	31	55		
	viperkillercuts_p720x576_8000kbps_L5_25fps.m4v, YUV420, D1 @ 9 mbps, IQ and Quarterpel	175	271		
	harryPotter_p720x480_8000kbps_L5_30fps.m4v, YUV420, D1 @ 8 mbps, IQ and Quarterpel	178	200		
MPEG4_DEC_001	harryPotter_p720x480_8000kbps_L5_30fps_BFRAME.m4v, YUV420, D1 @ 7 mbps, IQ, Quarterpel and B frames	214	236		
	football_800x480_420_5mbps_30fps_BFRAME.m4v, YUV420, WVGA @ 5 mbps, B frames	170	250		
	jets_p848x480_420_5mbps_30fps_BFRAME.m4v,, YUV420, WVGA @ 5 mbps, B frames	185	284		
	jets_p854x480_420_5mbps_30fps_QUANT1.m4v YUV420, WVGA @ 5 mbps, Quant1	117	164		
	jets_p864x480_420_5mbps_30fps_QUANT1.m4v YUV420, WVGA @ 5 mbps, Quant1	124	170		
	shild_wp1_wa3_qpel0_IP_8mbps.m4v YUV420, D1 @ 8 mbps,GMC	135	368		
	shild_wp1_wa3_qpel0_IPB_8mbps.m4 YUV420, D1 @ 8 mbps,GMC	187	368		

⁽¹⁾ Measured with program memory, stack, and I/O buffers in external memory and with cache configuration: 32 K-bytes L1P cache, 16 K-bytes L1D cache, 64 K-bytes L1D RAM, and 64 K-bytes L2 cache.

Table 3. Cycles Information for MPEG-4 SP Streams - Profiled on OMAP3530 EVM with Code Generation Tools Version 6.0.8

CONFIGURATION ID	PERFORMANCE STATISTICS (MEGA CYCLES PER SECOND) ⁽¹⁾				
CONFIGURATION ID	TEST DESCRIPTION	AVERAGE ⁽²⁾	PEAK ⁽³⁾		
	akiyo_qcif10_q1.m4v, MPEG4 SP, YUV420, QCIF @ 1 mbps	14	22		
	cif_high_256kbps_100f_fixedqp20_nofilter.m4v, MPEG4 SP, YUV420, CIF @ 256 kbps	27	40		
MPEG4_DEC_001	foreman_vga_dp0.m4v, MPEG4 SP, YUV420, VGA @ 3 mbps	78	83		
	hp_720x480.m4v, MPEG4 SP, YUV420, D1 @ 10 mbps	150	213		
	CIMG0389_480x854.m4v, MPEG4 SP, WVGA @ 6 mbps	131	150		
	football_p864x480_420_8mbps_30fps_NoTools.m4v, WVGA 8mbps	150	192		
	football_p800x480_420_8mbps_30fps_NoTools.m4v, WVGA 8mbps	143	174		
	football_p848x480_420_8mbps_30fps_NoTools.m4v, WVGA 8mbps	149	184		
	football_p854x480_420_8mbps_30fps_NoTools.m4v, WVGA 8mbps	150	188		

⁽¹⁾ Measured with program memory, stack, and I/O buffers in external memory and with cache configuration: 32 K-bytes L1P cache, 16 K-bytes L1D cache, 64 K-bytes L1D RAM, and 64 K-bytes L2 cache.

⁽²⁾ Average cycles are calculated by taking frame level average for entire sequence and then scaling it to 25/30 fps

³⁾ Peak cycles are calculated by taking frame level peak over entire sequence and then scaling it to 25/30 fps.

⁽²⁾ Average cycles are calculated by taking frame level average for entire sequence and then scaling it to 25/30 fps.

⁽³⁾ Peak cycles are calculated by taking frame level peak over entire sequence and then scaling it to 25/30 fps.



Table 4. Cycles Information for H263 Streams - Profiled on OMAP3530 EVM with Code Generation Tools Version 6.0.8

CONFIGURATION ID	PERFORMANCE STATISTICS (MEGA CYCLES PER SECOND) ⁽¹⁾			
CONFIGURATION ID	TEST DESCRIPTION	AVERAGE ⁽²⁾	PEAK ⁽³⁾	
	akiyo.qcif.263, H263, YUV420 , QCIF @ 122 kbps	9	13	
MPEG4_DEC_001	QCIF_64kbps_AnnexIJKT.mpg4, H263, YUV420, QCIF @ 300 kbps, Annex IJKT	16	25	
	D1p25_mobcal_420p_8Mbps_Level60.263, YUV420, H263 Level 60, 720x288 @ 8 Mbps	95	101	

⁽¹⁾ Measured with program memory, stack, and I/O buffers in external memory and with cache configuration: 32 K-bytes L1P cache, 16 K-bytes L1D cache, 64 K-bytes L1D RAM, and 64 K-bytes L2 cache.

Table 5. Cycles Information - Profiled on OMAP3530 EVM with Code Generation Tools Version 6.0.8 for foreman_vga_dp0.m4v, MPEG4 (YUV420, VGA @ 3 mbps)

CONFIGURATION ID	PERFORMANCE STATISTICS (MEGA CYCLES PER SECOND) ⁽¹⁾			
CONFIGURATION ID	TEST DESCRIPTION AVERAGE ⁽²⁾ PE		PEAK ⁽³⁾	
	With De-blocking enabled	180	187	
MPEG4_DEC_001	With De-ringing enabled	199	204	
	With De-blocking and De-ringing enabled	245	252	

Measured with program memory, stack, and I/O buffers in external memory and with cache configuration: 32 K-bytes L1P cache, 16 K-bytes L1D cache, 64 K-bytes L1D RAM, and 64 K-bytes L2 cache.

Table 6. Cycles Information - Profiled on OMAP3530 EVM with Code Generation Tools Version 6.0.8 for MPEG4 SP, MPEG4 ASP and H263 streams (YUV422)

CONFIGURATION ID	PERFORMANCE STATISTICS (MEGA CYCLES PER SECOND) ⁽¹⁾			
CONFIGURATION ID	TEST DESCRIPTION	AVERAGE ⁽²⁾	PEAK ⁽³⁾	
	foreman_vga_dp0.m4v, MPEG4 SP, YUV422, VGA @ 3 mbps	84	89	
MPEG4_DEC_001	viperkillercuts_p720x576_8000kbps_L5_25fps.m4v, MPEG4 ASP,YUV422, D1 @ 9 mbps, IQ and Quarterpel	183	280	
	hp_720x480.m4v, MPEG4 SP, YUV422, D1 @ 10 mbps	153	218	
	D1p25_mobcal_420p_8Mbps_Level60.263, YUV422, H263 Level 60, 720x288 @ 8 Mbps	105	112	

⁽¹⁾ Measured with program memory, stack, and I/O buffers in external memory and with cache configuration: 32 K-bytes L1P cache, 16 K-bytes L1D cache, 64 K-bytes L1D RAM, and 64 K-bytes L2 cache.

⁽²⁾ Average cycles are calculated by taking frame level average for entire sequence and then scaling it to 25/30 fps

⁽³⁾ Peak cycles are calculated by taking frame level peak over entire sequence and then scaling it to 25/30 fps.

⁽²⁾ Average cycles are calculated by taking frame level average for entire sequence and then scaling it to 25/30 fps.

⁽³⁾ Peak cycles are calculated by taking frame level peak over entire sequence and then scaling it to 25/30 fps.

⁽²⁾ Average cycles are calculated by taking frame level average for entire sequence and then scaling it to 25/30 fps.

⁽³⁾ Peak cycles are calculated by taking frame level peak over entire sequence and then scaling it to 25/30 fps.



Note:

- Profiled on OMAP3430 ES1 EVM. Measured with 32 bit DDR at 166 MHz and CPU at 330 MHz
- No hardware accelerators are used.
- Average and peak MCPS measurements can vary by +/-5%.

Table 7. Generated with Code Generation Tools Version 6.0.8

CONFIGURATION ID	MEMORY STATISTICS ⁽¹⁾				TOTAL
	PROGRAM MEMORY	DATA MEMORY			
		INTERNAL ⁽	EXTERNAL	STACK	
MPEG4_DEC_001 (QCIF)	306	61	661	8	1036
MPEG4_DEC_001 (CIF)	306	61	1610	8	1985
MPEG4_DEC_001 (VGA)	306	61	3990	8	4365
MPEG4_DEC_001 (D1)	306	61	4419	8	4794
MPEG4_DEC_001 (WVGA)	306	61	5191	8	5566

⁽¹⁾ All memory requirements are expressed in kilobytes (1K-byte = 1024 bytes) and there could be a variation of approximately 1-2% in values.

Table 8. Internal Data Memory Split-Up

	DATA MEMORY - INTERNAL (1)		
CONFIGURATION ID	SHARED		INSTANCE ⁽²⁾
	CONSTANTS	SCRATCH	INSTANCE.
MPEG4_DEC_001	0	61	0

¹⁾ Internal memory refers to L1DRAM. All memory requirements are expressed in kilobytes and there could be a variation of approximately 1-2% in values.

²⁾ Internal memory is placed in L1D RAM.

²⁾ I/O buffers not included. Some of the instance memory buffers could be scratch.



Notes

- I/O buffers:
 - Input buffer size = 405 K-bytes (WVGA, one YUV422 interleaved frame)
 - Output buffer size = 810 K-bytes (for decoding one WVGA frame)
- Memory Configuration
 - L1P: 32 K-bytes program cache (32 bytes cache line width, direct mapped cache)
 - L1D: 64 K-bytes data memory and 16 K-bytes data cache (64 bytes cache line width, 2 way set associative cache)
 - L2: 64 K-bytes cache (128 bytes cache line width, 4-way set associative cache)
- The algorithm uses 4 QDMA channels each requiring up to a maximum of 6 linked transfers. The algorithm uses DMAN3 interface for logical allocation of these channels.
- Total data memory for N non pre-emptive instances = Constants + Runtime Tables + Scratch + N*(Instance + I/O buffers + Stack)
- Total data memory for N pre-emptive instances = Constants + Runtime Tables + N*(Instance + I/O buffers + Stack + Scratch)
- MCPS calculations are done in frame based mode. In case, it is run in buffer mode, MCPS increase can be
 upto 10% assuming additional buffer size of 10 K-bytes per frame.

References

- MPEG4 Standard (ISO_IEC_14496-2_2001)
- H.263 Standard (ITU-T Series H 02/98)
- MPEG4 Simple Profile Decoder on C64x+ User's Guide(literature number: SPRUGT2)

Glossary

Term	Description	
Constants	Elements that go into .const memory section	
Scratch	Memory space that can be reused across different instances of the algorithm	
Shared	um of Constants and Scratch	
Instance	Persistent-memory that contains persistent information - allocated for each instance of the algorithm	

Acronyms

Acronym	Description
CIF	Common Intermediate Format
GMC	Global Motion Compensation
NTSC	National Television Standards Committee
QCIF	Quarter Common Intermediate Format
QVGA	Quarter Video Graphics Array
SQCIF	Sub Quarter Common Intermediate Format
VGA	Video Graphics Array
WVGA	Wide Video Graphics Array
XDM	eXpressDSP Multimedia

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Applications Products Amplifiers amplifier.ti.com Audio www.ti.com/audio Data Converters Automotive www.ti.com/automotive dataconverter.ti.com DLP® Products Broadband www.dlp.com www.ti.com/broadband DSP Digital Control dsp.ti.com www.ti.com/digitalcontrol Clocks and Timers www.ti.com/clocks Medical www.ti.com/medical Military Interface www.ti.com/military interface.ti.com Optical Networking Logic logic.ti.com www.ti.com/opticalnetwork Power Mgmt power.ti.com Security www.ti.com/security Telephony Microcontrollers microcontroller.ti.com www.ti.com/telephony Video & Imaging www.ti-rfid.com www.ti.com/video RF/IF and ZigBee® Solutions www.ti.com/lprf Wireless www.ti.com/wireless

> Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2009, Texas Instruments Incorporated