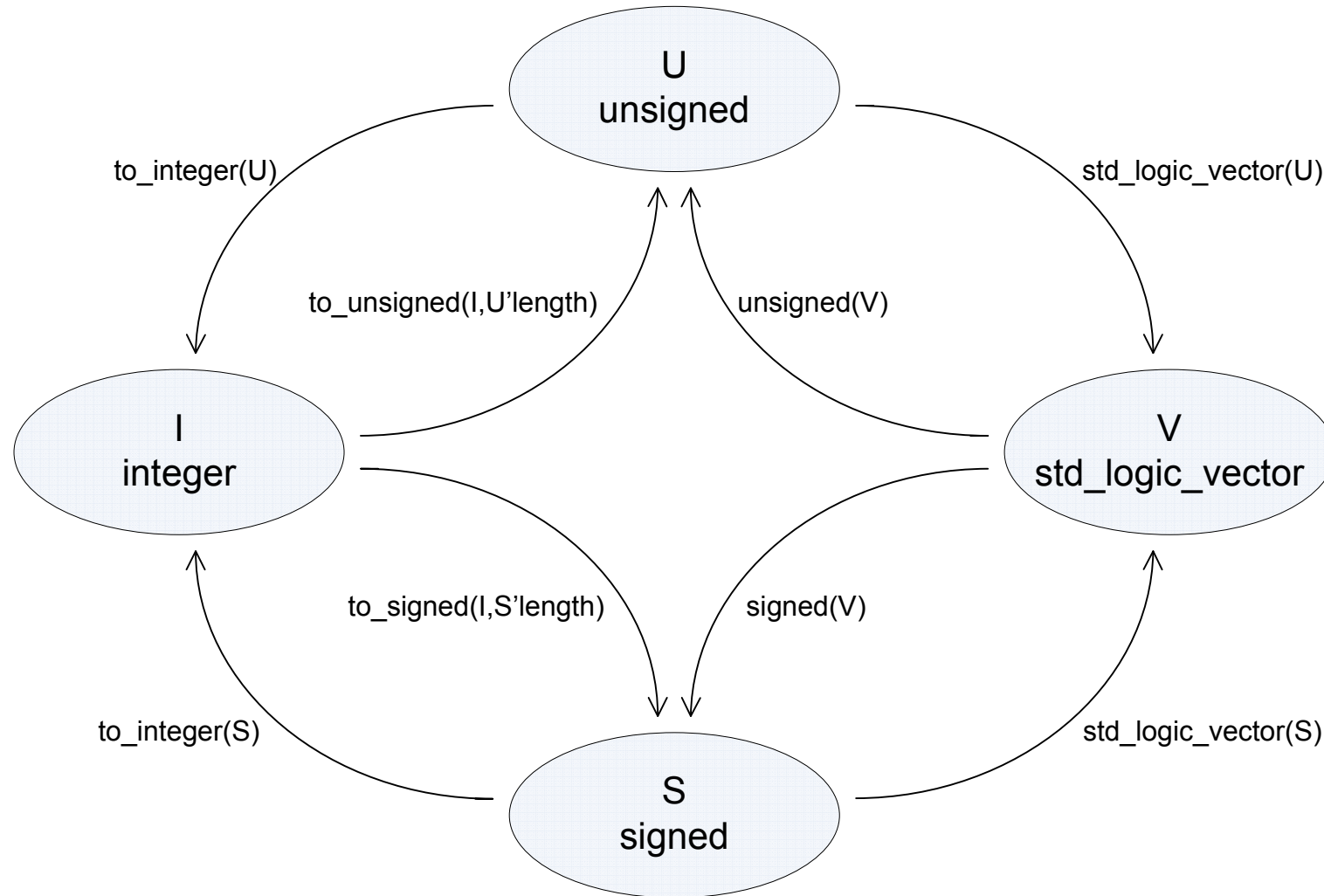```vhdl
library IEEE;
use IEEE.STD_LOGIC_1164.all;
use IEEE.NUMERIC_STD.all;
```



Zahlen

Vektoren

U
unsigned

I
integer

V
std_logic_vector

S
signed

to_integer(U)

std_logic_vector(U)

to_unsigned(I,U'length)

unsigned(V)

to_signed(I,S'length)

signed(V)

to_integer(S)

std_logic_vector(S)

Konvertierung

CAST

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