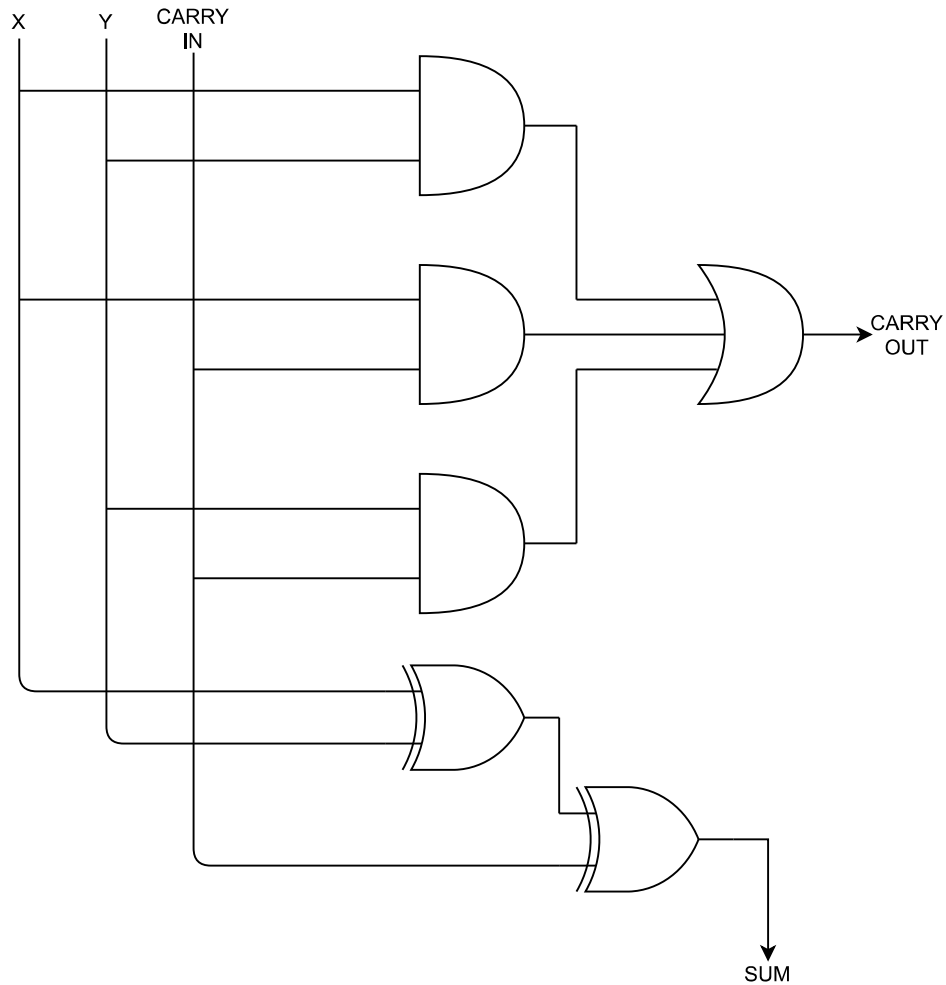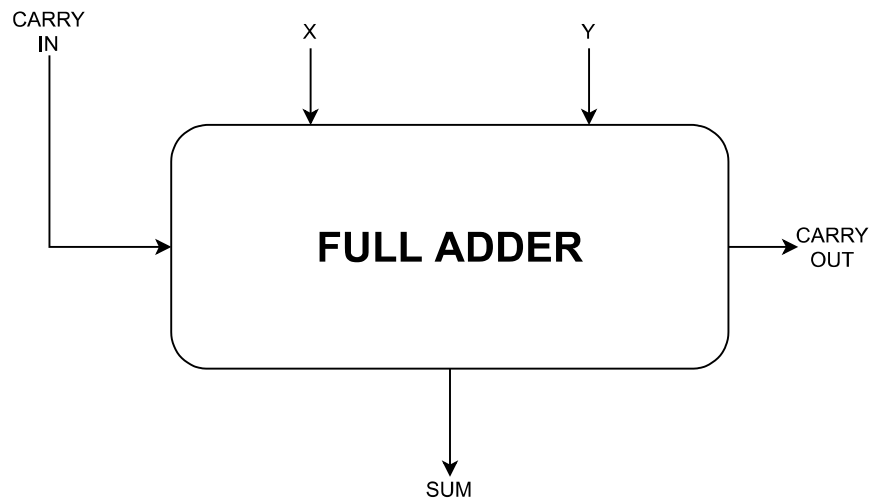# GATE LEVEL REPRESENTATION OF FULL ADDER

SYSTEM REPRESENTATION OF FULL ADDER

2 - BIT RIPPLE CARRY ADDER

X 0

Y 0

Cin 0

BIT 0

Cout 0 / Cin 1

SUM 0

X 1

Y 1

BIT 1

Cout 1 / SUM 2

SUM 1