

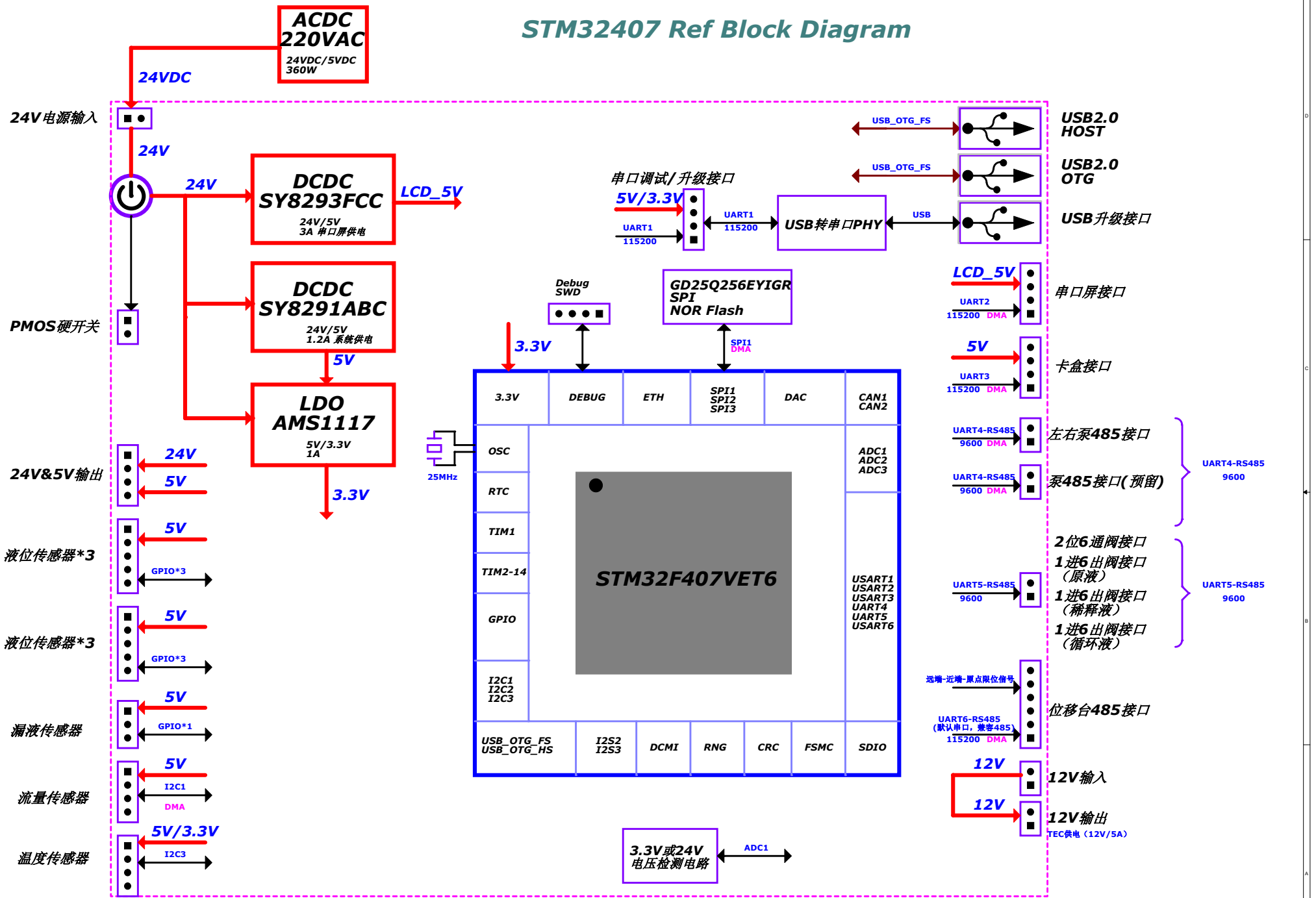
Schematics DevBoard

Revision History

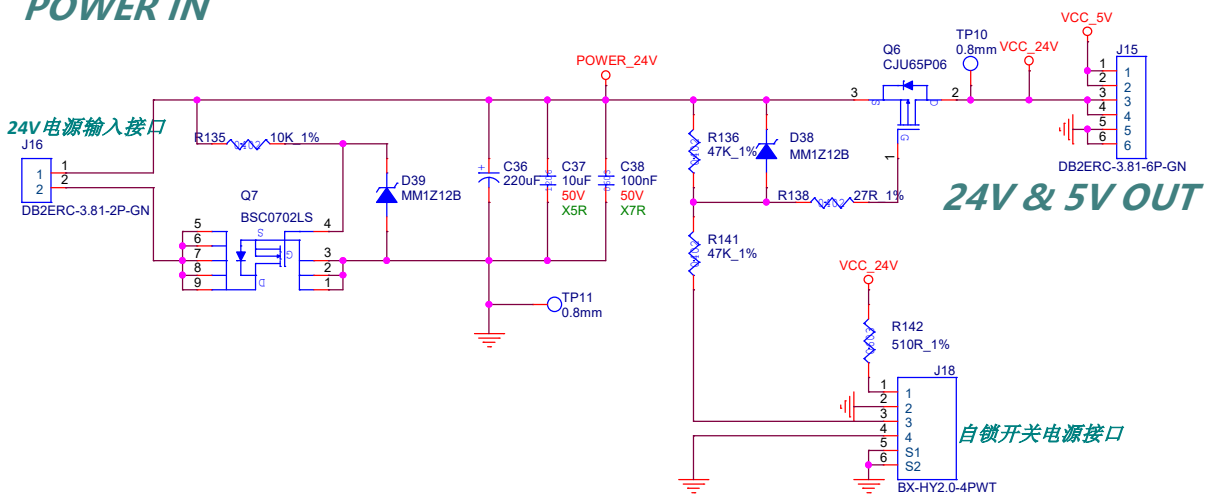
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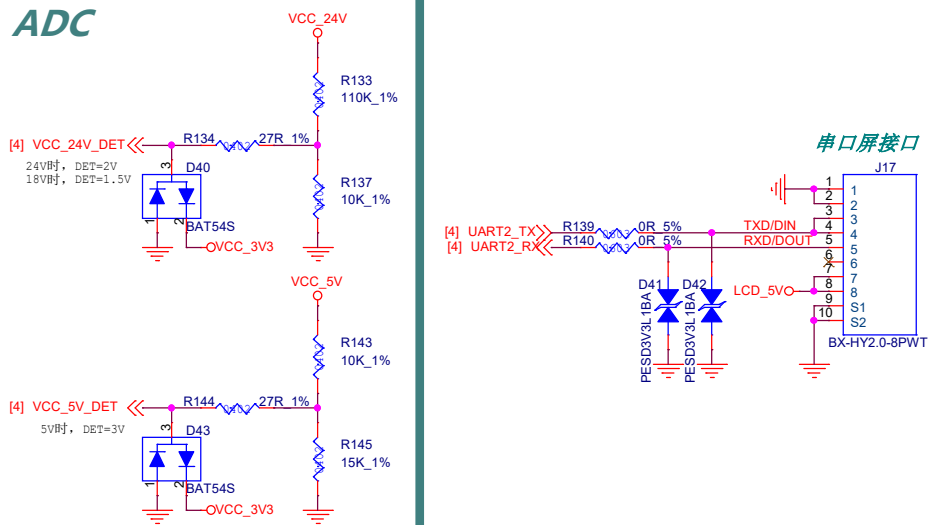
STM32407 Ref Block Diagram



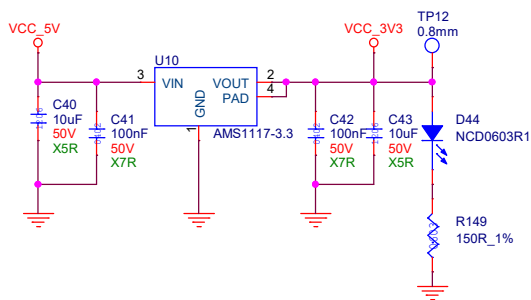
POWER IN



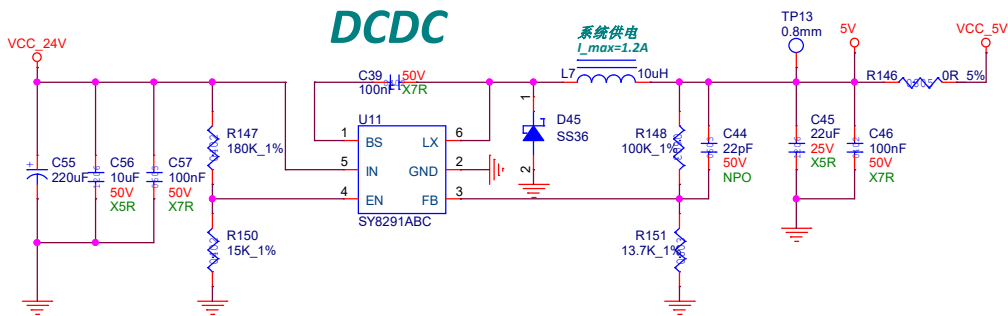
ADC



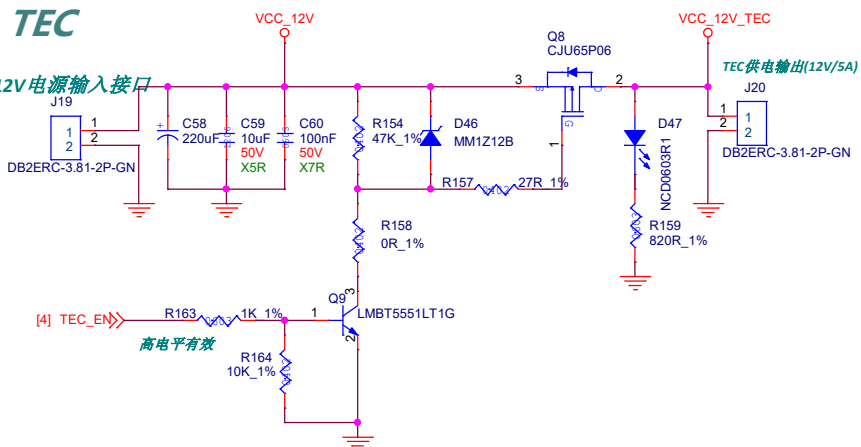
LDO



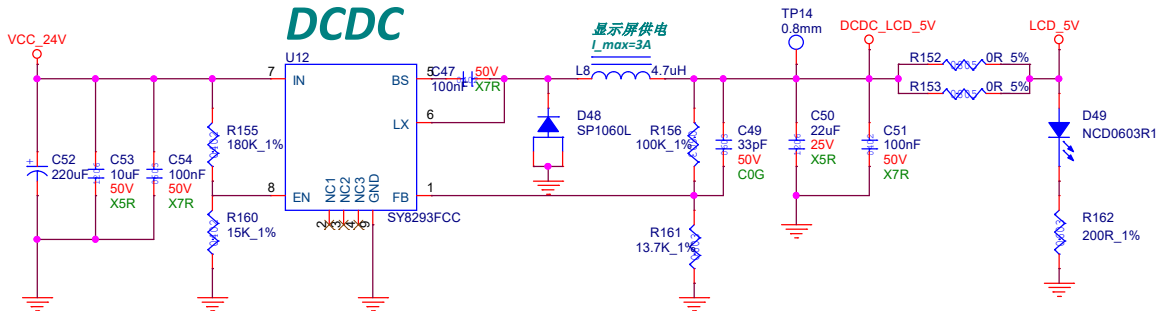
DCDC



TEC

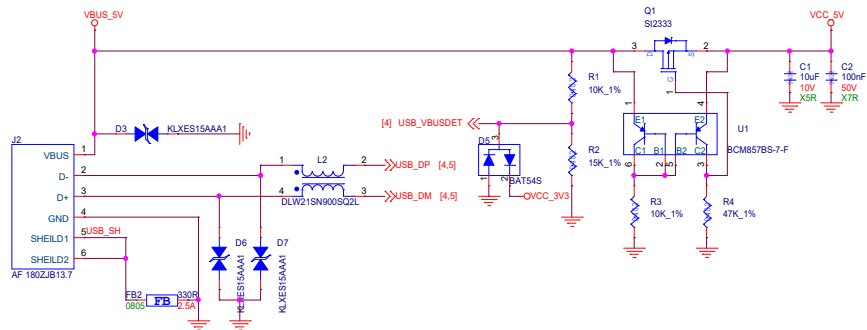


DCDC

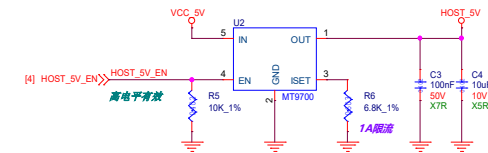
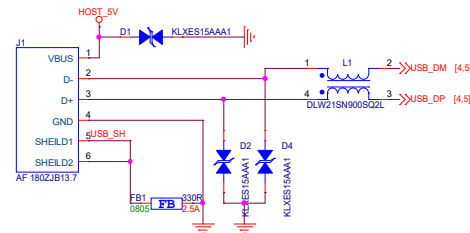


Company	中科佰乘 (深圳) 生物科技有限公司				
Drawn	Deniss	Dated	Tuesday, June 04, 2024	Title	主控板
Checked	Deniss	Dated	xxxx-xx-xx	Drawing No.	03-PWR
					Sheet 3 of 5

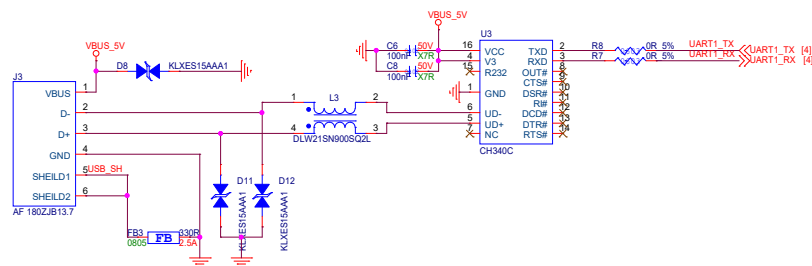
USB OTG



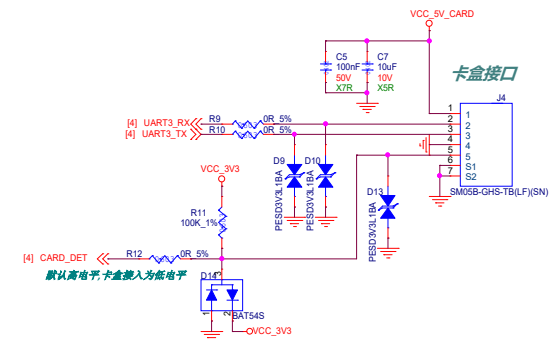
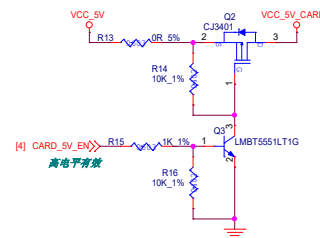
USB HOST



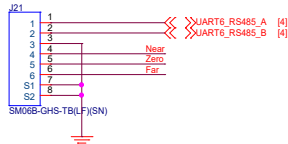
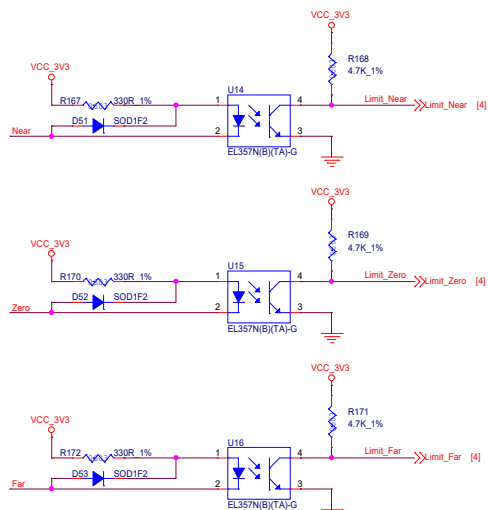
USB TO UART1



UART3



TABLE



Company	中科佰樂（深圳）生物科技有限公司				 中科佰樂（深圳）生物科技有限公司	
Drawn	Denies	Dated	Wednesday, May 29, 2024	Title	主控板	Rev V1_0
Checked	Denies	Dated	xxxx-xx-xx	Drawing No.	05-USB	Sheet 5 of 5