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Lakshmi Kameswari Maduri

Fremont
CA

Experience

Student Assistant	**San Jose State University**	**Spring 2016 - Present**

- Developed **python** scripts to automate the download of all publications
- Worked on converting static html to a dynamic website using **Python**
- Made upgrades to Prof. T. Y. Lin's homepage using JavaScript, HTML, CSS, PHP
- Moved the static HTML data to a MySQL database for the data provided in this webpage

Associate Software Engineer	**Accenture Services Pvt. Ltd**	**August 2013 - October 2014**

- Worked on SAP BI and Bex Reporting in a QA role
- **Formulated test plans**, **wrote test cases** to implement **unit testing**, **functional testing** and **performance testing** of the developed BI solutions
- Recognized for excellent communication skills and active participation in knowledge transfer sessions and team discussions

Education

Student	**Coursera.org**	**July 2016 - present**

- Automated test cases for my website using **Selenium Python SDK**
- Completed **Python** programming course with full grade. Certificate
- Solved all the chapters and uploaded the **Python** scripts from dive into python textbook
- Pursuing Front-End JavaScript Frameworks: AngularJS course

Open University Student	**San Jose State University**	**Fall 2015-Spring2016**

Coursework: Database Systems & Principles, Data Structures and Algorithms **(Overall G.P.A: 3.5/4.0)**
- *Concept based Semantic Search Engine:*
  - Developed the Front-End for the project using **HTML5**, **CSS3** and **JavaScript**
  - Developed the HTML parser to tokenize the webpage content and generate word frequencies using JavaCC
- *Document Correlator:*
  - Developed a program in Java to measure correlation between two documents using Data Structures learnt in the course

Master of Technology in VLSI Design(Electronics Engineering)	**VIT University, Vellore, India**	**Fall 2011 - Spring 2013**

Coursework: Scripting Languages and Verification using **Perl**
- *Accelerated 10/100 Ethernet Controller and FPGA Prototyping (thesis work at Research Center Imarat, DRDO, Hyd, India)*
  - Prototyped 10/100 Mbps Ethernet MAC that conforms to IEEE 802.3 ethernet protocol in both Full-duplex and Half-duplex modes on FPGA and developed Verilog HDL code for IP checksum calculation

Bachelor of Technology in Electronics & Instrumentation Engineering	**JNTU, Hyderabad, India**	**August 2007 - May 2011**

Coursework: Computer Organization, C Programming and Data Structures, Object Oriented Programming in Java

Technical Skills

Languages	**Ruby, Python**, Java, C, Perl, PHP (Rank in coding website Codewars : 6 kyu)
Web Technologies	Javascript, jQuery, AngularJs, Twitter Bootstrap, HTML5, CSS, Less, Sass, Bowe**r**
RDBMS	MySQL, Microsoft SQL Server
Operating Systems	Linux, Windows
Tools	**Git, JUnit, Selenium Webdriver, Firebug**, Eclipse, Filezilla, Visual Studio, Wordpress, Atom