

Register 0 0 0 0 0 0

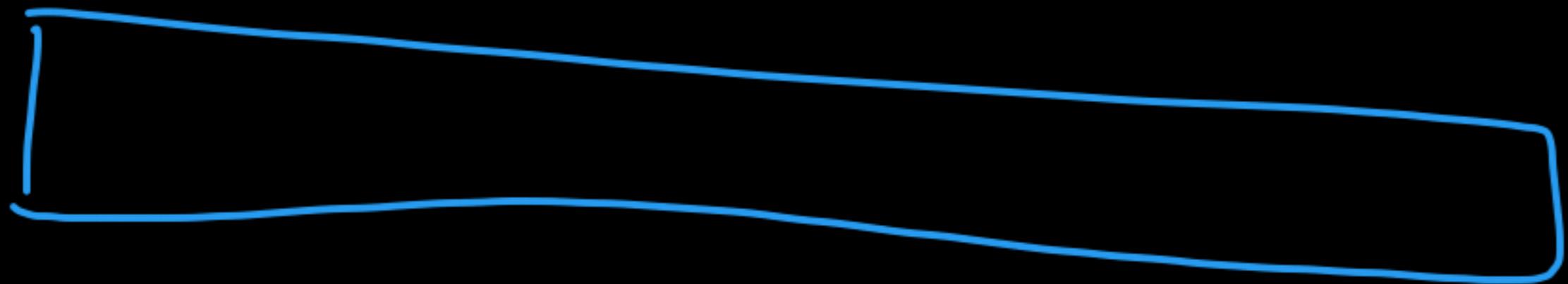
L₁



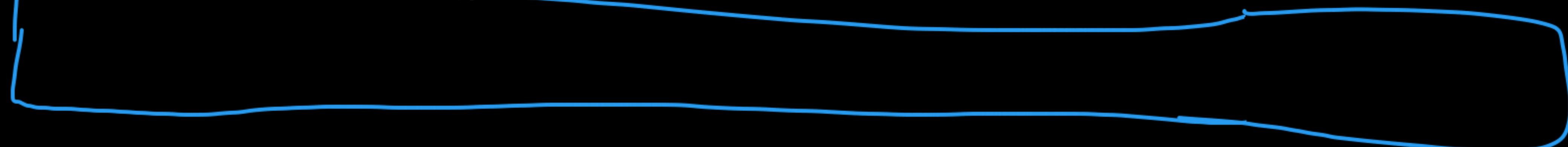
L₂



L₃



RAM



$$F: \{0,1\}^{32} \rightarrow \{0,1\}^3$$

~~(1010) 0010~~

Hit / Miss

5 cycles

100 cycles

0101... --- 1110
0010101... 0110

Address of block

