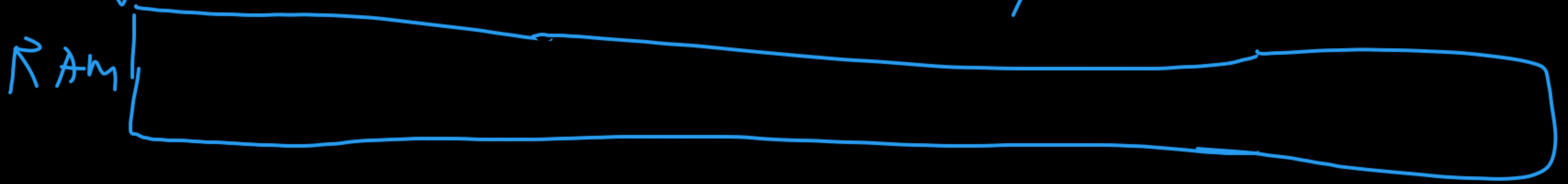
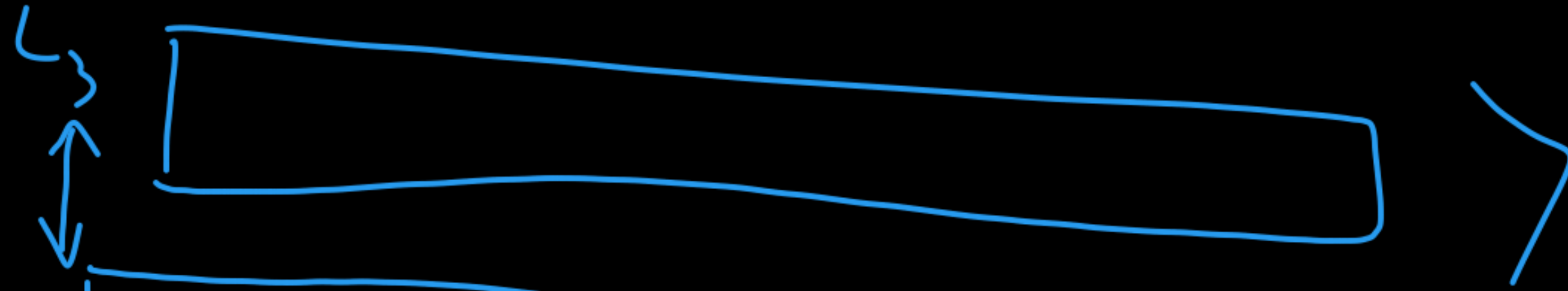


Register    0 0 0 0 1 0



$$\frac{1010}{11.1}$$

Hit / Miss  
5 cycles

100 cycles

✓

0101.0	...	1110
0010101.	-	0110

---

Address of block

block  
16 byte  
1 ↓

$\mathbb{F} : \{0, 1\}^{32} \rightarrow \{0, 1\}^3$

Cache ↓ ↓ Address Memory Contents

8 locations

Replaced

Address of first byte  
 32-bits  
 Add-

block

$\frac{2^k}{32}$

WORD = 4 bytes → integer

WORD = 4 bytes  $\rightarrow$  integer

RAM

Address → name byte 2000

↓  
Address  
size

