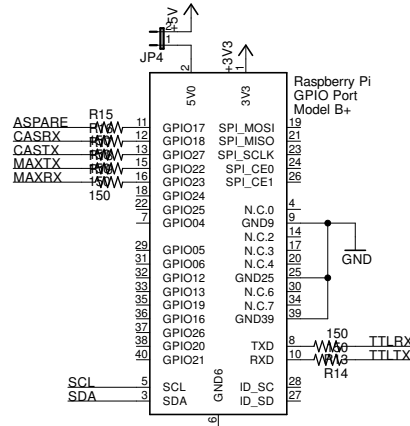


Revision 0.7:
 - Remove unnecessary inverter and jumpers
 - Move JP2 out from under pi
 - Set cassette header back a little
 - Add jumper to connect trainer 5V
 - Add jumper to connect pi 5V
 - Add pulldown to PLD enable
 - Put 0 ohm resistor in MAXRX
 Revision 0.8:
 - Reorient JP4 for easier header placement
 - Add a pad near C2 for bigger cap

JP1: 2-3
 JP2: 7, 7-8 (check manual)
 JP3: 1-2
 JP4: 1-2
 JP5: ?
 JP6: 1-2, 3-4
 JP7: 1-2, 3-4

Note:
 On ET-3400 schem, RE and 02 are NC
 On MEM/IO schem, NMI, IRQ, HALT, 5V are all
 hand-written as GND.



NOTE: Either do not connect pi and
 MAX202 at same time, or remove JP7
 while pi is installed!

