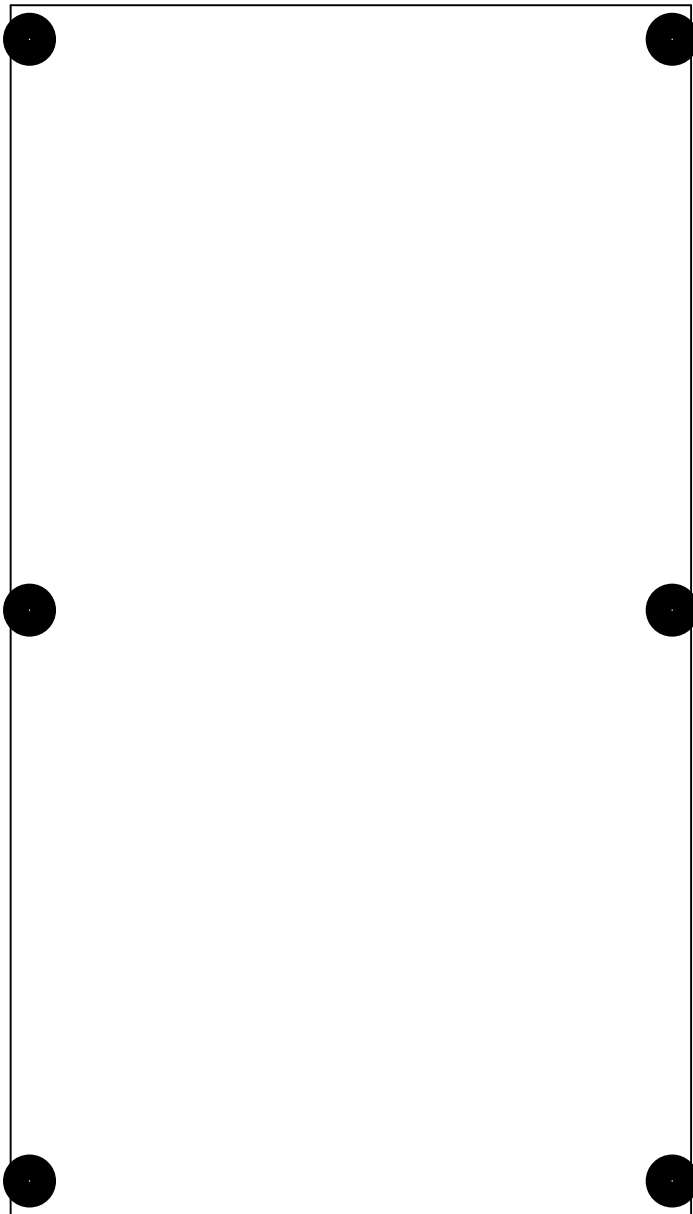


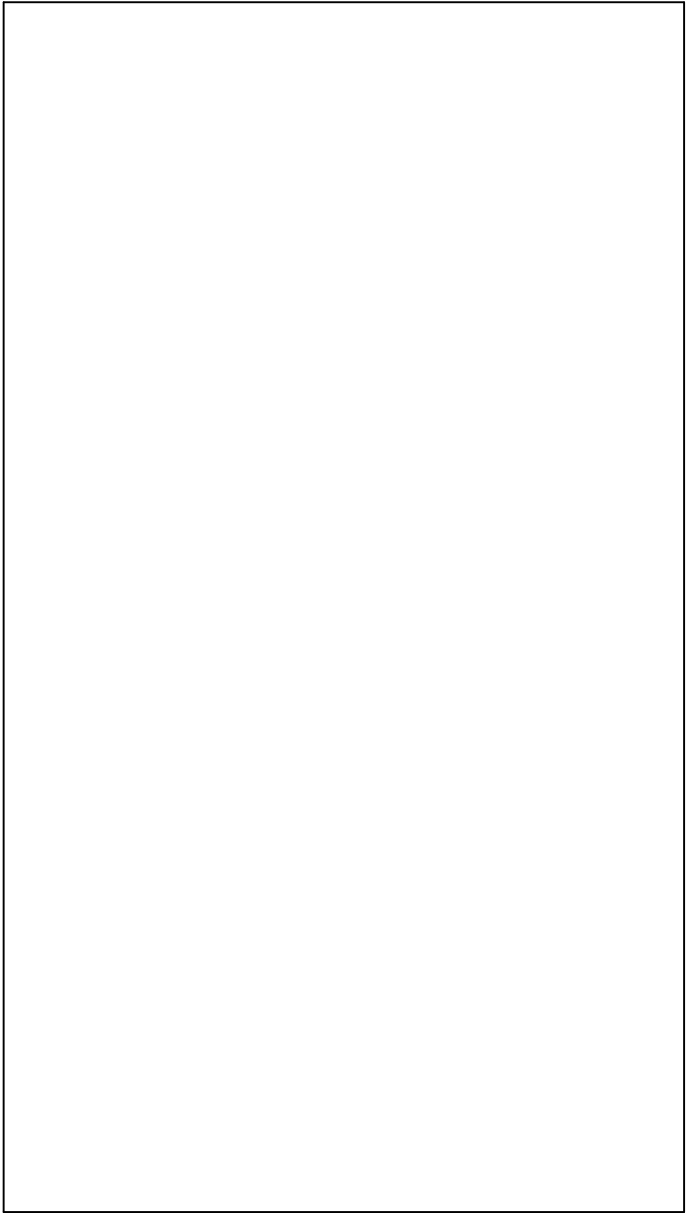
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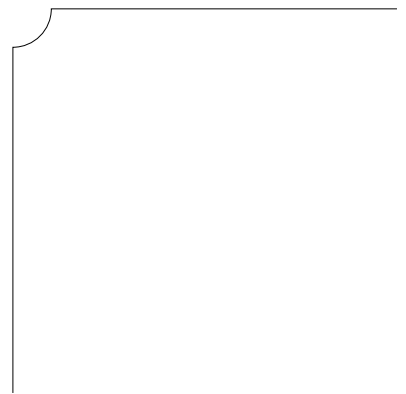
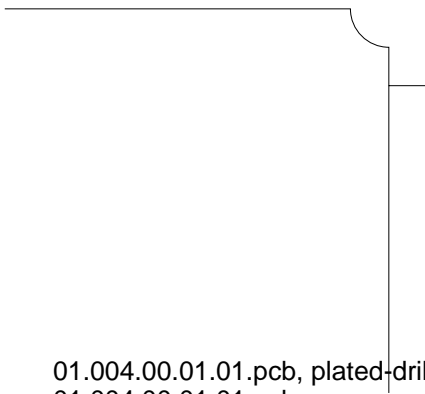
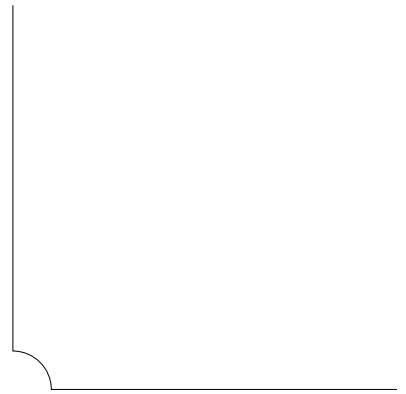
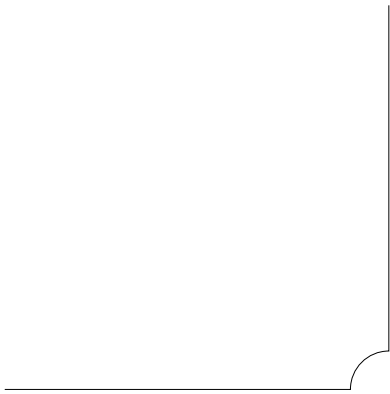
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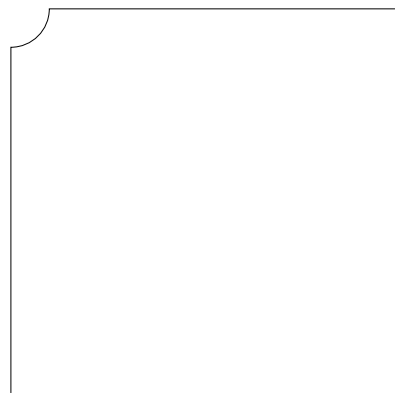
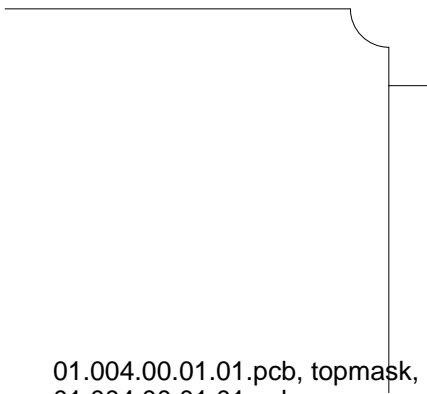
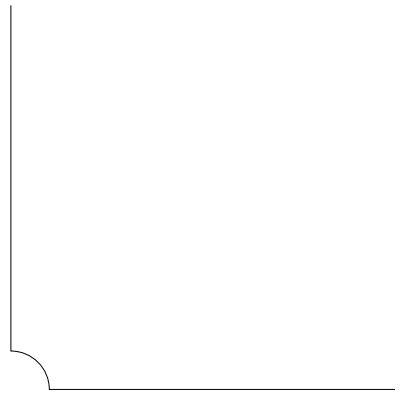
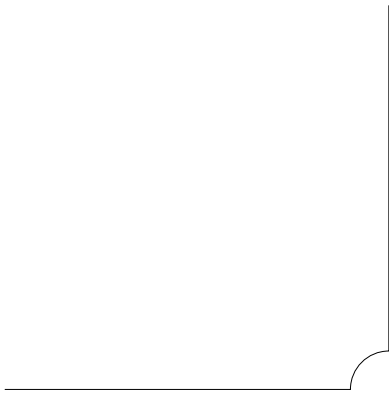
2. top
3. bottom
4. outline
5. plated-drill
6. componentmask
7. soldermask
8. topsilk
9. bottomsilk
10. topassembly
11. bottomassembly
12. fab

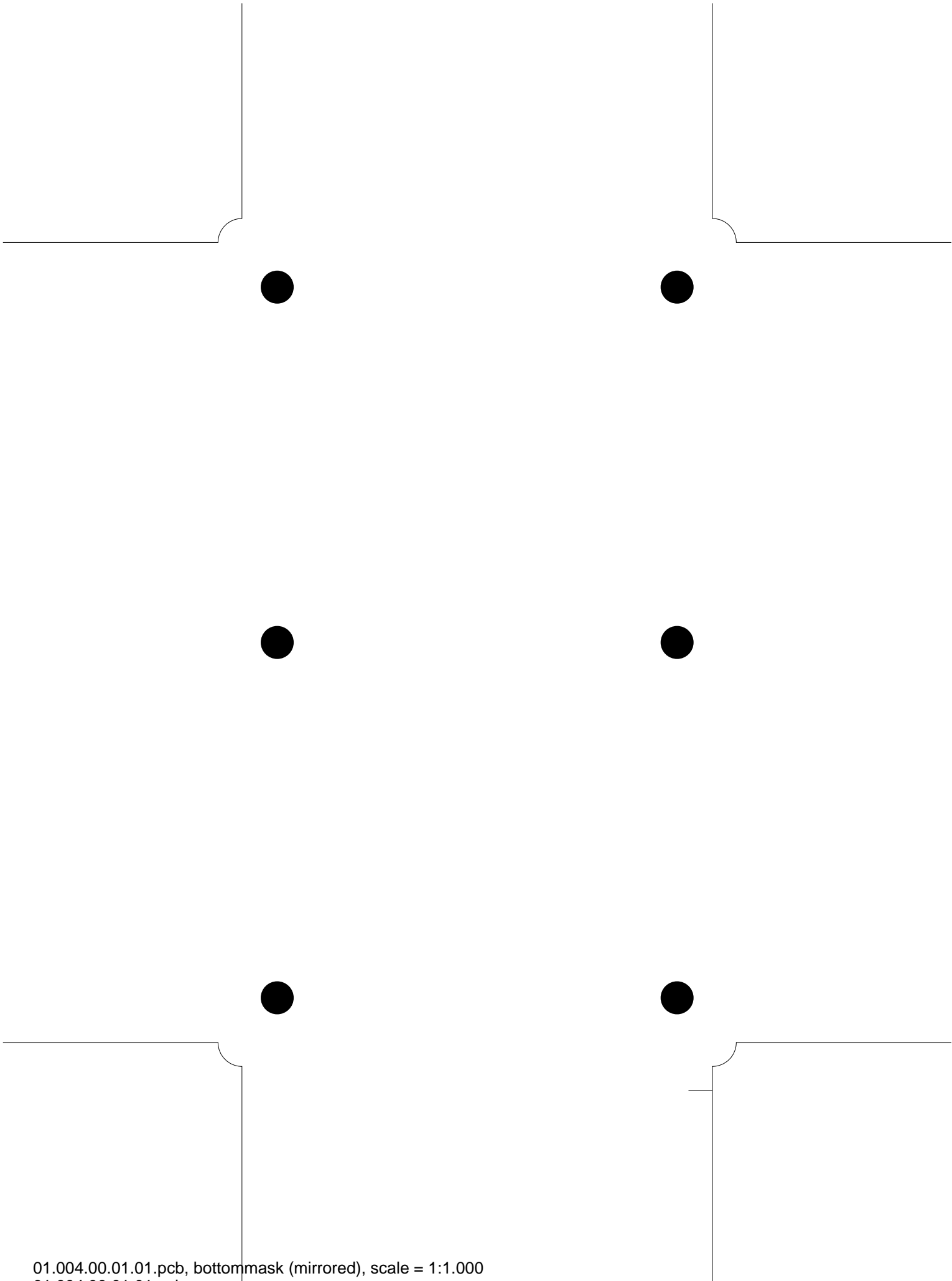












01.004.00.01.01.pcb, bottommask (mirrored), scale = 1:1.000  
01.004.00.01.01.pcb

E

C

nl

A

F

D

B



open source  
hardware

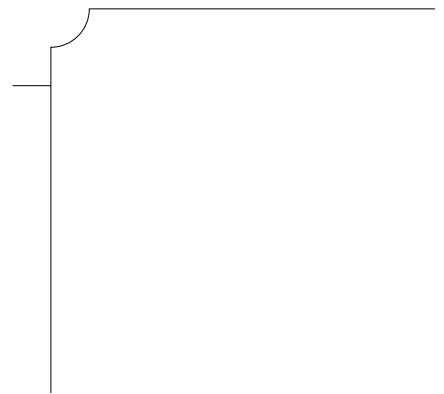
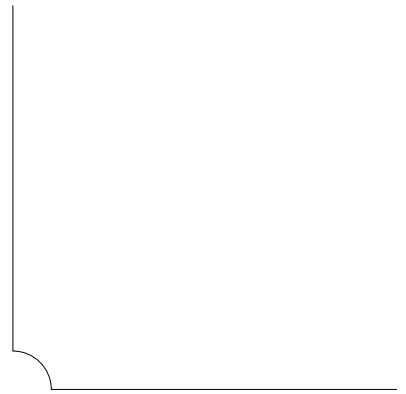
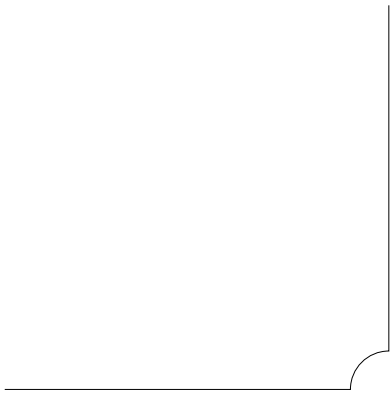
Shielding  
01.004.00.01.01  
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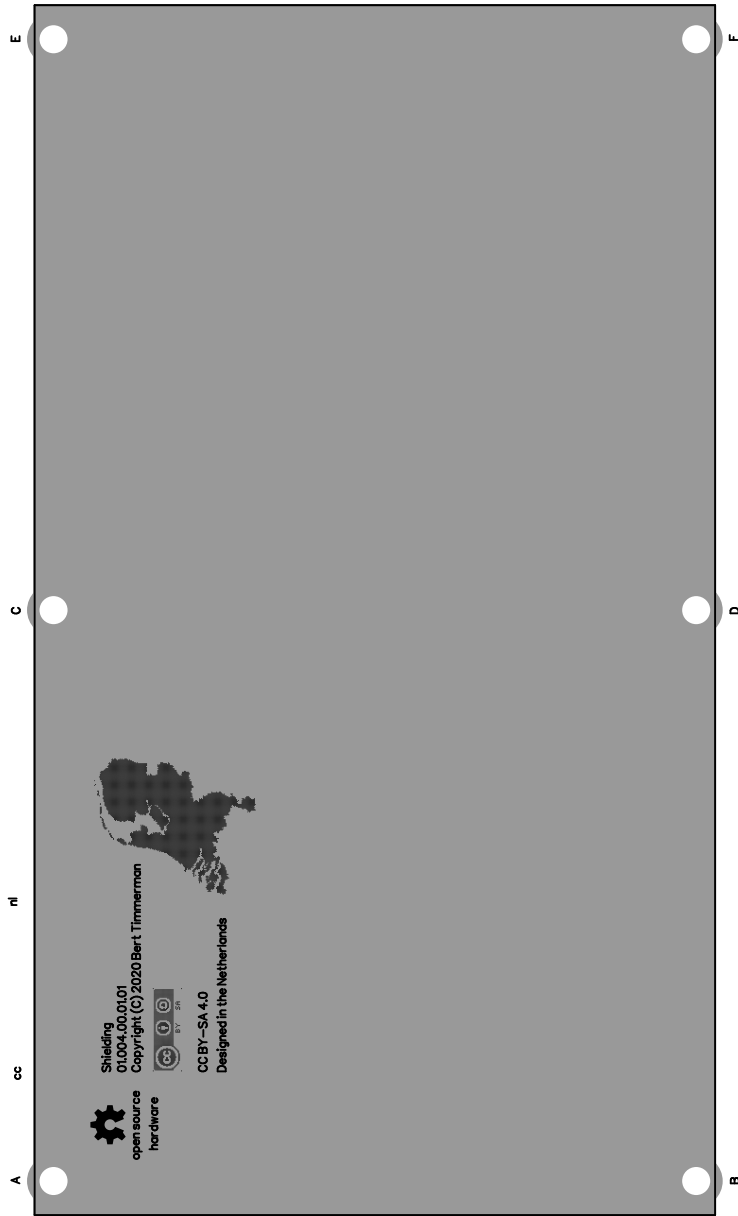


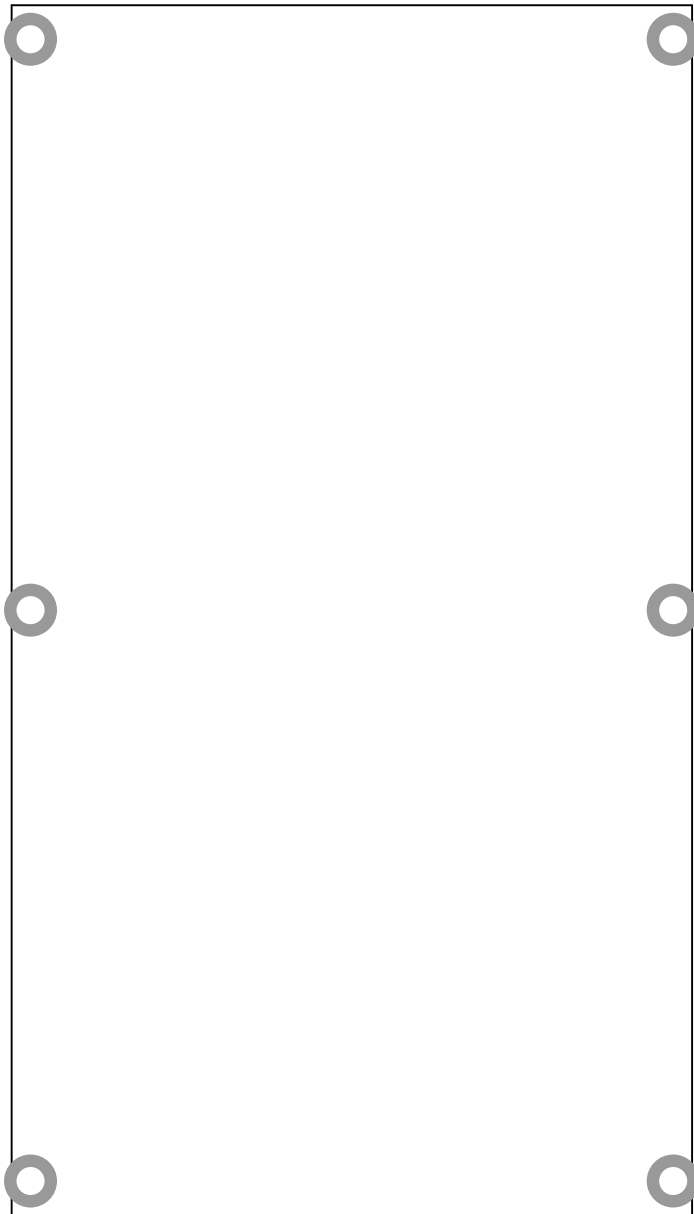
CC BY-SA 4.0  
Designed in the Netherlands







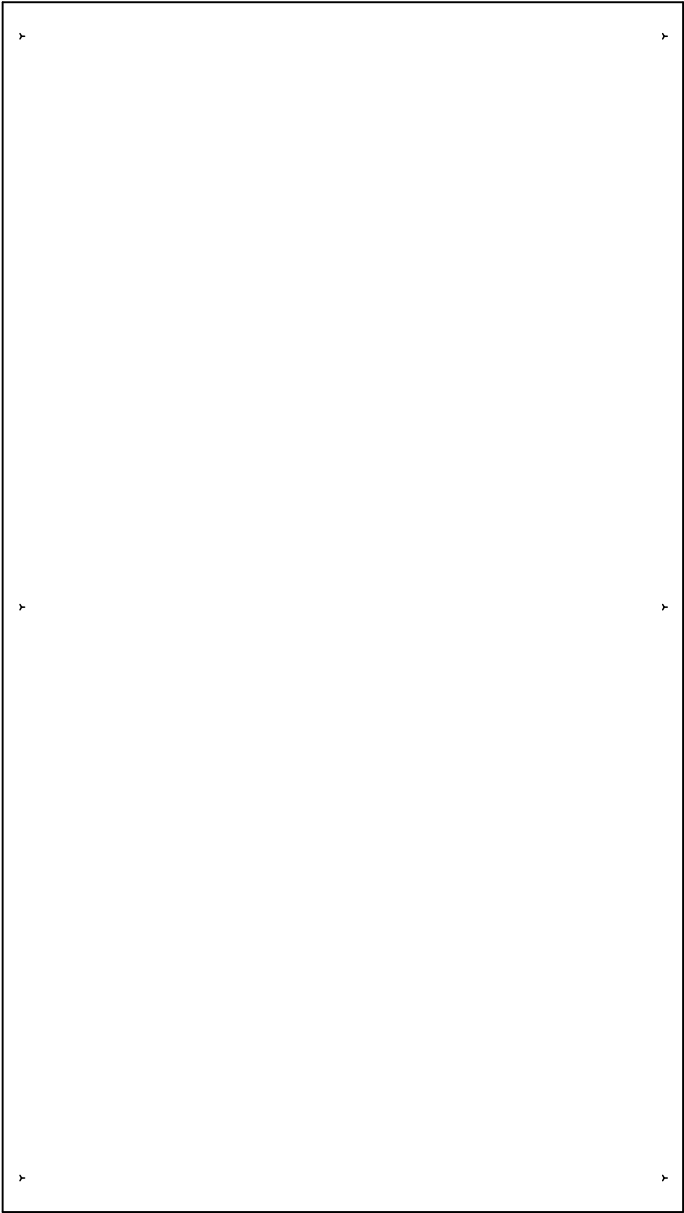




There are 1 different drill sizes used in this layout, 6 holes total

Symbol	Diam. (Inch)	Count	Plated?
⌘	0.146	6	YES

Title: 01.004.00.01.01.pcb – Fabrication Drawing  
Author: Bert Timmerman  
Date: Sun Dec 13 18:32:21 2020 UTC



Board outline is the centerline of this path