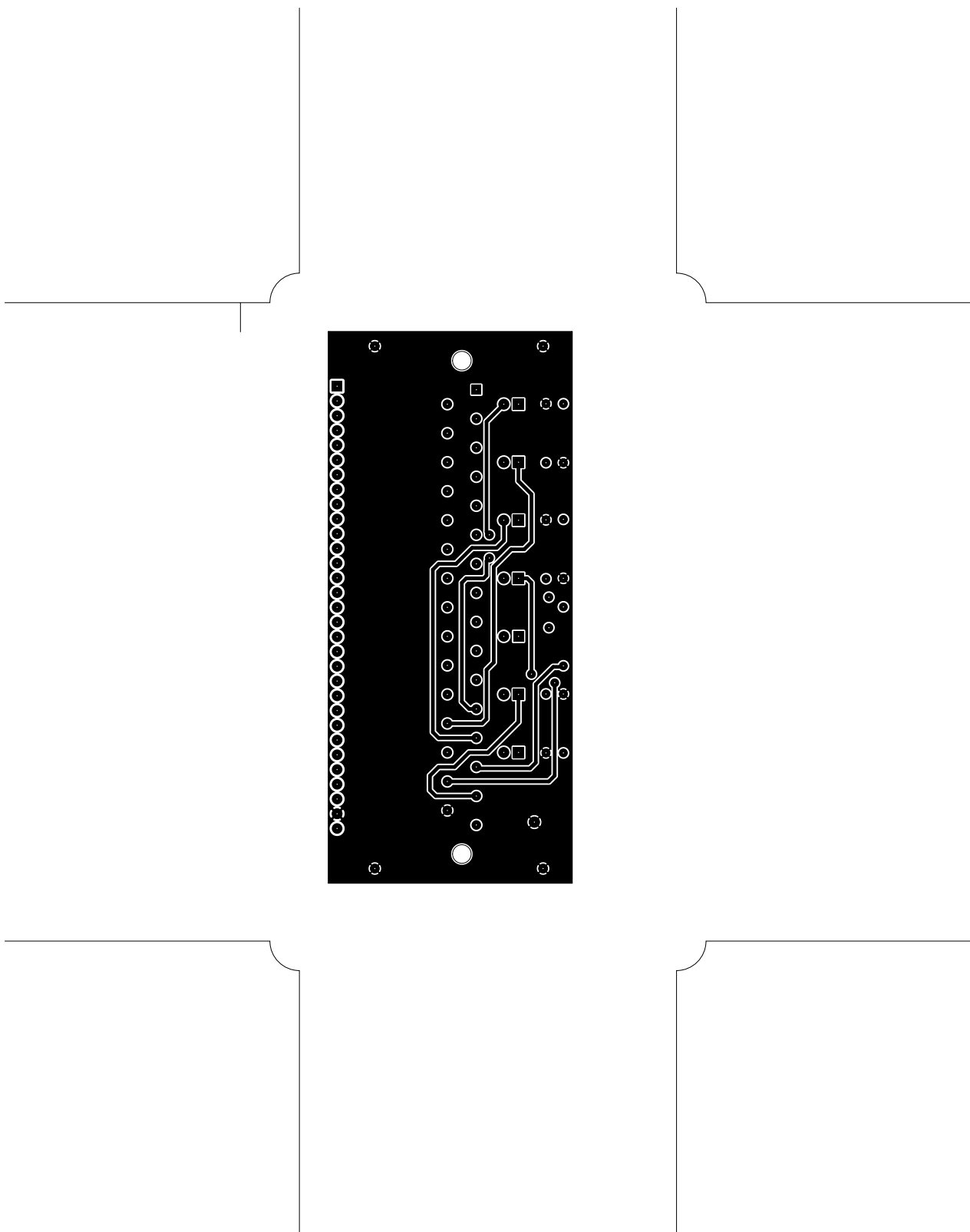
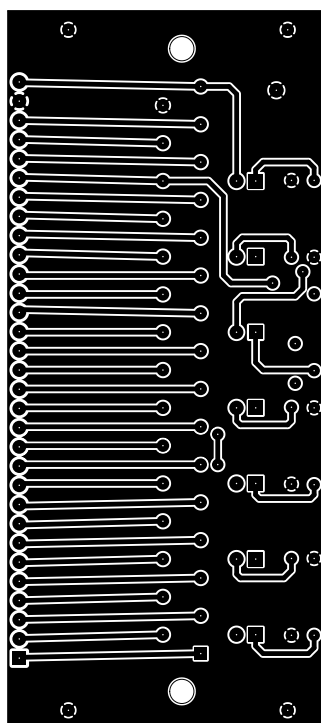


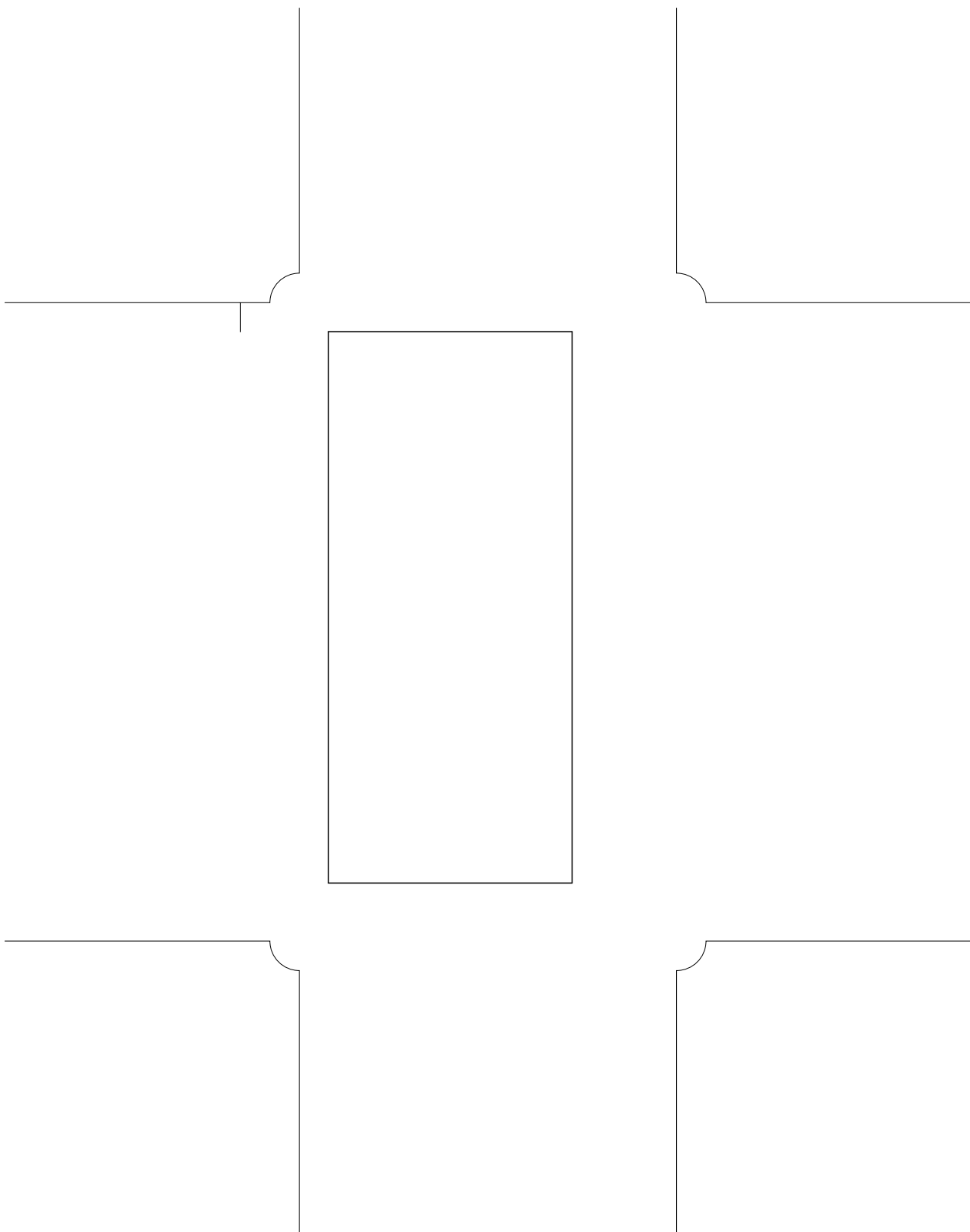
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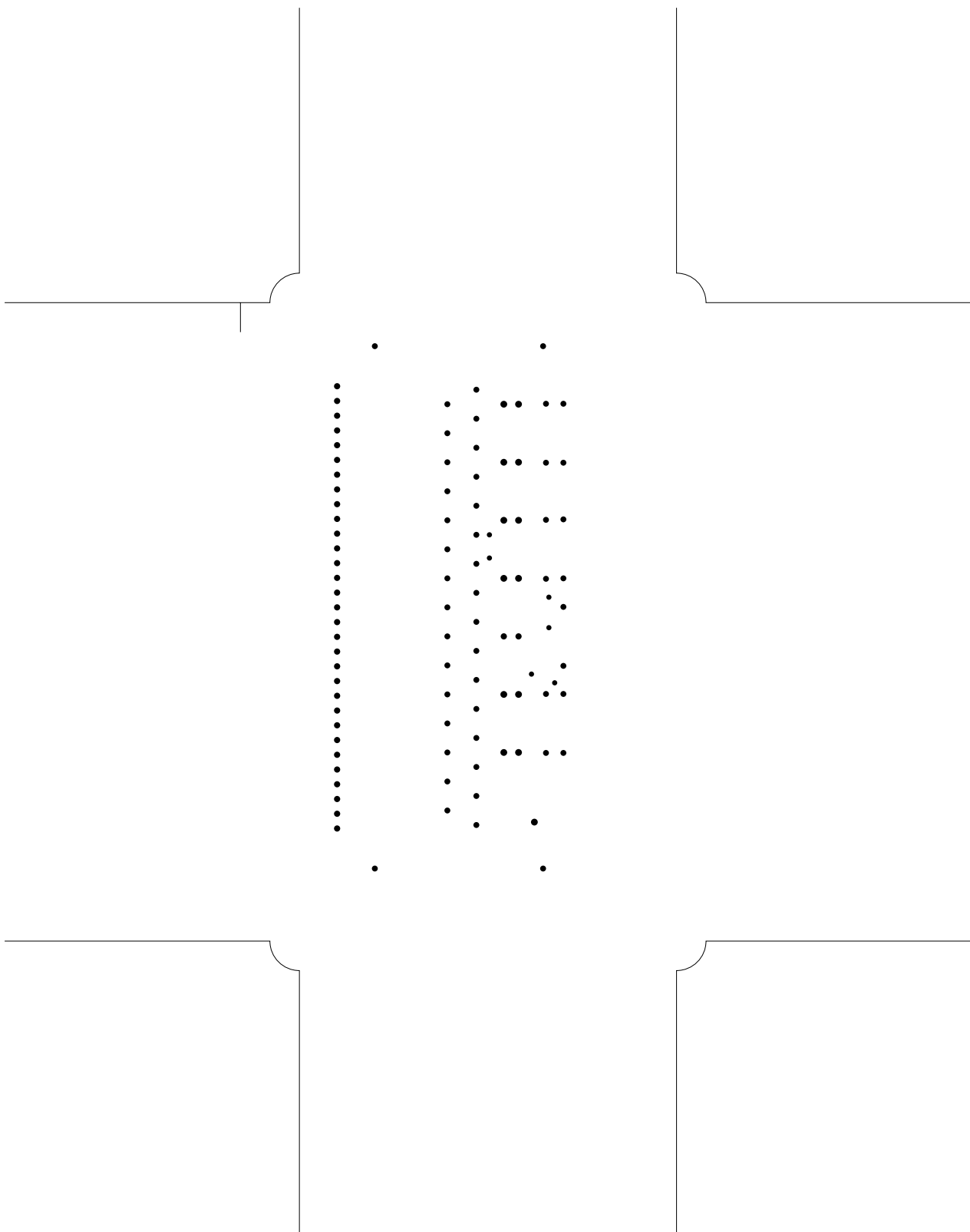
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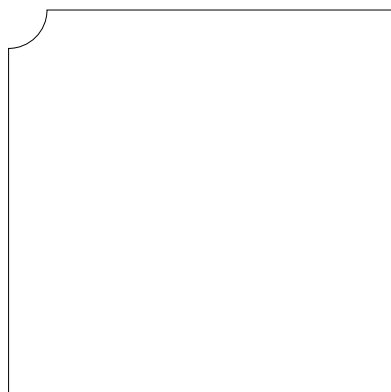
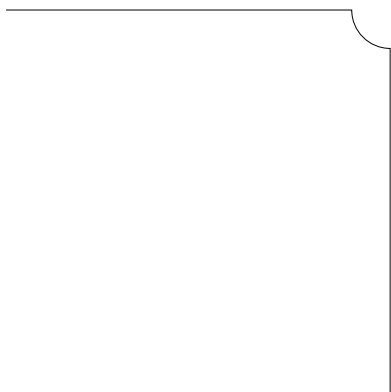
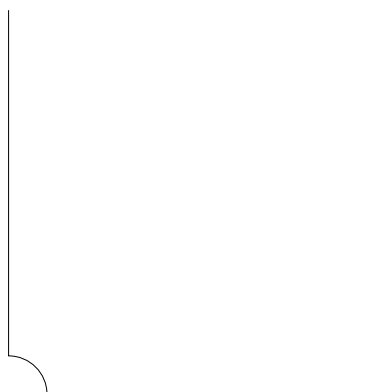
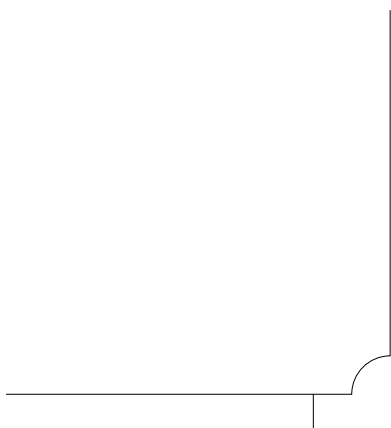
2. top
3. bottom
4. outline
5. plated-drill
6. unplated-drill
7. componentmask
8. soldermask
9. topsilk
10. bottomsilk
11. topassembly
12. bottomassembly
13. fab

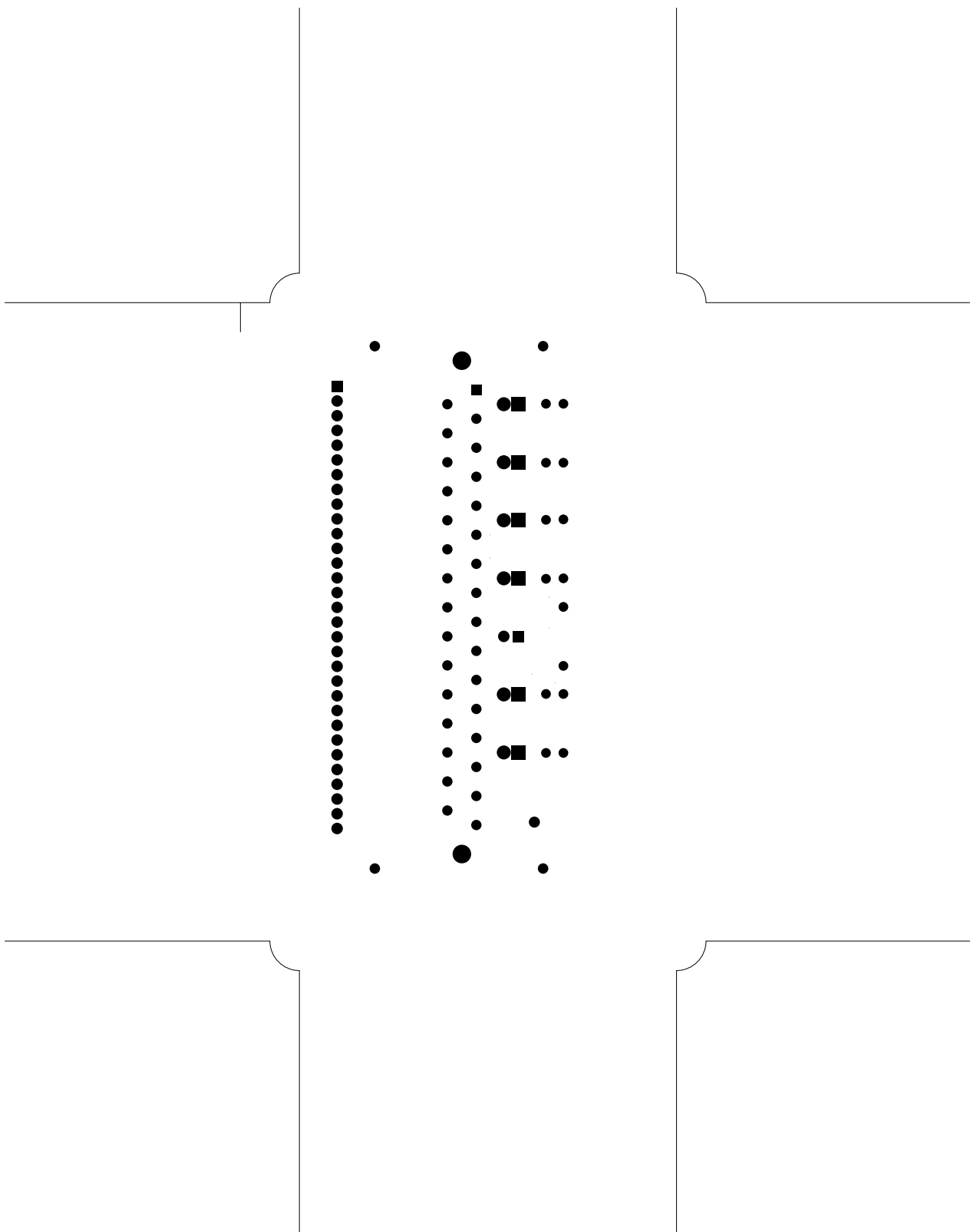


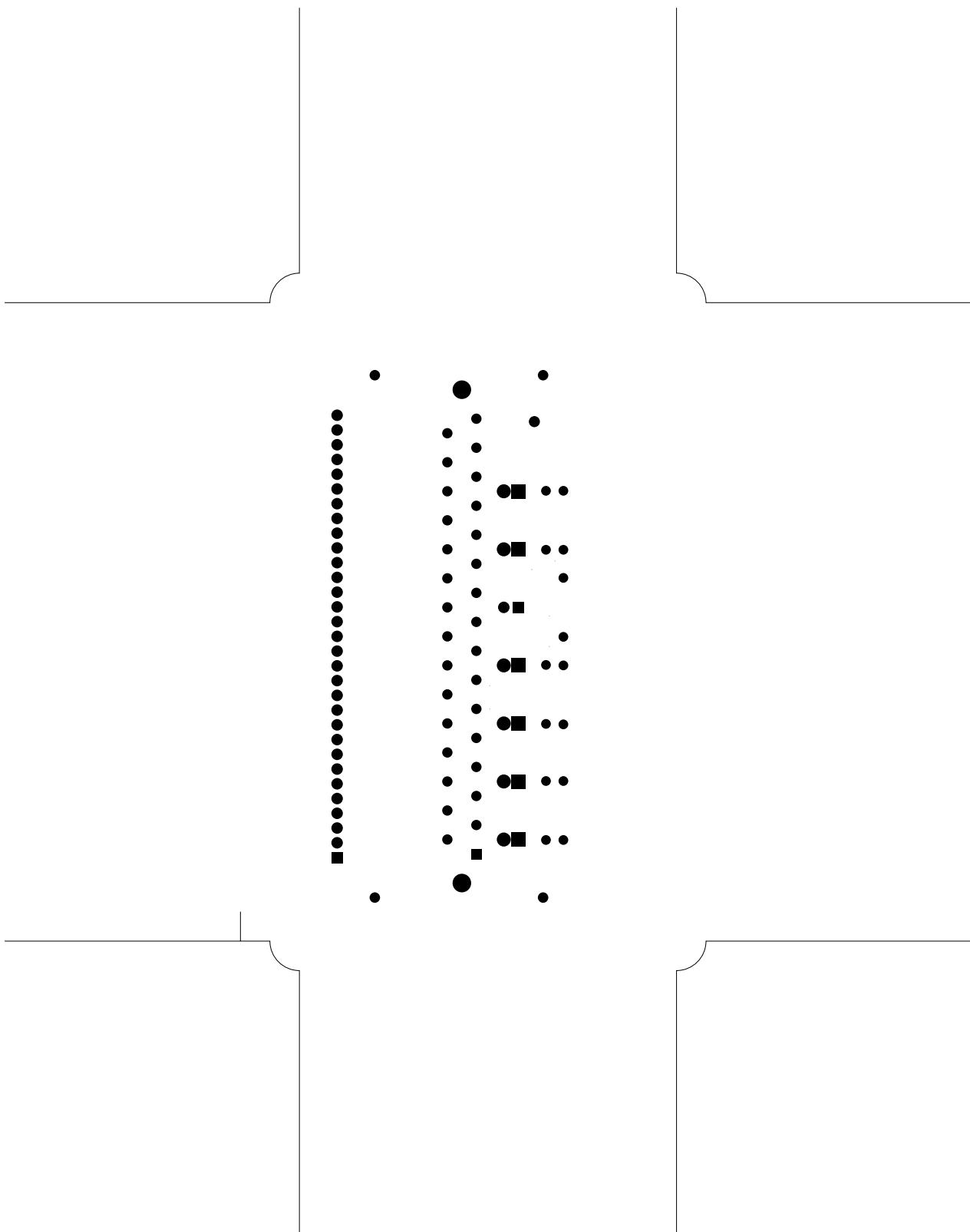


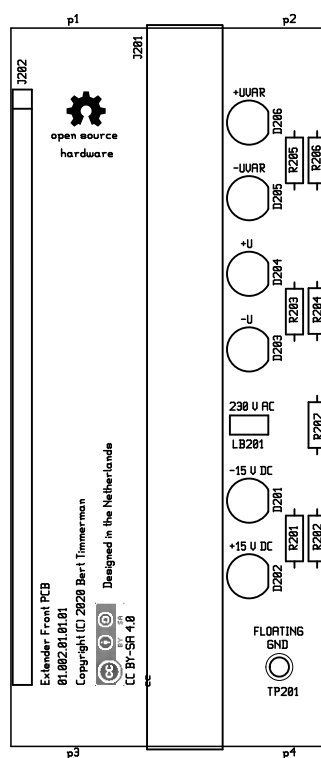


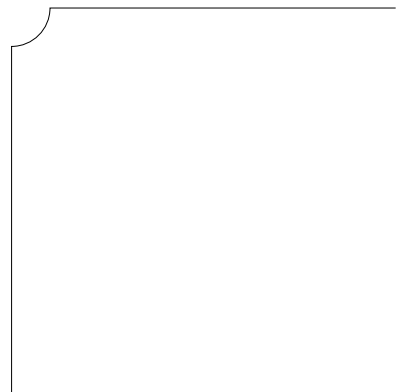
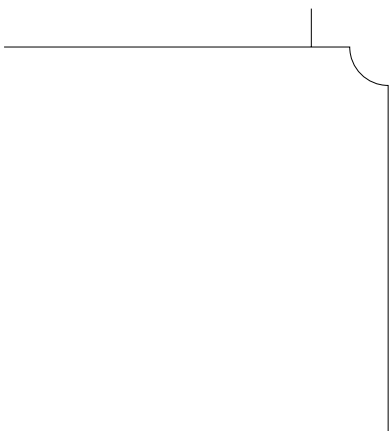
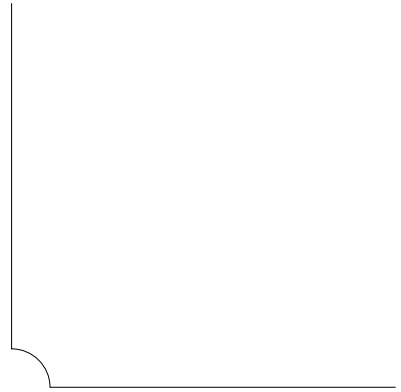
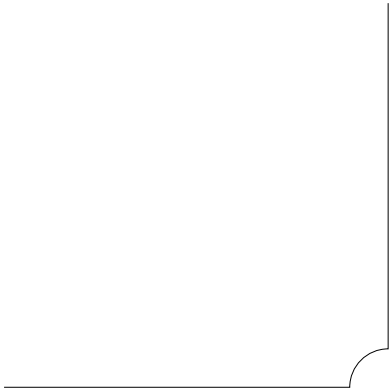


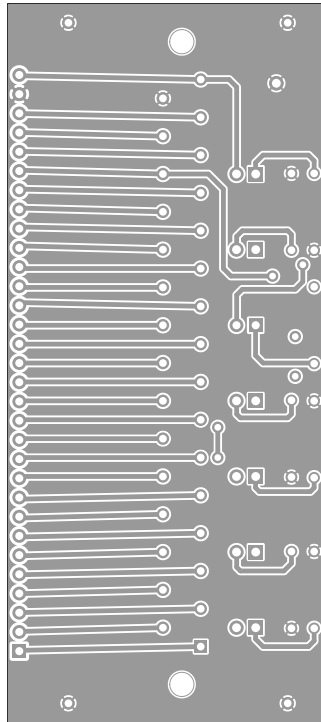








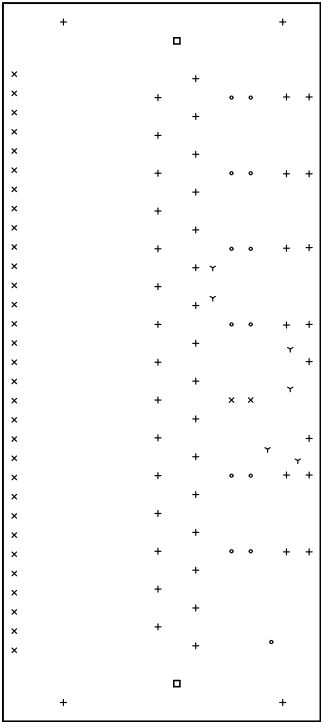




There are 5 different drill sizes used in this layout, 103 holes total

| Symbol | Diam. (Inch) | Count | Plated? |
|--------|--------------|-------|---------|
| γ | 0.035 | 6 | YES |
| + | 0.040 | 49 | YES |
| x | 0.042 | 33 | YES |
| • | 0.046 | 13 | YES |
| ◻ | 0.126 | 2 | NO |

Title: 01.002.01.01.01.pcb - Fabrication Drawing
Author: Bert Timmerman
Date: Sun Dec 13 18:32:12 2020 UTC



Board outline is the centerline of this path