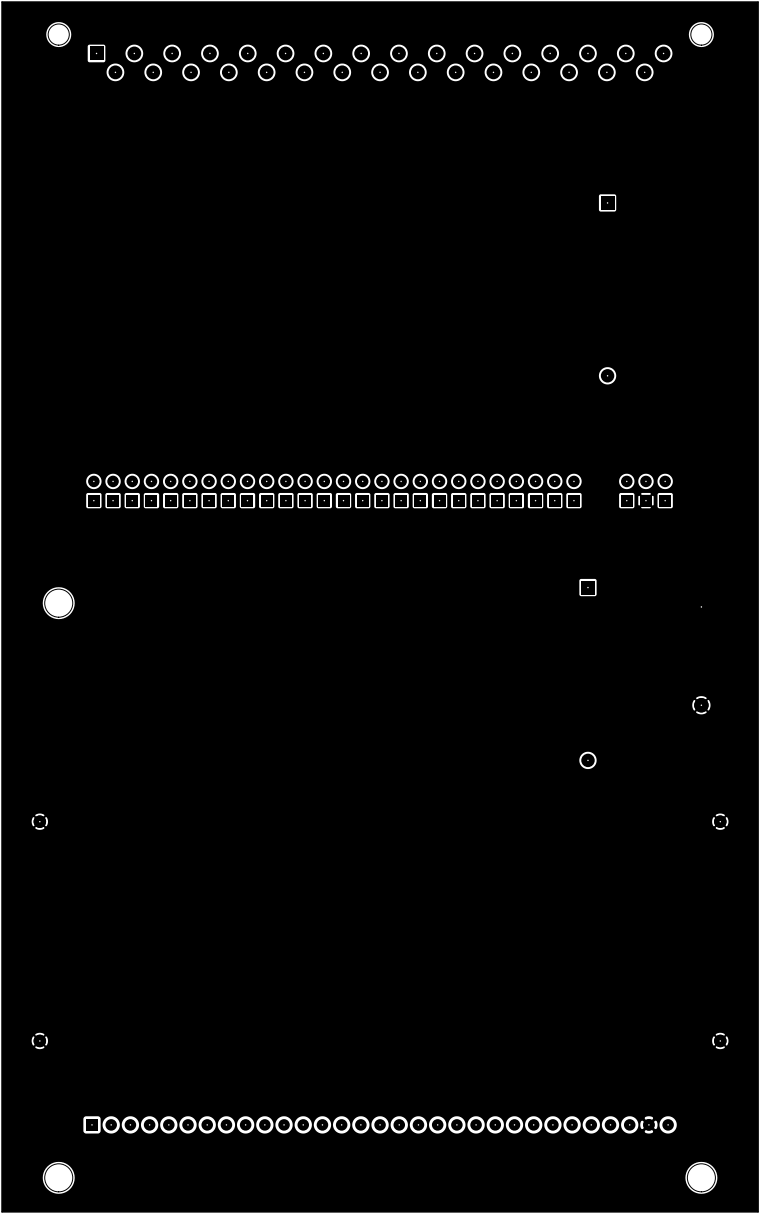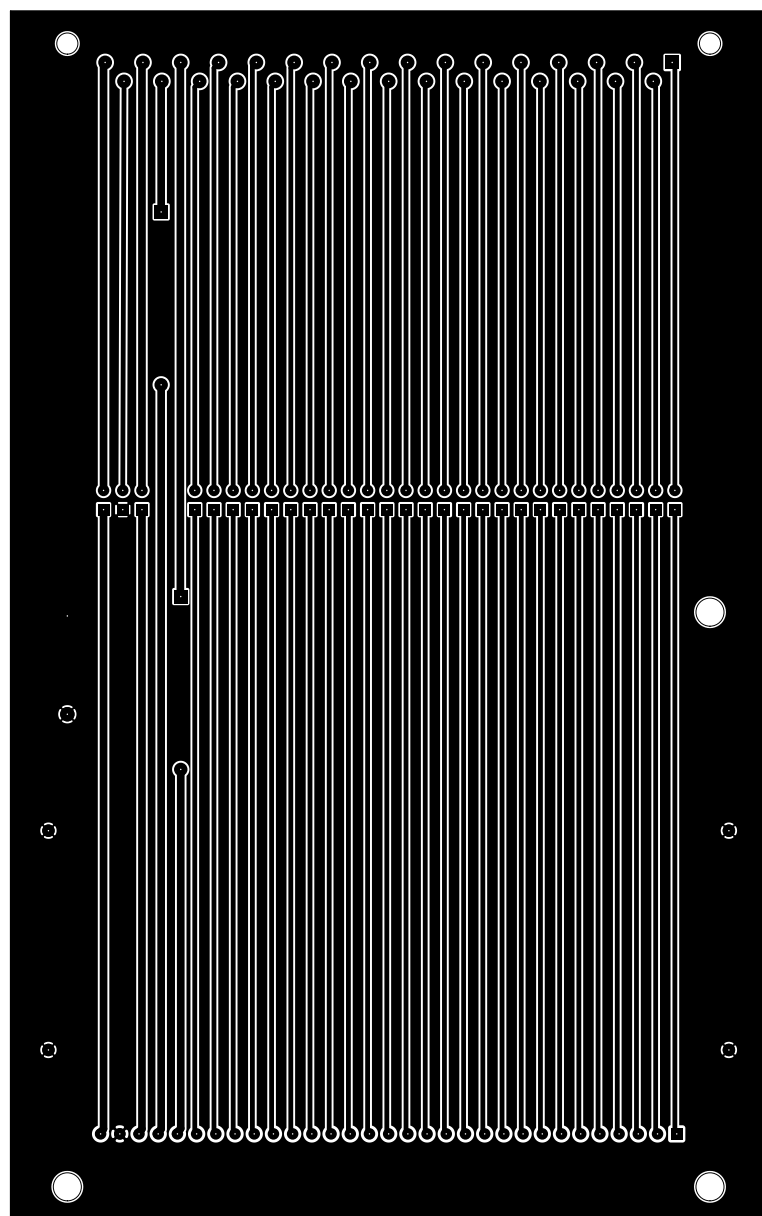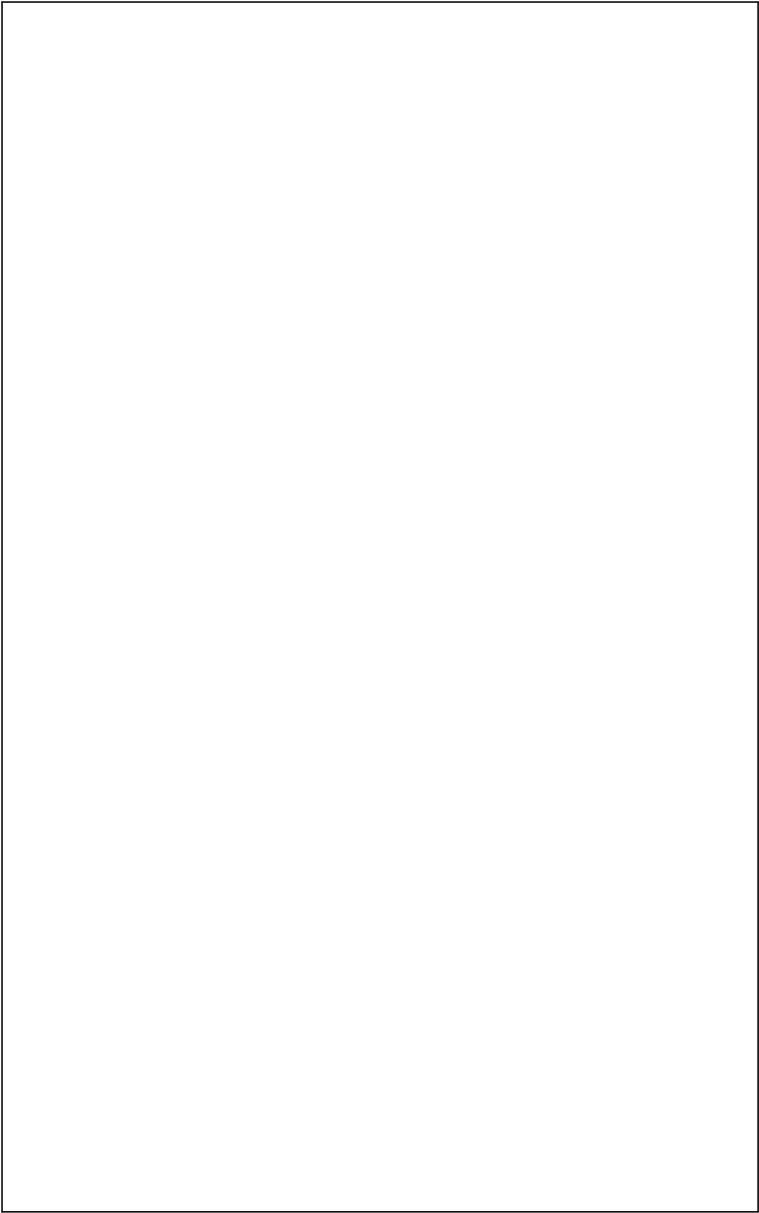1. Table of Contents (This Page)

   Created on Tue Feb 4 17:22:40 2020

2. top

3. bottom

4. outline

5. plated-drill

6. unplated-drill

7. componentmask

8. soldermask

9. topsilk

10. bottomsilk

11. topassembly

12. bottomassembly

13. fab

01.003.00.01.01.pcb, top, scale = 1:1.000
01.003.00.01.01.pcb

01.003.00.01.01.pcb, bottom (mirrored), scale = 1:1.000
01.003.00.01.01.pcb

01.003.00.01.01.pcb, outline, scale = 1:1.000
01.003.00.01.01.pcb

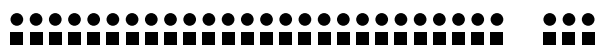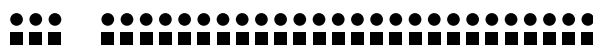01.003.00.01.01.pcb, plated-drill, scale = 1:1.000
01.003.00.01.01.pcb
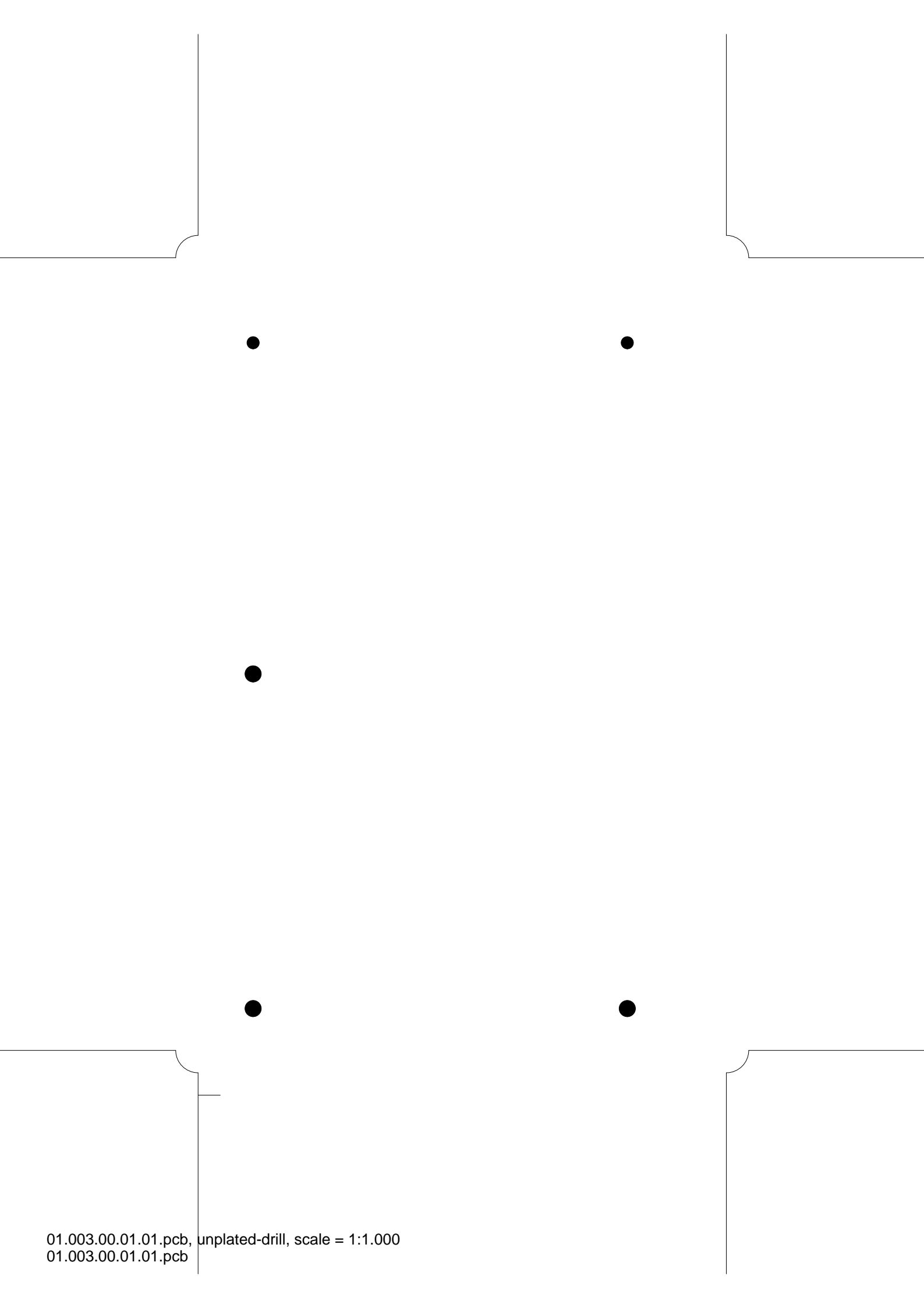
01.003.00.01.01.pcb, unplated-drill, scale = 1:1.000
01.003.00.01.01.pcb

01.003.00.01.01.pcb, topmask, scale = 1:1.000
01.003.00.01.01.pcb

P301

F328 FUSE 5x20 mm
WARNING: 230 V AC

J301
J302
J303
J304
J305
J306
J307
J308
J309
J310
J311
J312
J313
J314
J315
J316
J317
J318
J319
J320
J321
J322
J323 +UVAR
J324 −UVAR
J325 +U
J326 −U

J329 −15 VDC
J330 GND
J331 +15 VDC

A

D

Extender Main PCB
01.003.00.01.01
Copyright (C) 2020 Bert Timmerman

CC BY−SA 4.0
Designed in the Netherlands

open source
hardware

F327 FUSE 5x20 mm
WARNING: 230 V AC

FLOATING
TP301
GND

p1

p4

p2

p3

J300

B

C

CC

D1

01.003.00.01.01.pcb, bottomsilk (mirrored), scale = 1:1.000
01.003.00.01.01.pcb

P301

F328 FUSE 5x20mm

WARNING: 230 V AC

J301
J302
J303
J304
J305
J306
J307
J308
J309
J310
J311
J312
J313
J314
J315
J316
J317
J318
J319
J320
J321
J322
J323
J324
J325
J326

+UVAR
−UVAR
+U
−U

J329
J350
J331

−15 VDC
GND
+15 VDC

A

D

Extender Main PCB
01.003.00.01.01
Copyright (C) 2020 Bert Timmerman

CC BY−SA 4.0

Designed in the Netherlands

open source
hardware

F327 FUSE 5x20mm

WARNING: 230 V AC

FLOATING

TP301

GND

p1

p4

p2

p3

J300

B

C

01.003.00.01.01.pcb, bottomassembly (mirrored), scale = 1:1.000
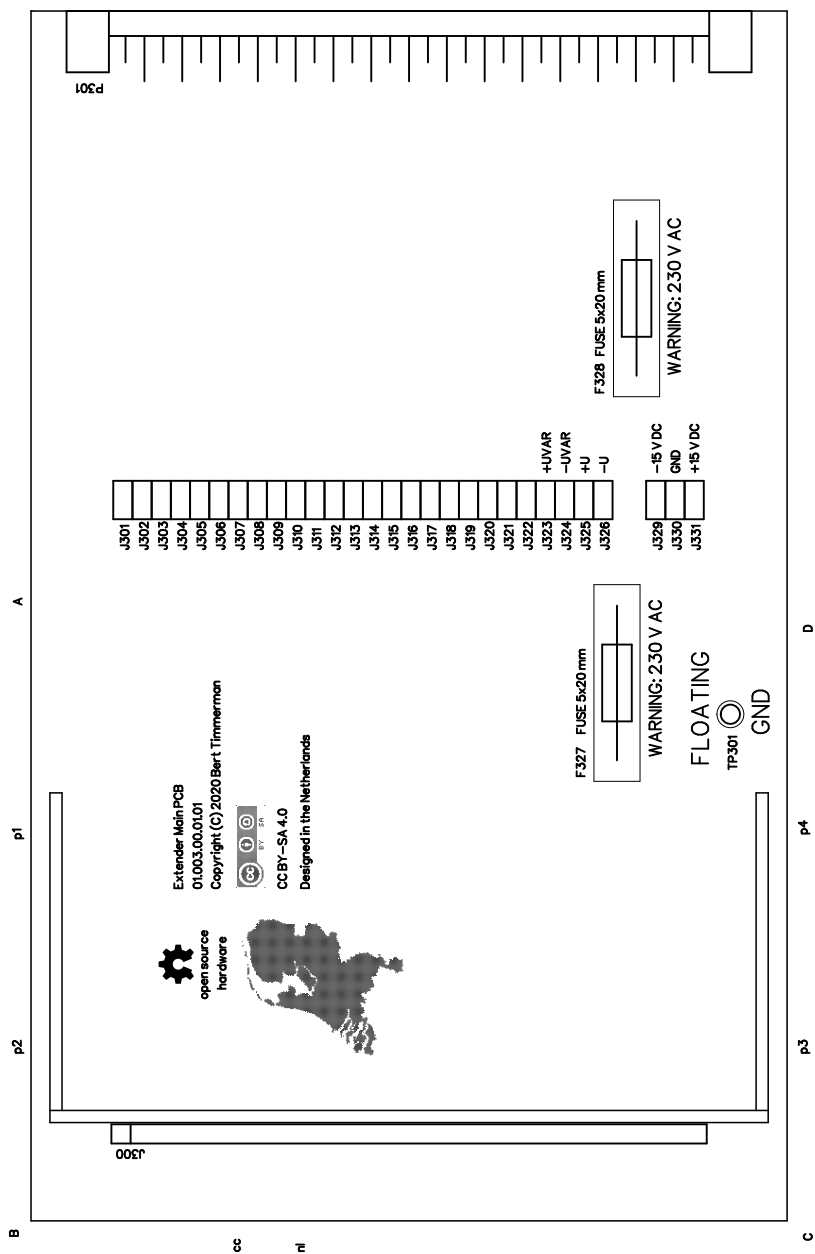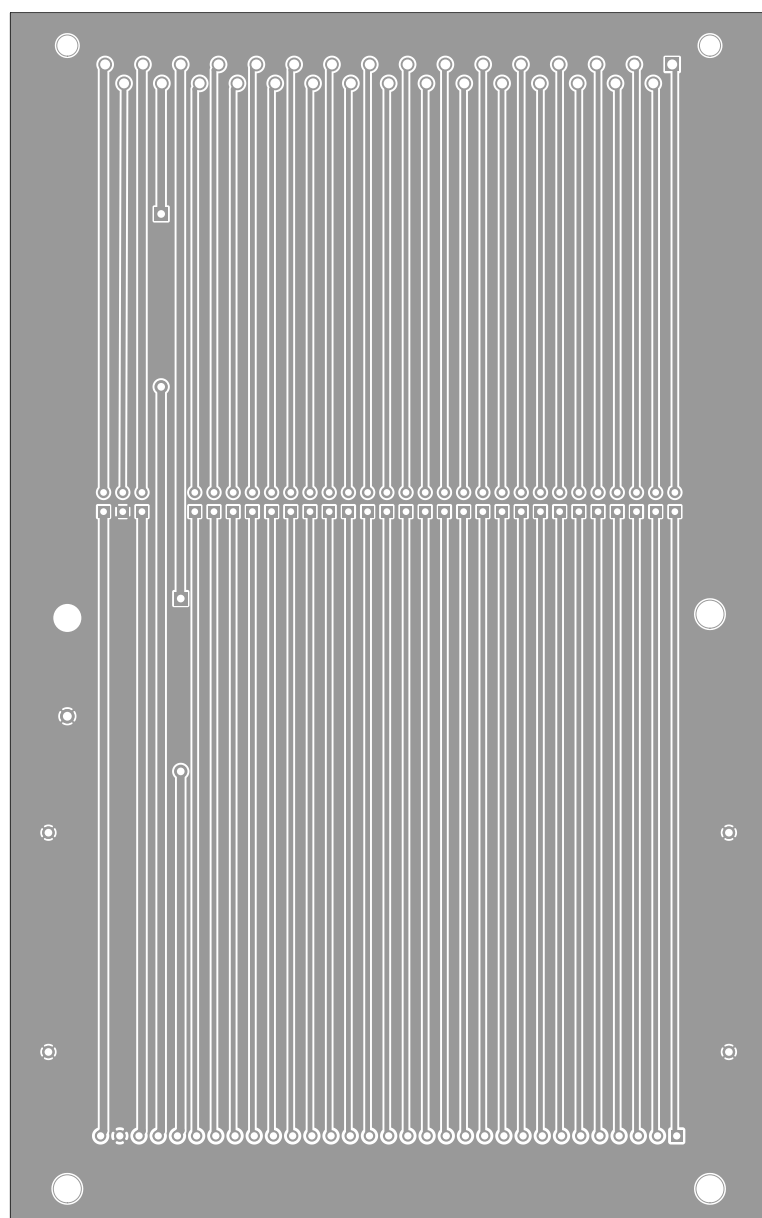01.003.00.01.01.pcb

There are 7 different drill sizes used in this layout, 135 holes total

Symbol	Diam. (Inch)	Count	Plated?
⊤	0.035	58	YES
+	0.038	31	YES
×	0.040	8	YES
∘	0.046	1	YES
□	0.052	31	YES
⚲	0.110	2	NO
⊕	0.146	3	NO
⊠	0.146	1	YES

Title: 01.003.00.01.01.pcb – Fabrication Drawing

Author: Bert Timmerman

Date: Tue Feb 4 16:22:40 2020 UTC
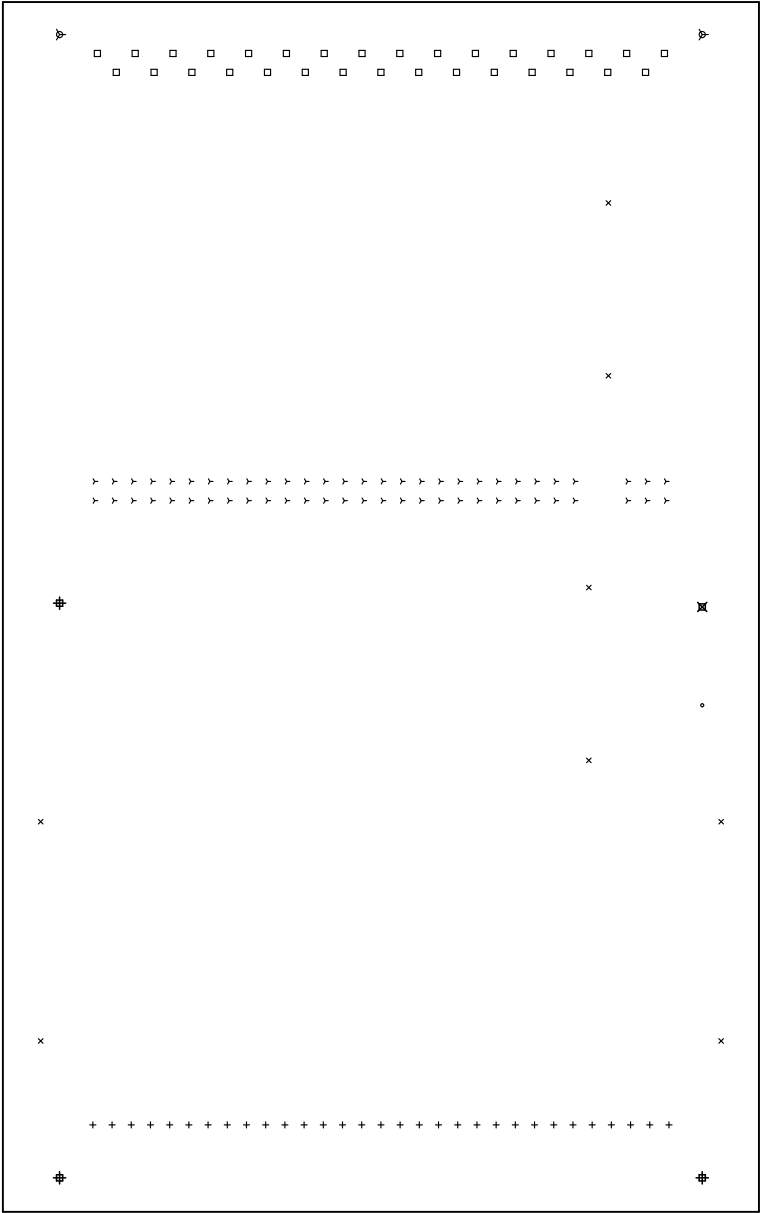
Board outline is the centerline of this path