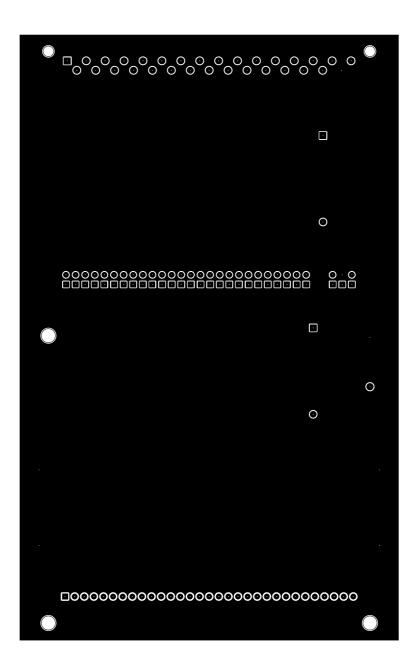
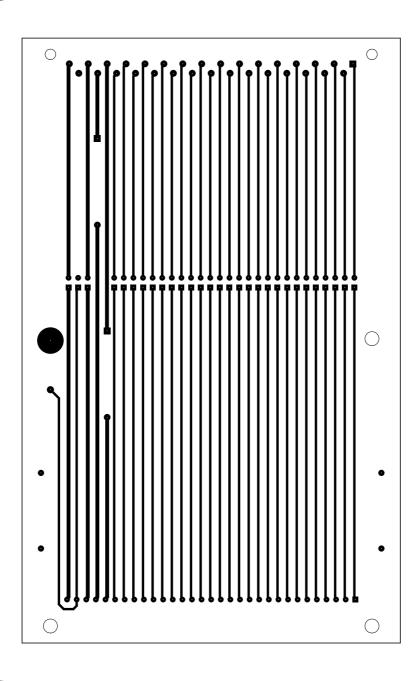
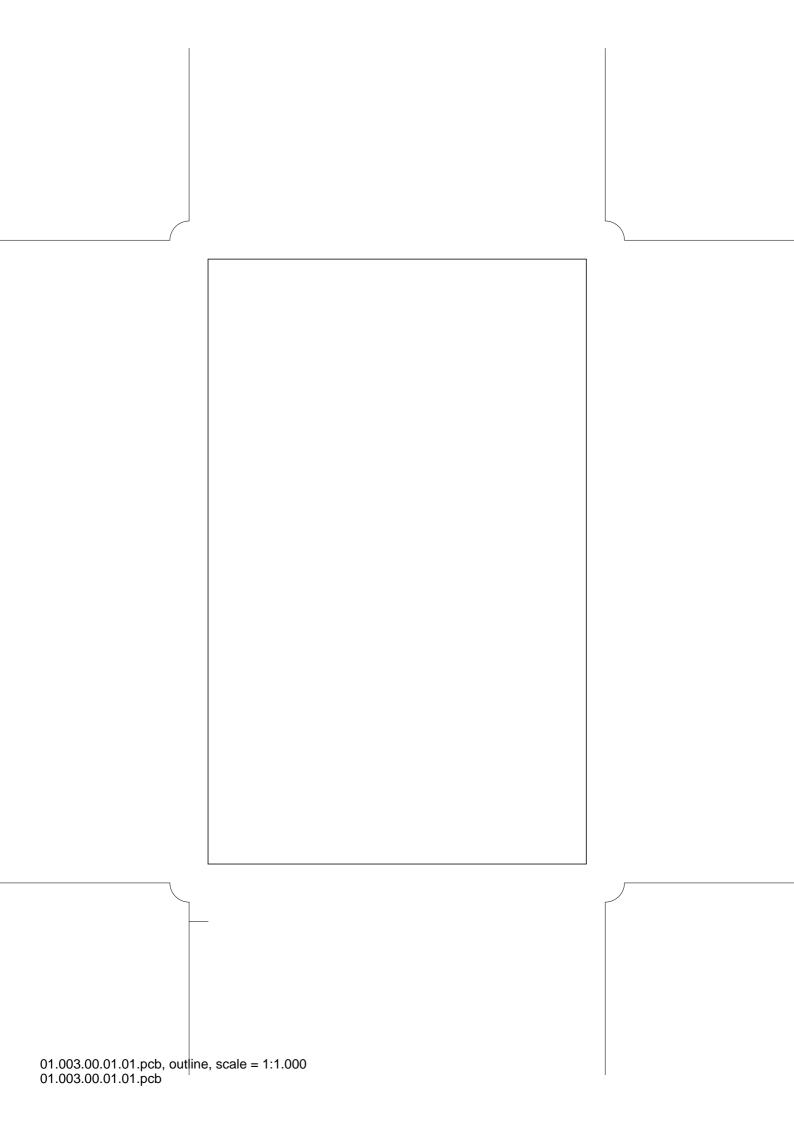
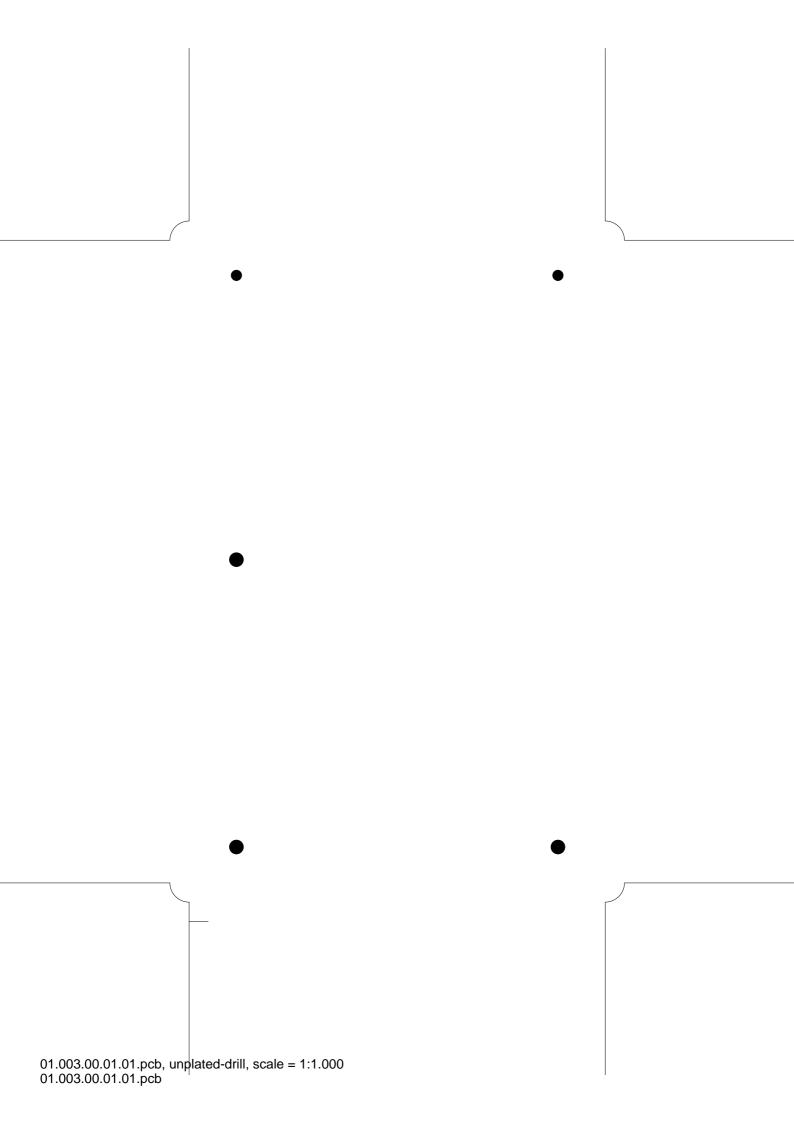
- Table of Contents (This Page)
 Created on Sat Sep 1 12:36:02 2018
- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. unplated-drill
- 7. componentmask
- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. topassembly
- 12. bottomassembly
- 13. fab

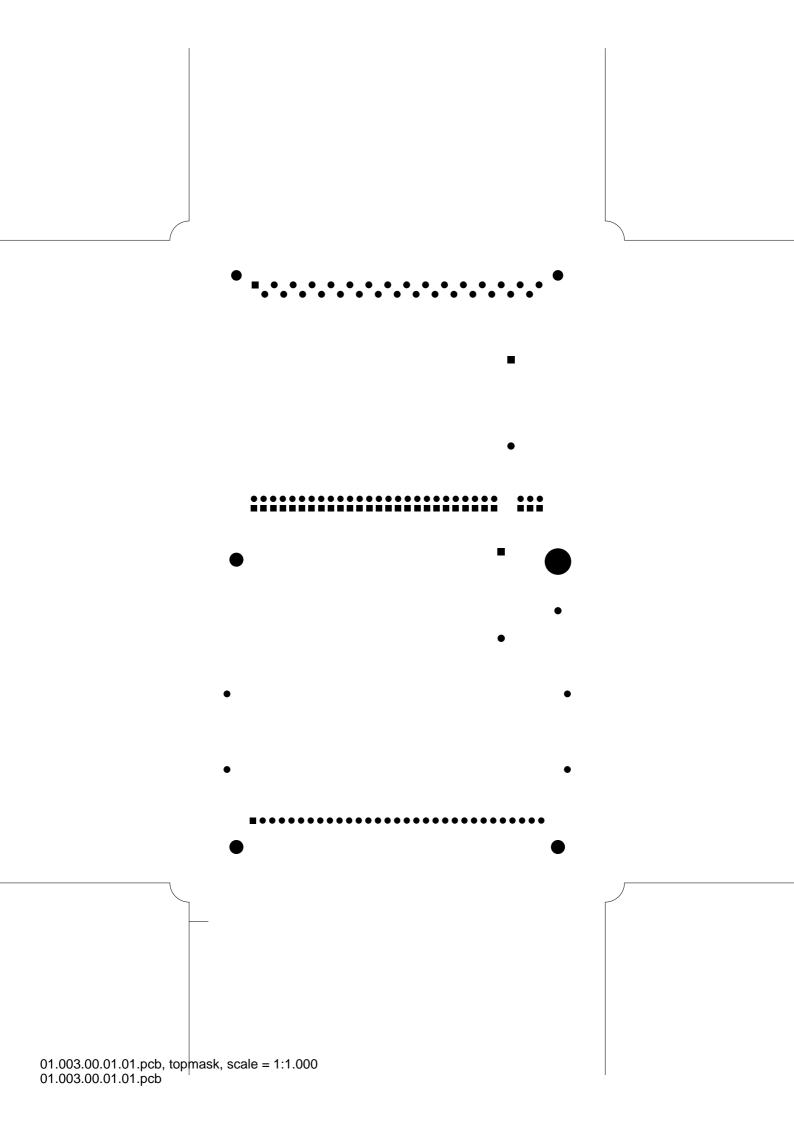


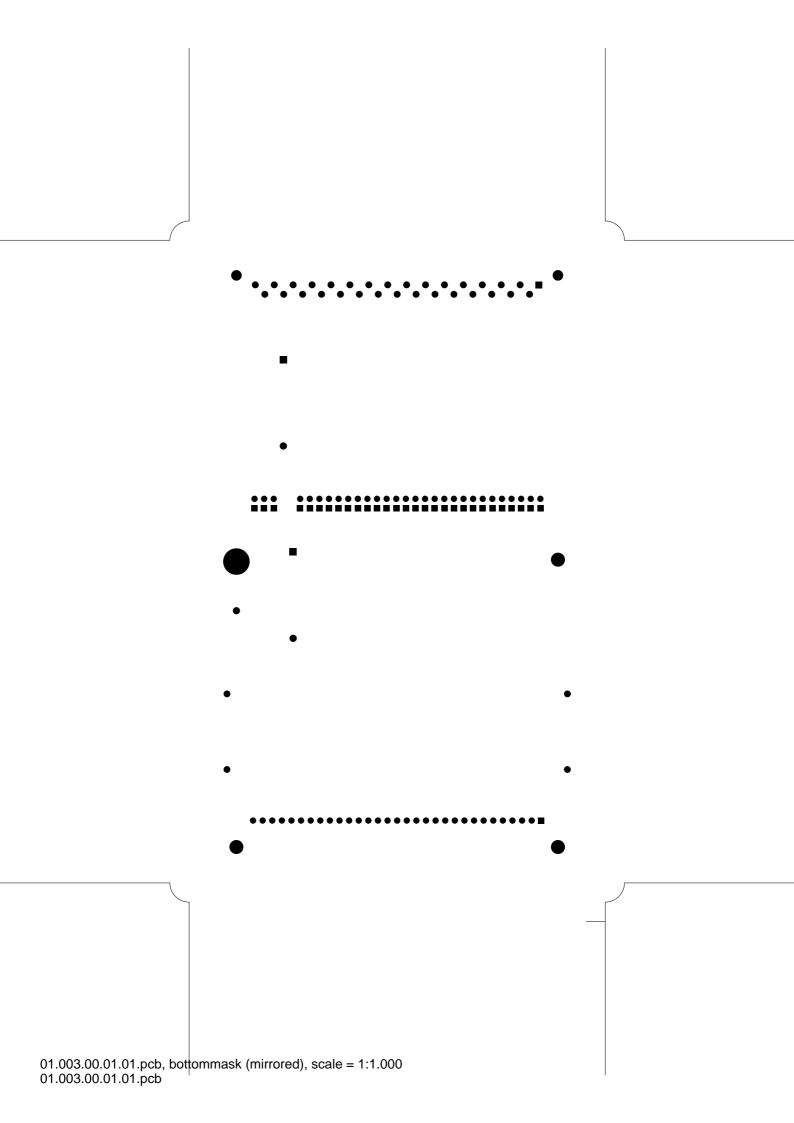


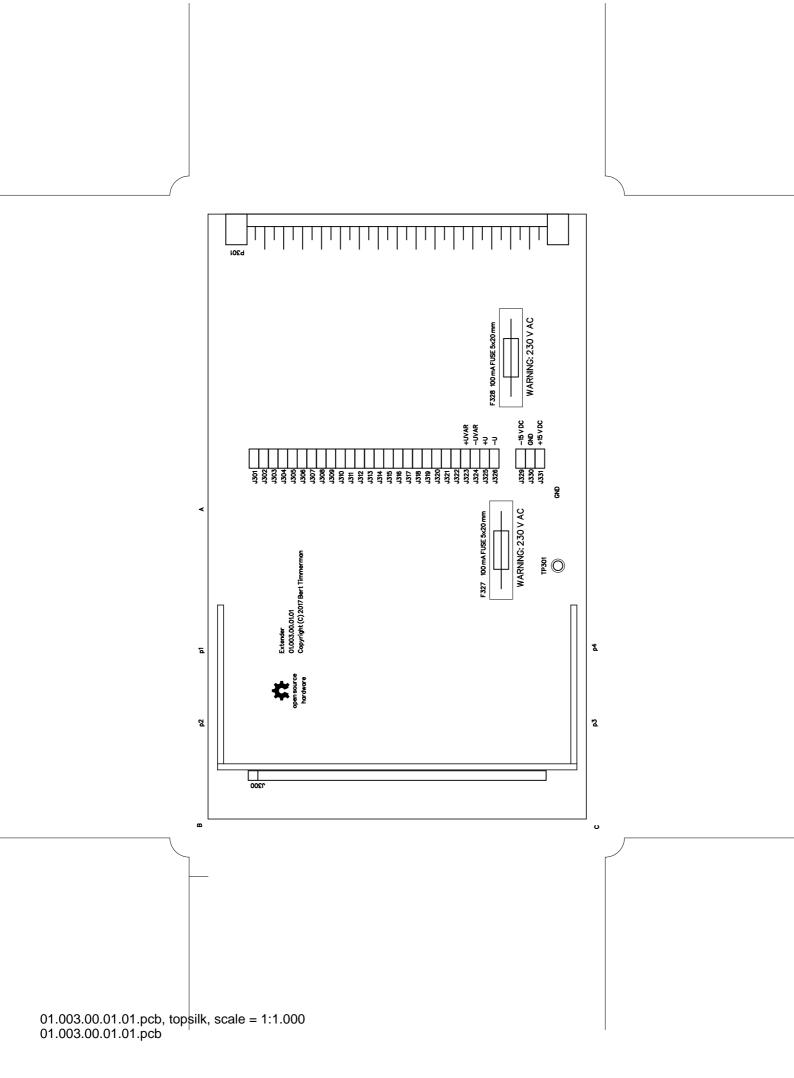


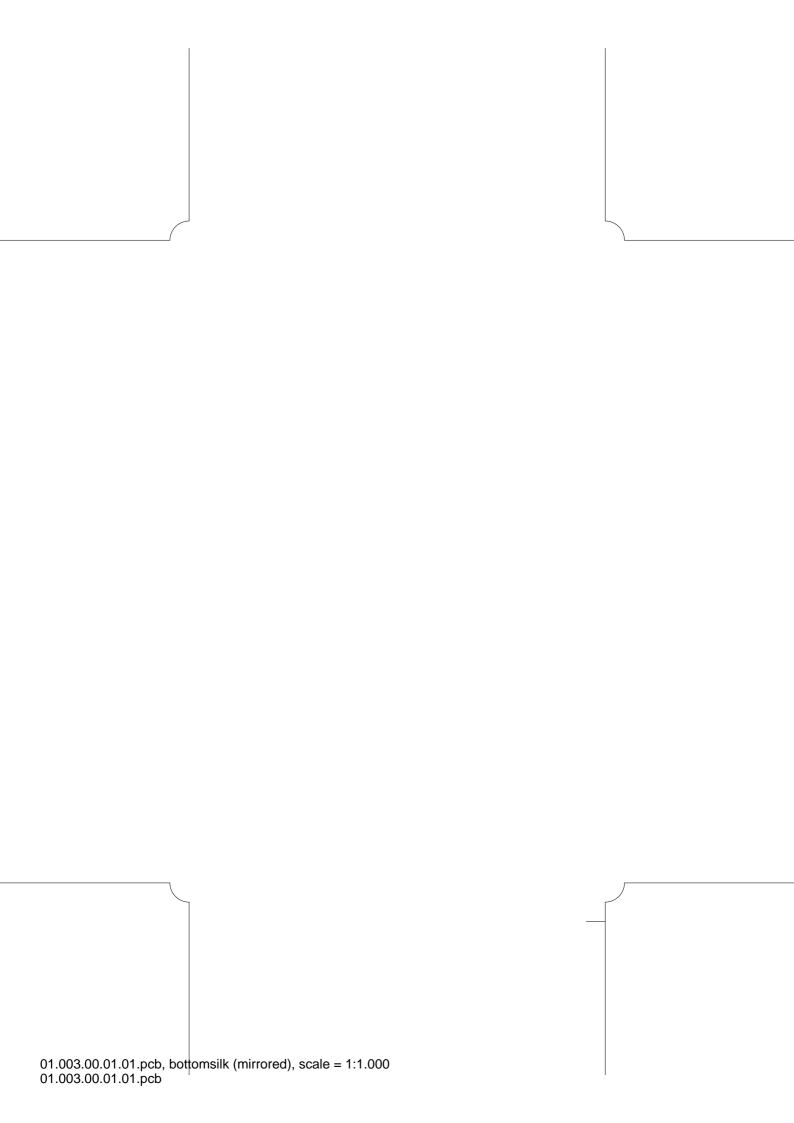
	•	
	•	
	•	
	•	
	•	
•	•	
•	•	
	• • • • • • • • • • • • • • • • • • • •	
01.003.00.01.01.pcb, plated-drill, s 01.003.00.01.01.pcb	cale = 1:1.000	

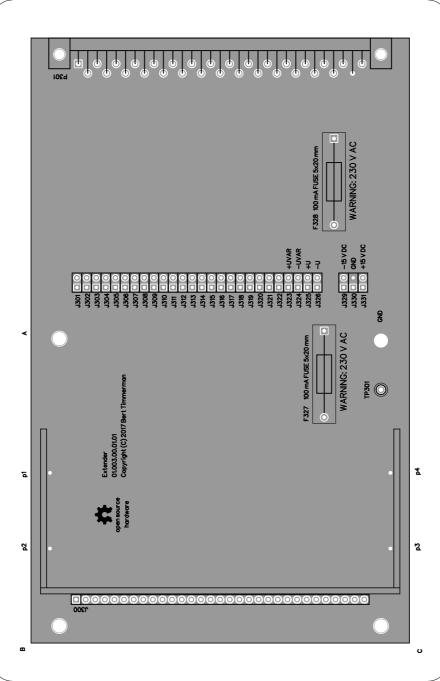




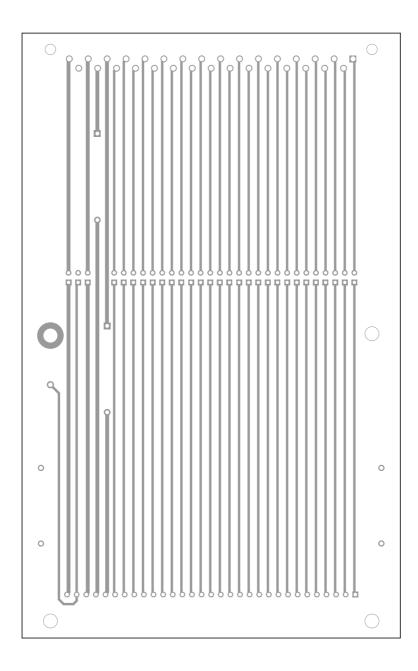








01.003.00.01.01.pcb, top assembly, scale = 1:1.000 01.003.00.01.01.pcb



≽																																≽
																												×				
																												×				
								>																						>		
+																											×					×
																											×					٠
×																																×
×																																×
ф	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+	+

Board outline is the centerline of this path

01.003.00.01.01.pcb, fab, scale = 1:1.000 01.003.00.01.01.pcb

Title: 01.003.00.01.01.pcb — Fabrication Drawing

here are 7 different drill sizes used in this layout, 135 holes total

Symbol Diam. (Inch) Count Plated?

0.035 0.038 0.040 0.046

Author: Bert Timmerman Date: Sat Sep 110:36:02 2018 UTC

YES NO NO YES

0.110 0.146 0.146